STEREO AUDIO SYSTEM

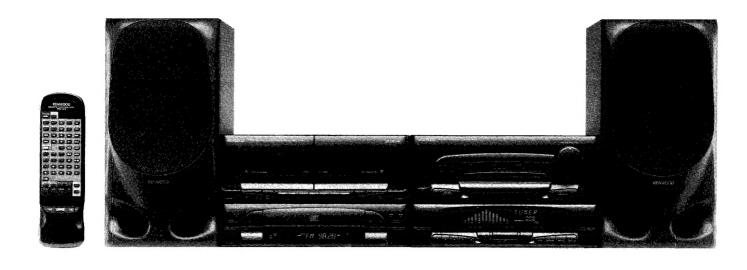
UD-300

SERVICE MANUAL

(A-A3/X-A3/LS-A3)

KENWOOD

© 1992-2 PRINTED IN JAPAN B51-4496-00(S)4074



Precautions when performing repairs.

- (1) If you want to power on X-A3 without A-A3 need power supply jig (RM-90PS). Power-on procedure is written on page 25 (USE TEST MODE).
- (2) Do not look directly at the laser beam while repairing the CD Player.

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ACCESSORIES

• AM loop antenna ... 1



(T90-0174-05)

Remote control unit ... 1



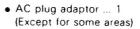
(X94-1010-11) BATTERY COVER (A09-0126-03)

• FM indoor antenna ... 1



(T90-0175-05)

Batteries (R06/AA) ... 2





(E03-0115-05)M

Antenna adaptor... 1
 (75 Ω/300 Ω)
 (Except for some areas)



(T90-0185-0;) T,E

Antenna 10lder

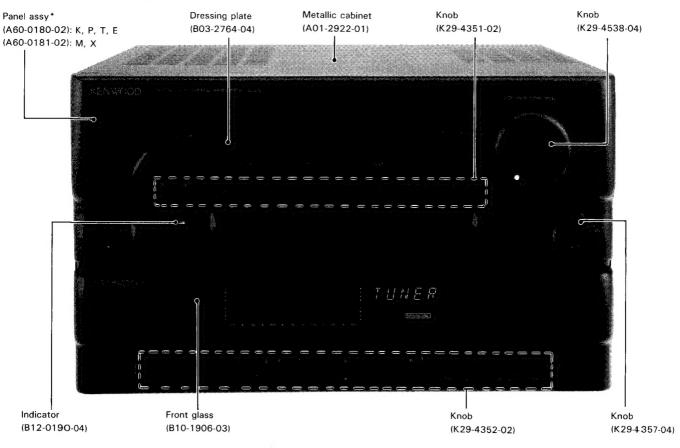
INSTRUCTION MANUAL

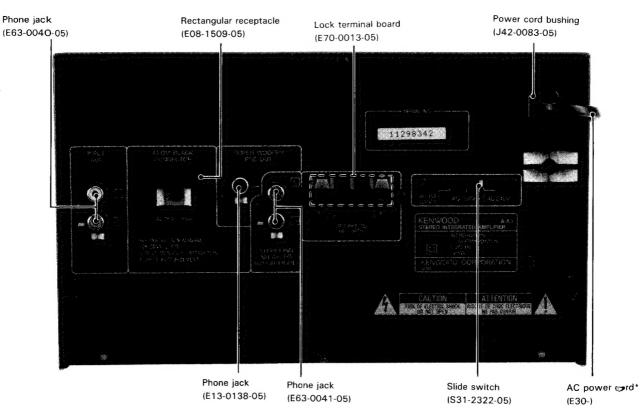
B60-0751-00 (ENGLISH) B60-0755-00 (ITA) E B60-0752-00 (FRE) P,E B60-0756-00 (CHI) M B60-0753-00 (GER) E B60-0757-00 (SPA) M,E B60-0754-00 (JAP) E B60-0758-00 (ARA) M



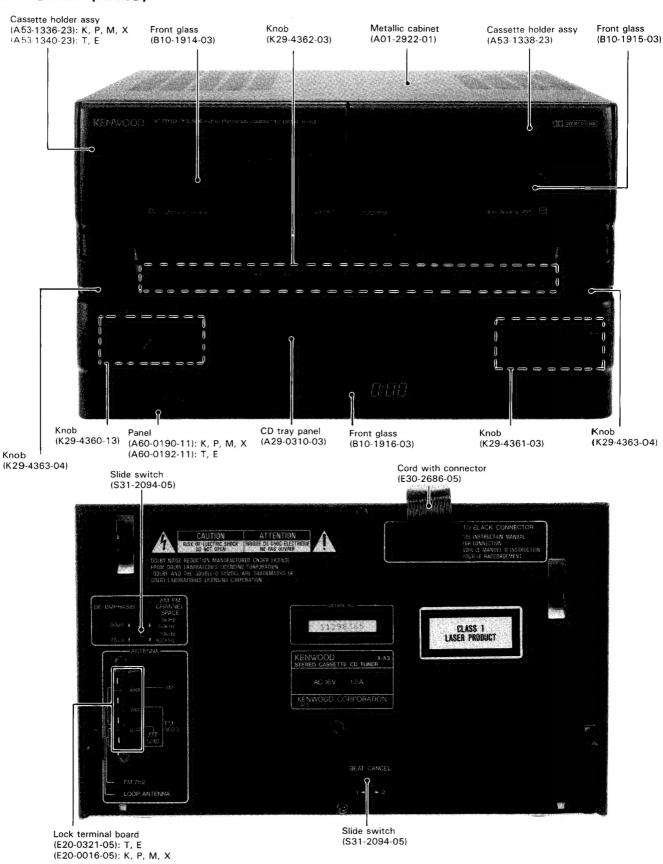
(J19-2815-04)

AMPLIFIER (A-A3)



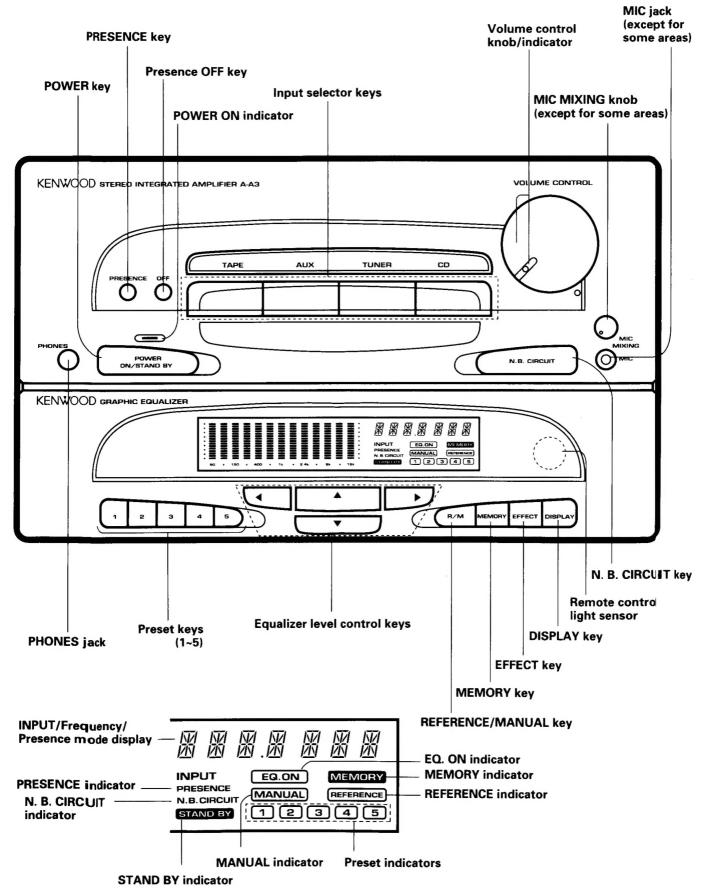


CD TUNER (X-A3)



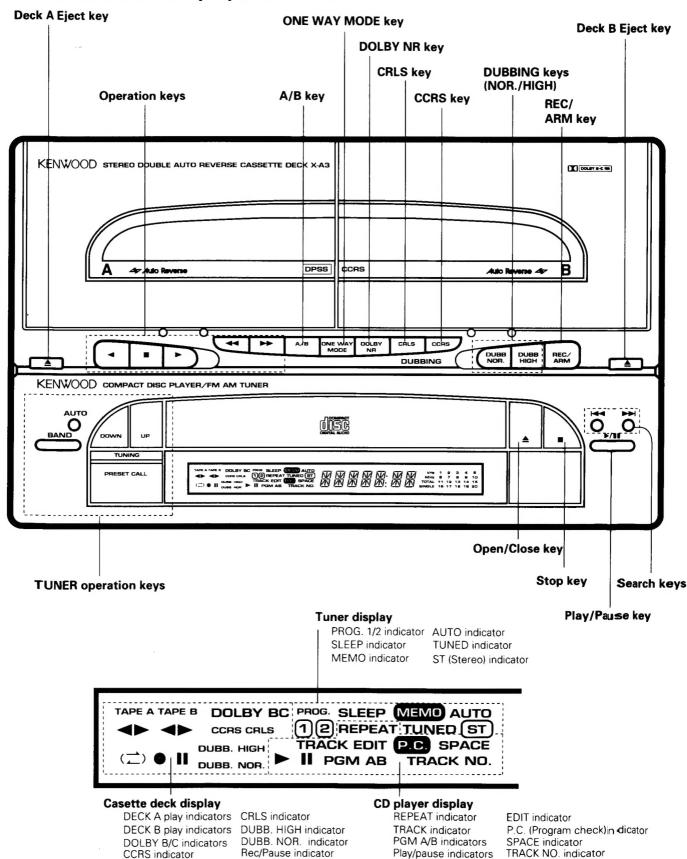
CONTROLS & INDICATORS

■ Amplifier/Graphic equalizer unit



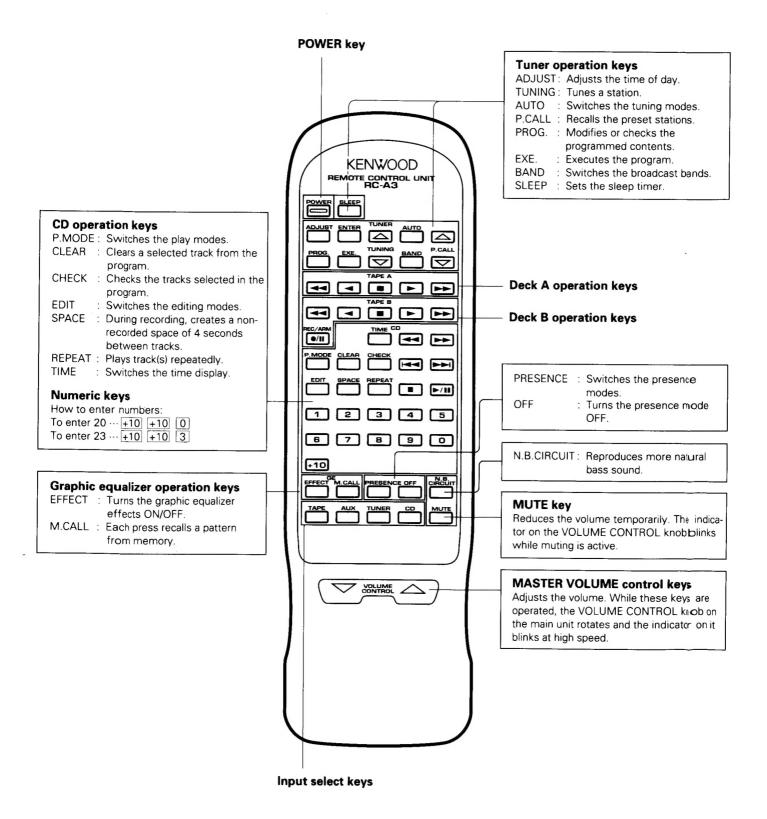
CONTROLS & INDICATORS

■ Cassette deck/CD player/tuner unit



Direction indicator

REMOTE CONTROLLER

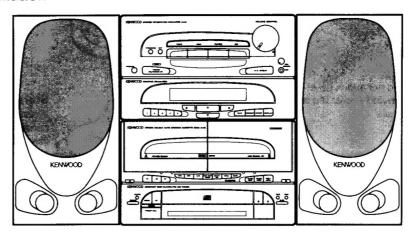


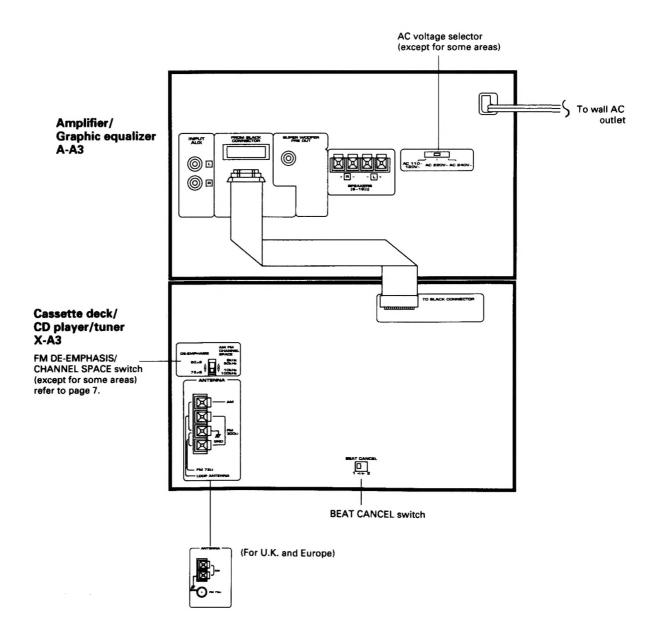
SYSTEM CONNECTIONS

Do not plug in the power cord until all connections are completed.

- When stacking components, follow the indicated order in the connections diagram.
- When connecting the related system components, refer also to the instruction manuals of the related components.

Vertical installation

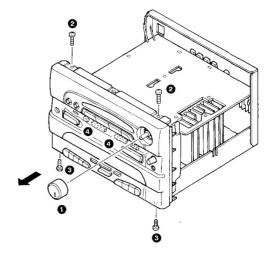




DISASSEMBLY FOR REPAIR

(Remove the metallic cabinet from the body beforehand) (A-A3)

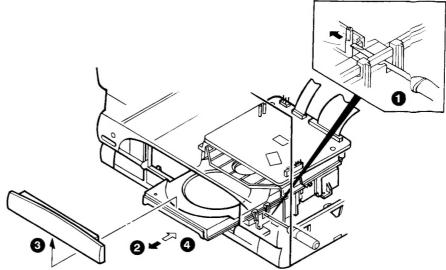
- 1 Removing the front panel ass'y.
- 1) Remove volume knob 1.
- 2) Remove 2 screws 2.
- 3) Remove 2 screws 3.
- 4) Pull out front panel ass'y frontwards with care of connectors 4.



- Removing the chassis of bottom board.
- 5) Remove 3 screws 5.
- 6) Remove 4 screws 6 of the transformer.
- 7) Remove 1 screw 7 .
- 8) Remove 2 screws 8.
- 9) Remove the chassis of bottom board 9.

(X-A3)

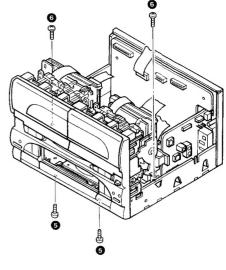
- 1) Removing the front panel ass'y.
- 1) Insert the driver to push the slider, into the rightside hole of mechanism ass'y (1).
- 2) Pull out the tray (2).
- 3) Remove the tray panel (3).
- 4) Push the tray backwards (4).

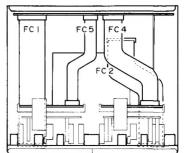


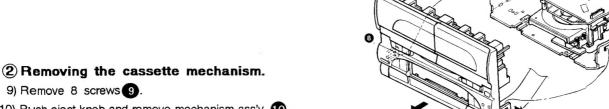
DISASSEMBLY FOR REPAIR

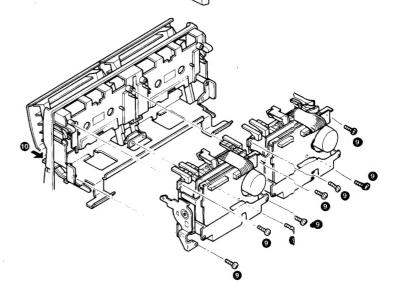
- 5) Remove 2 screws 5.
- 6) Remove 2 screws 6.
- 7) Remove flat cables (FC1~4) from connector.

8) Remove both side of catchers and panel 8.





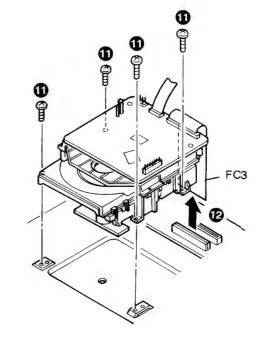




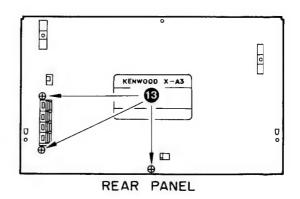
- 10) Push eject knob and remove mechanism ass'y 10 .

DISASSEMBLY FOR REPAIR

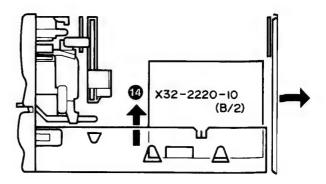
- 3 Removing the CD mechanism.
- 11) Remove 4 screws 11.
- 12) Remove FC3, 12.



- 4 Removing the tuner PCB.
- 13) Remove 2 screws 13.



14) Remove the tuner PCB from connector. 14.



DISASSEMBLY FOR REPAIR

1) How to Remove Tray

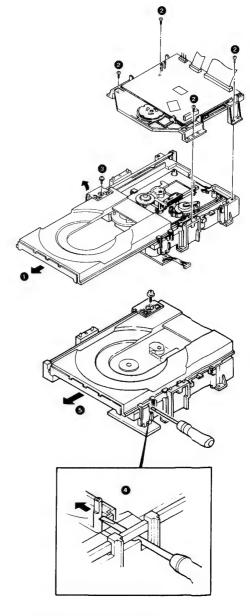
- 1. Turn the power switch to OFF when the tray is open condition (1).
- 2. Remove the screws (2) and clamper ass'y.
- 3. Remove the screw (3) and guide. And then remove the tray.

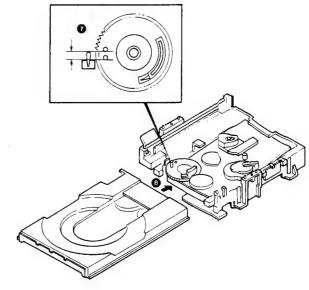
2) How to Remove the Tray When Power Switch is OFF or Tray Not Come Out

- 1. Insert the driver to the right-side hole of mechanism ass'y (4).
- 2. Tray can be pulled out (5).

3) How to Mount the Tray

- 1. Meet the mark on the gear with that of mechanism chassis ().
- 2. Insert the tray to both-side guide on chassis (3).
- 3. Mount the guide on the chassis with screw (3).



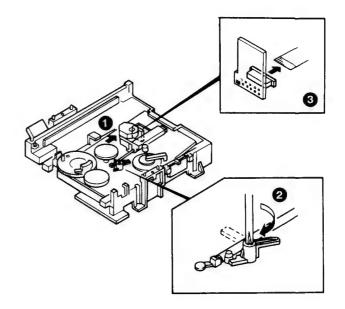


DISASSEMBLY FOR REPAIR

4) How to Replace the Pickup

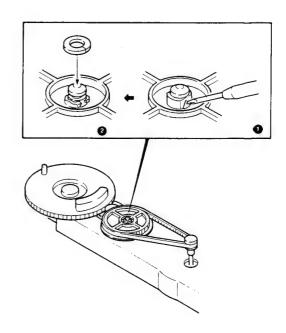
- 1. Remove the clamper ass'y and pull out the tray.
- 2. Move the pickup to center position of its all travel (1), and turn the stopper to clockwise (2).
- 3. Remove the flexible and pickup (3).

Note: When mounting the pickup, in the reverse order of disassembly.

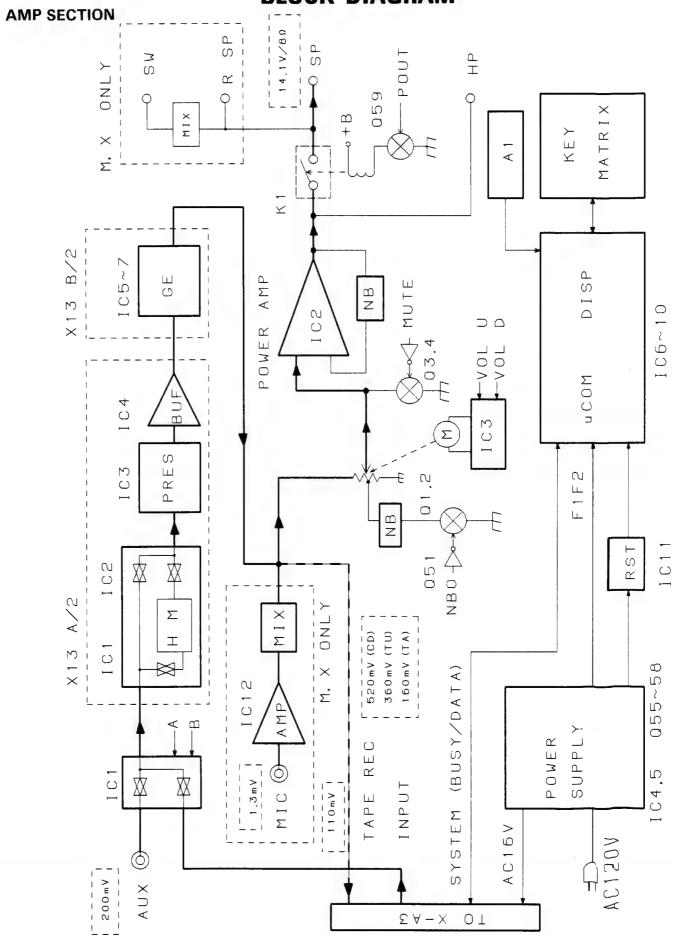


5) How to Replace the Loading Gear

- 1. Spread the hole of gear shaft (1).
- 2. When gear is broken, use the cut washer (2) (parts no. N19-1179-05)

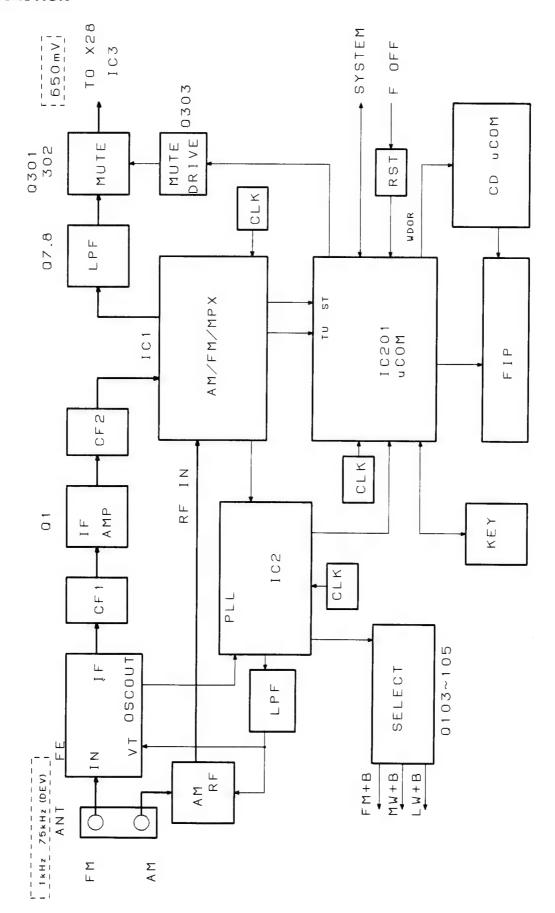


BLOCK DIAGRAM

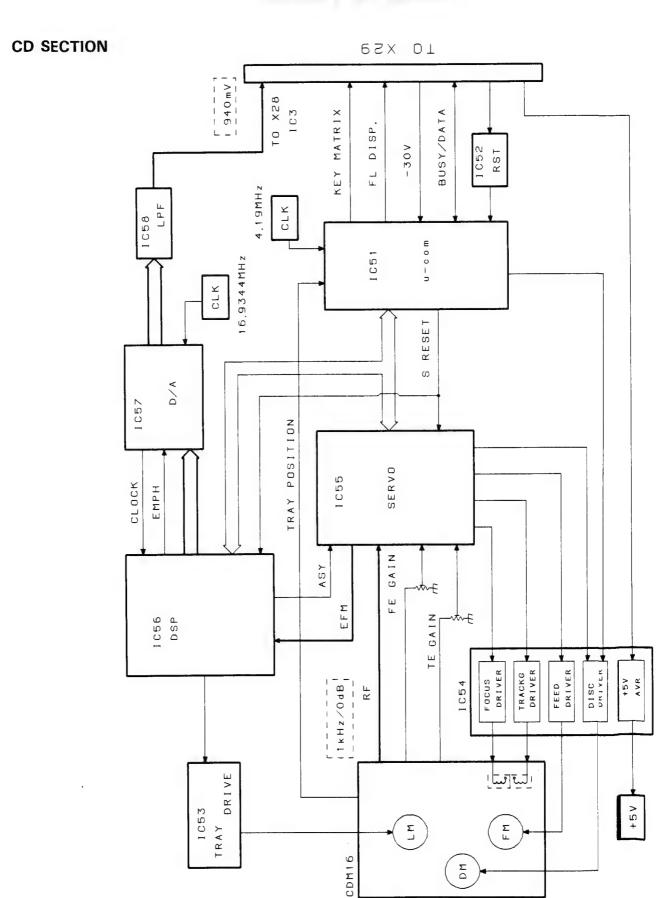


BLOCK DIAGRAM

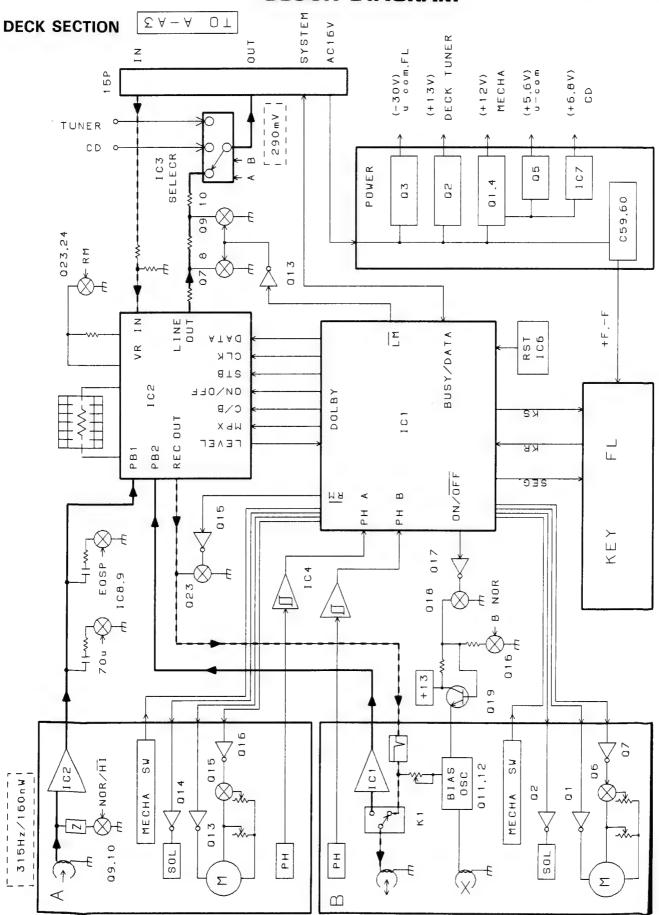
TUNER SECTION



BLOCK DIAGRAM

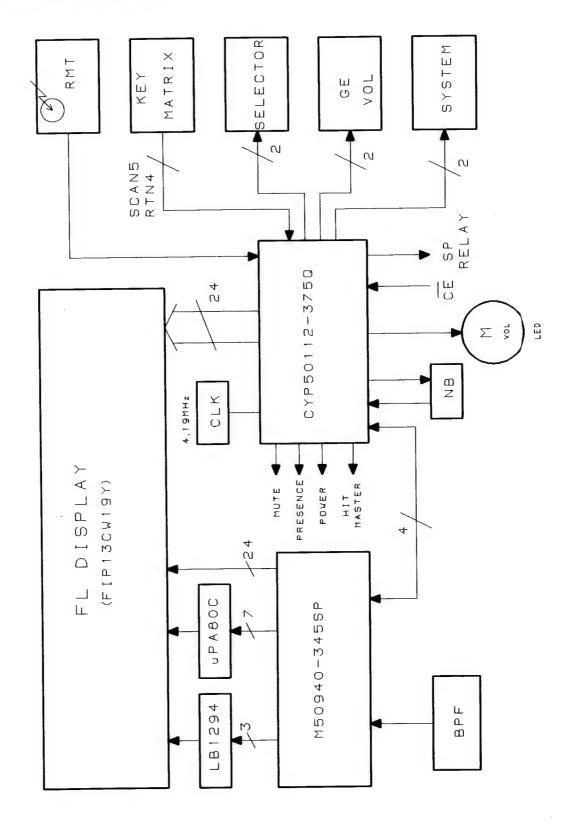


BLOCK DIAGRAM



CIRCUIT DIAGRAM

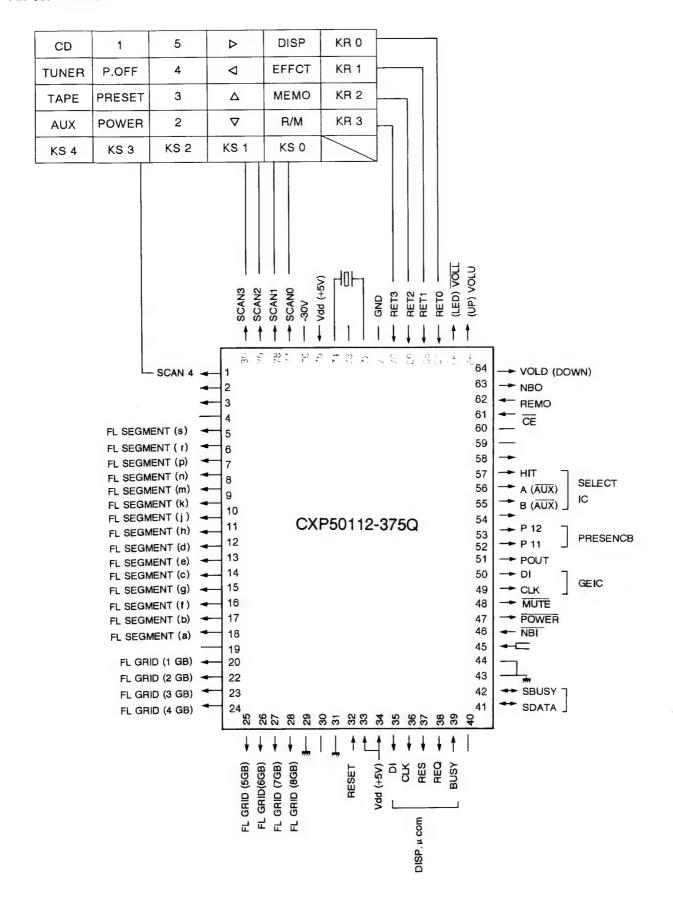
Terminal connection diagram (A-A3)



CIRCUIT DIAGRAM

Microprocessor: (CXP50112-375Q or CXP50112-388Q)

Pin connection



CIRCUIT DESCRIPTION

PIN DESCRIPTIONS (CXP50112-375Q or CXP50112-388Q)

PIN No	PIN NAME	1/0	SYMBOL/FUNCTION		
1	PG0	0	SCAN4		
2	PG1		NC		
3	PG2		NC		
4	PG3	_	NC		
5	PKO	0	FL SEGMENT (s)		
6	PK1	0	FL SEGMENT (r)		
7	PK2	0	FL SEGMENT (p)		
8	PK3	0	FL SEGMENT (n)		
9	PJ0	0	FL SEGMENT (m)		
10	PJ1	0	FL SEGMENT (k)		
11	PJ2	0	FL SEGMENT (j)		
12	PJ3	0	FL SEGMENT (h)		
13	T15	0	FL SEGMENT (d)		
14	T14	0	FL SEGMENT (e)		
15	T13	0	FL SEGMENT (c)		
16	T12	0	FL SEGMENT (g)		
17	T11	0	FL SEGMENT (f)		
18	T10	0	FL SEGMENT (b)		
19	Т9	0	FL SEGMENT (a)		
20	Т8	_	NC		
21	T7	0	FL GRID (1GB)		
22	Т6	0	FL GRID (2GB)		
23	T5	0	FL GRID (3GB)		
24	T4	0	FL GRID (4GB)		
25	Т3	0	FL GRID (5GB)		
26	T2	0	FL GRID (6GB)		
27	T1	0	FL GRID (7GB)		
28	TO	0	FL GRID (8GB)		
29	INT	_	GND		
30	TX	_	NC		
31	TEX		GND		
32	RES	ı	RESET		
33	NC	_	NC		
34	Vdd	_	Vdd (+5V)		
35	PIO	0	DI (for DISP μ-com)		
36	PI1	0	CLK (for DISP μ-com)		
37	PI2	0	RES (for DISP μ-com)		
38	PI3	0	REQ (for DISP μ-com)		
39	PB0	ı	BUSY (for DISP μ-com)		
40	PB1	_	NC		
41	PB2	I/O	SDATA		
42	PB3	I/O	SBUSY		

PIN No	PIN NAME	I/O	SYMBOL/FUNCTION				
43	EC	_	GND				
44	PX0	_	GND				
45	PX1	_	NC				
46	PX2	ı	NBI H: OFF L: ON				
47	PAO	0	POWER H: OFF L: ON				
48	PA1	0	MUTE				
49	PA2	0	CLK (for GE IC)				
50	PA3	0	DI (for GE IC)				
51	PF0	0	POUT (for SP RELAY) H: ON L: OFF				
52	PF1	0	PI1 P.MODE PI1 PI2				
			P.MODE PI1 PI2 ARENA I I				
53	PF2	0	PI2 STADIUM I I				
	,,,,		JAZZ I 0				
			OFF 0 0				
54	PF3		NC				
55	PEO	0	AUX				
56	PE1	0	AUX				
57	PE2	0	HIT.M H: ON				
58	PE3		NC NC				
59	PY0	_	NC				
60	PY1		NC				
61	PY2	1	CE (BACK UP: L)				
62	PY3		REMO				
63	PDO	0	NBO				
64	PD1	0	VOLD				
65	PD2	0	VOLU				
66	PD3	0	VOLL				
67	PC0	ı	RETO				
68	PC1	ı	RET1				
69	PC2	ı	RET2				
70	PC3	1	RET3				
71	Vss	_	Vss GND				
72	XTAL	_	XTAL				
73	NC	_	NC				
74	EXTAL	_	EXTAL				
75	Vdd	_	Vref (+5V)				
76	Vfdp	_	Vfdp (-30V)				
77	SCANO	0	S0				
78	SCAN1	0	S1				
79	SCAN2	0	S2				
80	SCAN3	0	S3				

CIRCUIT DESCRIPTION

Initial condition

INPUT SELECTOR	TUNER
PRESENCE MODE	OFF
EFECT	OFF
MANU/REFER	MANU
N.B	OFF
GE	FLAT (0dB)

Test mode

1)Operation

Insert the AC plug into the wall outlet while holding down the selector "CD" key.

2)Cancel

Power OFF.

3)Content

a)Indicators lighting test.

All the indicator are turned ON at first, and they are returned to the normal indication when any key is pressed.

Set the contents of memories $M \sim M5$ as follows.

M1,4 -12dB

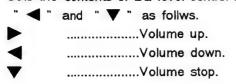
M2,5 0dB

M3 +12dB

In all the range of frequency, the EQ leve "UP" key is used to set three points of +12dB,0dB and -12dB

b)Motor volume test

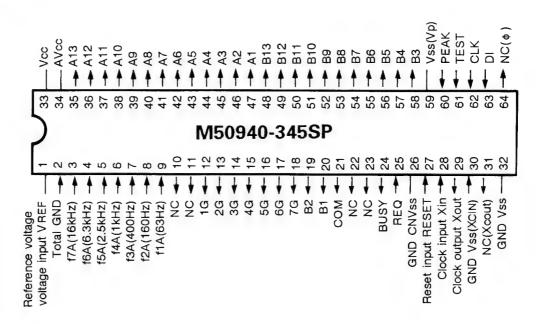
Sets the contents of EQ level control keys, " > ",



CIRCUIT DESCRIPTION

GE microprocessor: M50940-345SP

Pin connection

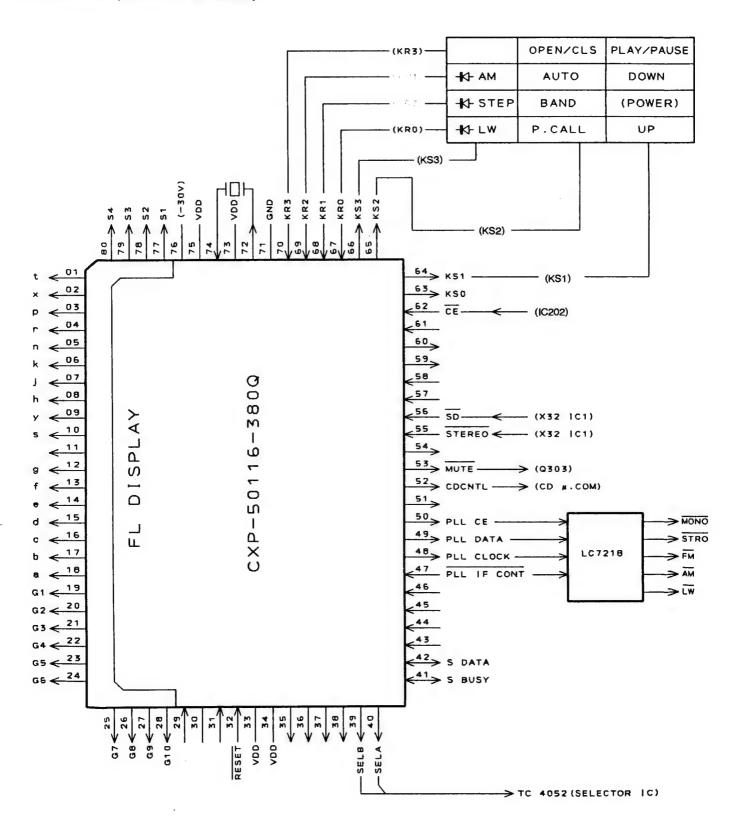


Pin function

Pin No.	Pin name	I/O	Name	Description	
1	VREF	_	VREF	Reference voltage input for A/D converter.	
2	IN7	1	Total	Not used (GND).	
3~9	IN6~0	V	f7A~f1A	f7A~f1A (16k, 6.3k, 2.5k, 1k, 400, 160, 63Hz) level analog input.	
10, 11	P47, 46		_	Not used (Open).	
12~18	P45~40, 37		1GB~7GB	FL grid drive (1GB~7GB).	
19~21	P34~36	0	B2, B1, COM	FL segment drive	
22, 23	P32, 33			Not used (Open).	
24	P31		BUSY	BUSY signal output pin for communicating to main microprocessor.	
25	P30	- 1	REQ	REQ signal input pin for communicating to main microprocessor.	
26	CNVss	_	_	Connect the GND.	
27	RESET	1	RESET	Reset pin (H: Normal, L: Reset).	
28	XIN			Oscillator connect pin (4MHz).	
29	Хоит	0	_		
30	Xcin	1		Not used (GND).	
31	Хсоит	0		Not used (Open).	
32	Vss	_		GND pin.	
33	_	0		Not used (Open).	
34	R3		DI	DATA signal input pin for communicating to main microprocessor.	
35	R2	-1	CLK	CLOCK signal input pin for communicating to main micro processor.	
36	R1		TEST	Test mode setting pin (H: Test, L: Normal). Not Used.	
37	RO		PEAK	Peak hold detection (H: Without, L: With). Not Used.	
38	VP	_	_	Connect the -30V.	
39~49	P17~P05		B3~B13	Fl segment drive (16B~26B), (H: ON, L: OFF).	
50~62	P04~P00 P27~P20	0	A1~A13	FL segment drive (1B~13B), (H: ON, L: OFF).	
63, 64	AVcc, Vcc	_		Power supply pin (+5V).	

CIRCUIT DIAGRAM

FL DISPLAY (CXP50116-380Q)



CIRCUIT DESCRIPTION

Pin descriptions (CXP50116-380Q)

PIN No	PIN NAME	I/O	SYMBOL/FUNCTION		
1	PG0	0	FL SEGMENT (t)		
2	PG1	0	FL SEGMENT (x)		
3	PG2	0	FL SEGMENT (p)		
4	PG3	0	FL SEGMENT (r)		
5	PKO	0	FL SEGMENT (n)		
6	PK1	0	FL SEGMENT (k)		
7	PK2	0	FL SEGMENT (j)		
8	PK3	0	FL SEGMENT (h)		
9	PJ0	0	FL SEGMENT (y)		
10	PJ1	0	FL SEGMENT (s)		
11	PJ2	0	FL SEGMENT (m)		
12	PJ3	0	FL SEGMENT (g)		
13	T15	0	FL SEGMENT (f)		
14	T14	0	FL SEGMENT (e)		
15	T13	0	FL SEGMENT (d)		
16	. T12	0	FL SEGMENT (c)		
17	T11	0	FL SEGMENT (b)		
18	T10	0	FL SEGMENT (a)		
19	Т9	0	FL GRID (1G)		
20	Т8	0	FL GRID (2G)		
21	T7	0	FL GRID (3G)		
22	Т6	0	FL GRID (4G)		
23	T5	0	FL GRID (5G)		
24	T4	0	FL GRID (6G)		
25	Т3	0	FL GRID (7G)		
26	T2	0	FL GRID (8G)		
27	T1	0	FL GRID (9G)		
28	ТО	0	FL GRID (10G)		
29	INT	_	GND		
30	TX	0	NC		
31	TEX	ı	GND		
32	RES	ı	RESET		
33	NC	_	Vdd		
34	Vdd	_	Vdd (+5V)		
35	PIO	1	GND		
36	PI1	ı	GND		
37	PI2	ı	GND		
38	PI3	ı	GND		
39	PB0	0	SEL B		
40	PB1	0	SEL A		

PIN No	PIN NAME	I/O	SYMBOL/FUNCTION
41	PB2	I/O	SDATA
42	PB3	1/0	SBUSY
43	EC	١	GND
44	PX0	ı	GND
45	PX1	ı	GND
46	PX2	1	GND
47	PA0	- 1	PLL IF
48	PA1	0	PLL CK
49	PA2	0	PLL DT
50	PA3	0	PLL CE
51	PF0	0	GND
52	PF1	0	CDCNTL L: TUNER H: CD
53	PF2	0	MUTE L: ON H: OFF
54	PF3	0	POWER L: OFF H: ON
55	PEO	1	STEREO L: MONO H: ST
56	PE1	1	SD L: TUNED
57	PE2	1	GND
58	PE3	1	GND
59	PY0	0	_
60	PY1	0	_
61	PY2	ı	CE (BACK UP: L)
62	PY3	1	GND
63	PD0	0	GND
64	PD1	0	KS1
65	PD2	0	KS2
66	PD3	0	KS3
67	PC0		KRO
68	PC1	ı	KR1
69	PC2	ı	KR2
70	PC3	-	KR3
71	Vss	_	Vss GND
72	XTAL	0	XTAL
73	NC		NC
74	EXTAL	ı	EXTAL
75	Vdd	_	Vref (+5V)
76	Vfdp	_	Vfdp (-30V)
77	SCANO	0	FL SEGMENT (S 1)
78	SCAN1	0	FL SEGMENT (S2)
79	SCAN2	0	FL SEGMENT (S3)
80	SCAN3	0	FL SEGMENT (S4)

CIRCUIT DESCRIPTION

Test mode (without A-A3)

Setting of tuner test mode

(1) Method

While pressing "DOWN" key, turn the AC ON.

(2) Contents

Power ON

FL all lit

Selector TUNER

Test frequency setting (table 1)

TYPE	OTHER T	, E TYPE	T, E TYPE		
СН	NARROW	WIDE	LW/MW	MW	
1	FM 98.0MHz	FM 98.0MHz	FM 98.0MHz F	M 98.0MHz	
2	FM108.0MHz	FM108.0MHz	FM108.0MHz F	FM108.0MHz	
3	AM 630KHz	AM 630KHz	AM 630KHz	AM 630KHz	
4	AM 990KHz	AM 990KHz	AM 990KHz	AM 990KHz	
5	AM 1440KHz	AM 1440KHz	AM 1440KHz	AM 1440KHz	
6	AM 1610KHz	AM 1610KHz	AM 1602KHz	AM 1602KHz	
7	FM 87.5MHz	AM 1700KHz	LW 162KHz F	FM 87.5MHz	
8	FM 87.5MHz	FM 87.5MHz	LW 216KHz F	FM 87.5MHz	
9	FM 87.5MHz	FM 87.5MHz	LW 270KHz F	FM 87.5MHz	
10	FM 89.1MHz	FM 89.1KHz	FM 89.1MHz F	FM 89.1MHz	
11	FM 87.5MHz	FM 87.5MHz	LW 279KHz	FM 87.5MHz	
12	FM 90.0MHz	FM 90.0MHz	FM 90.0MHz F	FM 90.0MHz	
13	FM106.0MHz	FM106.0MHz	FM 106.0KHz	FM 106.0KHz	
14	AM 530KHz	AM 530KHz	AM 531KHz	AM 531KHz	
15	FM 87.5MHz	FM 87.5MHz	LW 153KHz	FM 87.5MHz	
16	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	
17	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	
18	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	
19	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	
20	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	FM 87.5MHz	

Setting of deck test mode.

(1) Method

While pressing "UP" key, turn the AC ON.

(2) Contents

Power ON

Selector DECK

Setting of CD test mode.

(1) Method

While pressing "PLAY/PAUSE" key, turn the AC

Then just short-circuiting the TP7 and TP8.

(2) Contents

Power ON.

Selector CD

Setting of initial conditions (reset)

(1) Method

While pressing "P.CALL" key, turn the AC ON.

(2) Contents

Clears all the memory and returns to the initial conditions.

However, the test frequency in newly memorized in the preset memory at this time.

Conditions by destination

TYPE	Di	ode	e S	W			Channel		
	3	2	1	0	BAND	f range	space	IF	RF
м	0	1	0	0	FM	87.5~108.0MHz	100kHz	10.7MHz	50kHz
IVI		Ľ	U	U	AM	530~1610kHz	10kHz	450kHz	10kHz
K.P	0		1	0	FM	87.5~108.0MHz	100kHz	10.7MHz	50kHz
K.F	O		1	0	AM	530 ~ 1700kHz	10kHz	450KHz	10kHz
м.х	0	0	0	0	FM	87.5~108.0MHz	50kHz	10.7MHz	50kHz
IVI. A	O	0	U	٥	AM	531 ~ 1602kHz	9kHz	450kHz	9kHz
					FM	87.5~108.0MHz	50kHz	10.7MHz	50kHz
T.E	0	0	0	1	MW	531 ~ 1602kHz	9kHz	450kHz	9kHz
					LW	153~279kHz	9kHz	450kHz	9kHz

1. With diode

0. Without diode

DO →D212

D1 →D213

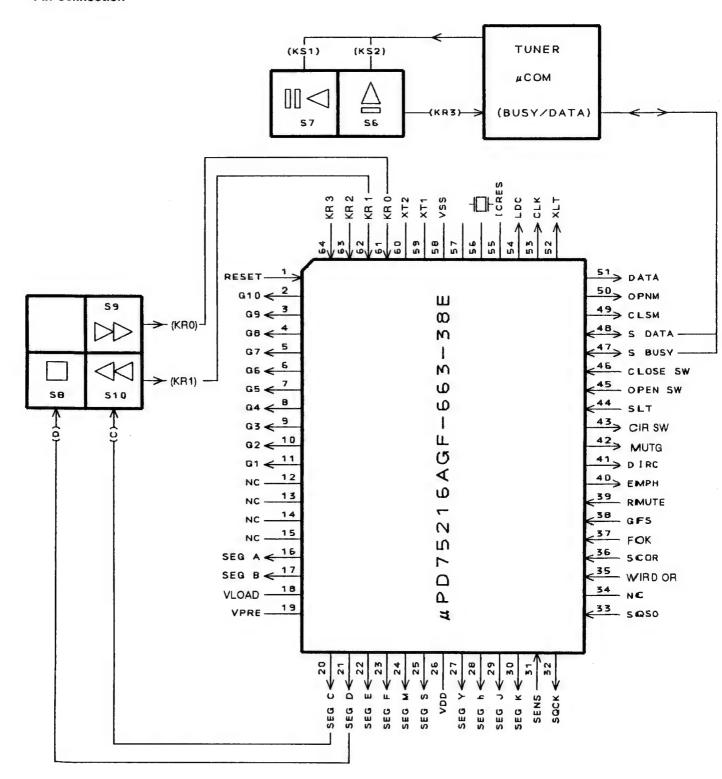
D2 → D214/D216

D3 -

CIRCUIT DIAGRAM

CD: Microprocessor (µPD75216AGF-663-3BE)

Pin connection



CIRCUIT DESCRIPTION

Pin description

NO	PIN NAME	1/0	NAME	FUNCTION	
1	RESET	1	RESET	Reset input	(ACTIVE:L)
2~11	T0~T9	0	G10~G1	FL digit control terminals	
3~15	T10~T13	_	NC		
16	T14	0	SEG A		
17	T15	0	SEG B		
18	VLOAD	ı	VLOAD	FL driver negative power supply -30V	
19	VPRE	ı	VPRE	FL predriver power supply -5V	
20~25	S9~S4	0	SEG C,D,E,F,M,S	C and D also used for key scan SIGNAL	
26	VDD	1		+5V	
27~30	S3~S0	0	SEG Y,R,J,K	_	
31	INT4	ı	SENS	Signal detection terminal for sense signal from preservo IC	rocessor and
32	SCK	0	SQCK	Q data read clock input terminal	
33	so	1	sqso	Q data input terminal	
34	SI	-		NC	
35	INTO	1	WIRD OR	Display select	
36	INT1	1	SCOR	Sub-code frame sync detection signal input	
37	INT2	Ī	FOK	FOK signal from RF amp	focus OK:H
38	T10	ı	GFS	Frame sync signal input	H:Frame synd
39	P20	0	R MUTE	Analog mute control ACT	
40	P21	0	EMPH	Not use	
41	P22	0	DIRC	Dirc terminal of servo IC	
42	P23	0	MUTG	Not use	
43	P30	0	CIRSW	+5V ON/OFF control for CD	
44	P31	ı	SLTSW	SLED LIMIT switch	INNER:L
45	P32	1	OPEN SW	TRAY OPEN switch	OPEN:L
46	P33	1	CLOSE SW	TRAY CLOSE switch	CL0SE:L
47	P60	1/0	SBUSY	_	
48	P61	1/0	SDATA	_	
49	P62	0	CLSM	TRAY motor close	ACTIVE: H
50	P63	0	OPNM	TRAY motor open	ACTIVE: H
51	P40	0	DATA	Signal processor and servo IC control DATA	
52	P41	0	XLT	Signal processor and servo IC control LATCH	
53	P42	0	CLK	Signal processor and servo IC control CLOCK	
54	P43	0	LDC	Laser ON/OFF signal output	ACTI VE:H
55	PR0	0	ICRES		
56	X1	1	_	Systemclock terminal	
57	X2	0	_	Systemclock terminal	
58	Vss	-	_	GND	
59	XT1	_	_	GND	
60	XT2	_	_	NC	
61~64	P50~P53	1	KR0~KR3	-	

CIRCUIT DESCRIPTION

Test Mode

Setting the Test Mode

This microprocessor built in this unit can be put to TEST MODE by just short-circuiting the test pins (#7 and #8).

The TEST MODE can be also initiated with short-circuiting the test pins when tray is OPEN. If unit is in test mode, TRACK No. displays "05".

1-2. Key and functions valid in test mode

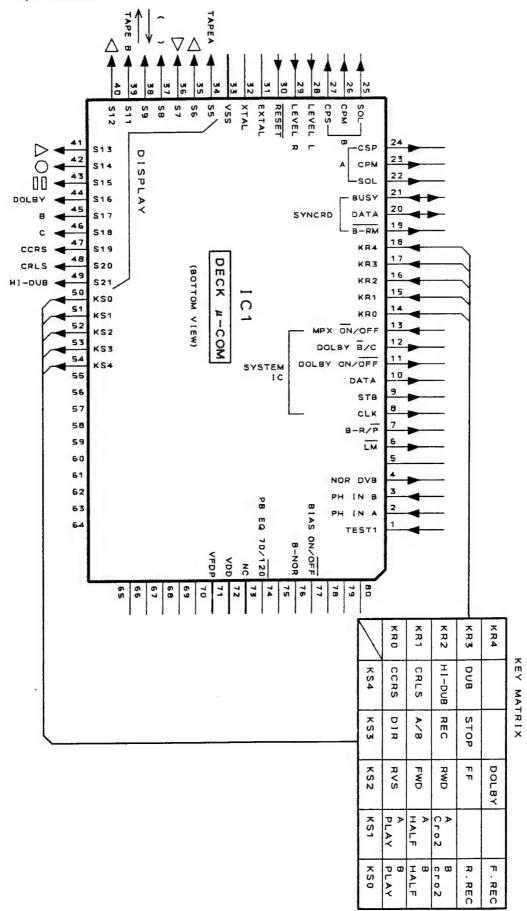
No.	Input key	Function	Track No. display
1	PLAY ▷	(1) Focusing servo ON (2) Tracking servo ON (3) Feed servo ON	TRACK NO.
			Displayed for a few seconds after completion (1), (2) and (3). Disc Track No. is displayed.
2	UP ⋈	(1) Focusing servo ON (2) Tracking servo OFF (3) Feed servo OFF	TRACK NO.
3	STOP	(1) Focusing servo OFF (2) Tracking servo OFF (3) Feed servo OFF	TRACK NO.
4	DOWN I⊲⊲	Track No. 7, 8, and 6 (High-speed) are programmed and playback from Track No. 7. The test mode is cancelled.	-
5	OPEN/CLOSE	When the tray is opened then closed. Track No. 7, 8, and 6 are programmed and set is in STOP mode. The test mode is cancelled.	TRACK NO.

INITIAL SET-UP

SYSTEM OFF LDC LOW CIRSW LOW ICRESET LOW DSP IC DEAD	WIRD OR LOW (TUNER) DIRC HIGH TIME SINGLE INCS MODE TRACK REPEAT OFF
--	--

CIRCUIT DIAGRAM

DECK-Microprocessor



CIRCUIT DESCRIPTION

PIN No	NAME	I/O	SYMBOL/FUNCTION
1	PE3	1	TEST 1
2	PE4	1	PHA
3	PE5	1	PHB
4	PE6	0	NOR DUBB [DISPLAY]
5	PE7	0	NC
6	PB0	0	LM
7	PB1	0	R/B O
8	PB2	0	CLK THE
9	PB3	0	DATA
10	PB4	0	
11	PB5	0	DOL NO/OFF ≥
12	PB6	0	DOL B/C
13	PB7	0	DOL NO/OFF
14	PC0	ŀ	KR0
15	PC1	ŀ	KR1
16	PC2	ŀ	KR2
17	PC3	I	KR3
18	PC4	1	KR4
19	PC5	0	RM
20	PC6	I/O	DATA
21	PC7	I/O	BUSY
22	PAO	0	SOL ¬
23	PA1	0	CAP A MECHA.
24	PA2	0	SP —
25	PA3	0	SOL ¬
26	PA4	0	CAP B MECHA.
27	PA5	0	SP _
28	PA6	1	L LEVEL INPUT
29	PA7	1	R LEVEL INPUT
30	RST	1	RESET
31	EXTAL	_	
32	XTAL	_	
33	Vss	_	GND

PIN No	NAME	I/O	SYMBOL/FUN	CTION
34	PD0	0	TAPE A	7
35	PD1	0	⊲	
36	PD2	0	D	
37	PD3	0	()	
38	PD4	0	\$	
39	PD5	0	TAPE B	
40	PD6	0	⊲	DISPLAY CONTROL
41	PD7	0	▷	T F
42	PFO	0	REC (
43	PF1	0	H	_ ≽
44	PF2	0	DOLBY	
45	PF3	0	В	DIS
46	PF4	0	С	
47	PF5	0	CCRS	
48	PF6	0	CRLS	
49	PF7	0	HIGH-DUBB —	
50	S16	0	KS0	
51	S17	0	KS1	
52	S18	0	KS2	
53	S19	0	KS3	
54	S20	0	KS4	
55			NC	
1		_	₹ .	
70			NC	
71	VFDP	1	- 30V	
72	VDD	1	+ 5V	
73			NC	
74	PG0	0	PBEQ 70/120	
75	PG1		NC	
76	PG2	0	BIAS NOR/Cr02	
77	PG3	0	BIAS ON/OFF	
78	PE0	I	PULL UP	
79	PE1	-	PULL UP	
80	PE2	1	TEST 2	

CIRCUIT DESCRIPTION

Test mode

1) Method

TEST 1: While pressing "STOP" key, turn

the AC ON.

TEST 2: While pressing "one way mode"

key,turn the AC ON.

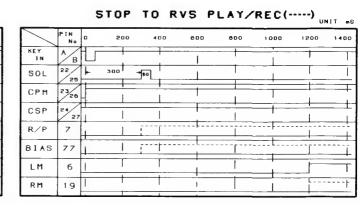
2) Cancellation

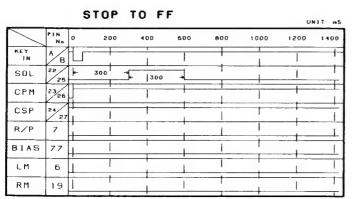
Power OFF

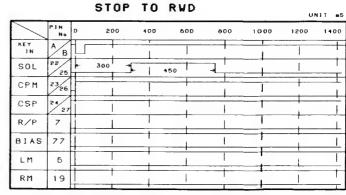
3) Operation

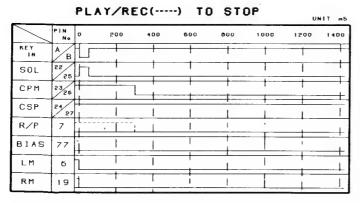
	NO	KEY	OPERATION	DISPLAY
TEST1	1	REC	4 sec. REC PLAY 4 sec. The se	
	2	CCRS	12 sec. REC REC PLAY -6 sec6 sec6 sec1 DOL OFF DOL C RWD DOL OFF DOL C	DOL OFF DOL C
	3	CRLS	A,B MECHA CHECK.(PLAY MODE) HI-SP NOR-SP NOR-SP RVS A DECK 2 7 2 B DECK 2 7 2	
	4		REEL PULS CHECK. REC INHIBIT SW CHECK.(B DECK)	FLASHING TAPE A , TAPE B LIGHT RVS FWD
TEST2	5	CCRS	INPUT LEVEL ATTENUATION. PRESSING THE "CCRS" KEY DURING IN REC MODE.	

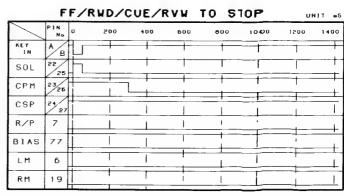
CIRCUIT DESCRIPTION





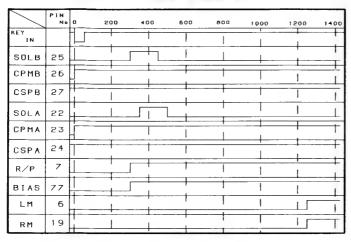




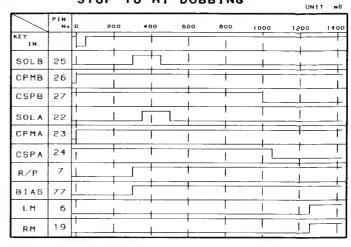


CIRCUIT DESCRIPTION

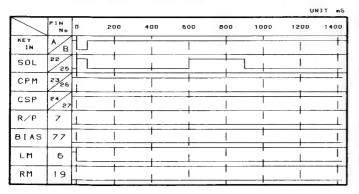
STOP TO NOR DUBBING



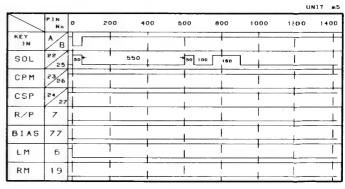
STOP TO HI DUBBING



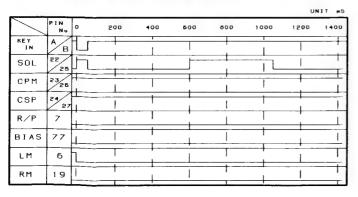
FWD PLAY TO CUE



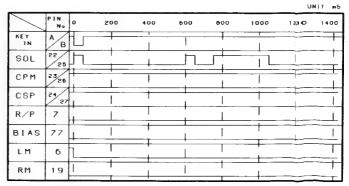
RVS PLAY TO CUE



FWD PLAY TO REVEW



RVS PLAY TO REVEW



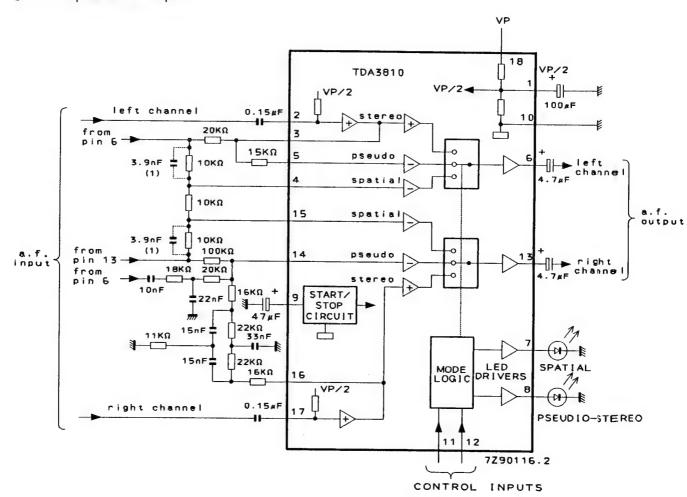
CIRCUIT DESCRIPTION

The TDA3810 integrated circuit provides spatial, stereo and pseuod-stereo sound for radio and television equipment. Features

- Three switched functions: Spatial (widened stereo image)
 - Stereo

Pseudo-stereo (artificial stereo from a mono source)

- Offset compensated operational amplifiers to reduce switch noise
- LED driver outputs to facilitate indicator of selected operating mode
- Start/stop circuit to rediuce switch noise and to prevent LED-flicker
- TTL-compatible control inputs



Truth table

	Control input state		LED	LED
Mode			Spatial	Pseudo
	pin 11	pin 12	pin 7	pin 8
Mono pseudo-stereo	HIGH	LOW	off	on
Spatial stereo	HIGH	HIGH	on	off
Stereo	LOW	Х	off	off

MODE	PIN 11	PIN 12
ARENA	1	1
STADILIM	t	1
JAZZ	1	0
HIT-MASTER	1	0
OFF)	0

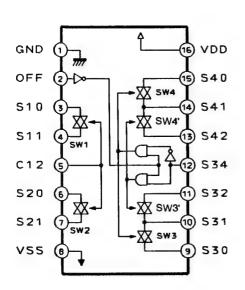
LOW =0 to 0,8 V (the less positive voltage)

HIGH=2V to 5,5 V (the more positive voltage)

X =don't care

CIRCUIT DESCRIPTION

TC9215P (SELECTOR)



Pin No.	Pin name	Function
1	GND	GND
2	OFF	Switch (3) (4) off input
3	S10	
4	511	Switch (1) 1/O
5	C12	Switch (1) (2) control
6	\$20	
7	521	Switch (2) I/O
8	Vss	Power supply (-)
9	530	
10	831	Switch (3) I/O
11	532	
12	C34	Switch (3) (4) control

13	B42	
14	B41	Switch (4) I/O
15	B40	
16	VDD	Power supply (+)

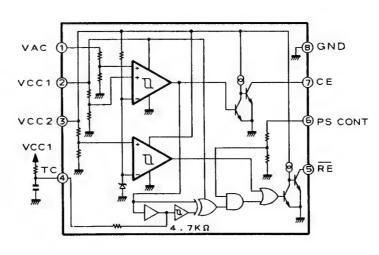
Truth value table

C12	SW1,SW2
Н	ON
L	OFF

OFF	C34	B30-B31	B31-B32
		B40-B41	B41-B42
	L	ON	OFF
L	н	OFF	ON
Н	*	OFF	OFF

(* H or L)

PST620D (RESET IC)



PIN NO.	PIN NAME	FUNCTION	
1	Vac	Holds +2.0V detection voltage, conducts rapid power failure detection by monitoring the primary side of the AC power supply (which is the original source of all power) and the stabilizing power supply.	
2	V CC1	+5V main power supply	
3	Vcc2	Backup power supply (connected to backup condensor)	
4	TC	Pulse sharper pulse width setting pin (connect to condensor and resistor)	
5	RE	Reset output	
6	PS CONT	Pulse sharper output ON-OFF switch Hi: OFF Lo: ON	
7	CE	Chip enable signal output	
8	GND	GND (earth)	

CIRCUIT DESCRIPTION

FM, AM, MPX system IC: LA1851N

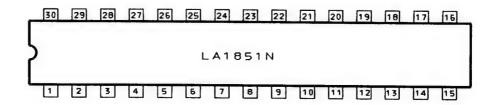
Function - FM: IF amprifier , Quadrant latchar detector , IF count buffer , S meter output , Tu indicator (variable sensitivity)

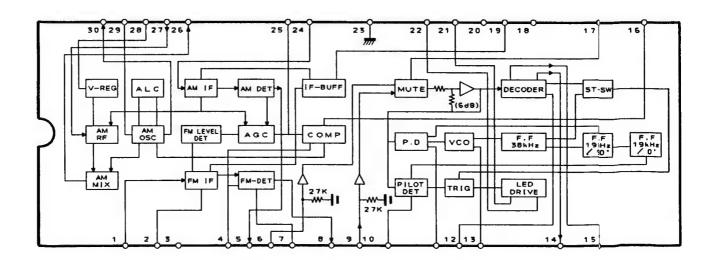
- AM: RF amplifier, mixer, oscillator, oscillator buffer, IF amplifier, detector, AGC, IF count buffer, Tu indicator (variable sensitivity)

- MPX: PLL decorder , ST indicator , VCO stop mute, sepalation control, VCO nonadjust, forced monaural (VCO stop) Salient features

FM,AM tuner and MPX in a single chip

- MPX made nonadjusting
- · Electronic synchronization compatible IF count buffer output (FM/AM)
- ST separation control
- · Forced monaural, VCO stop





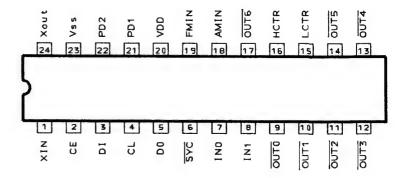
CIRCUIT DESCRIPTION

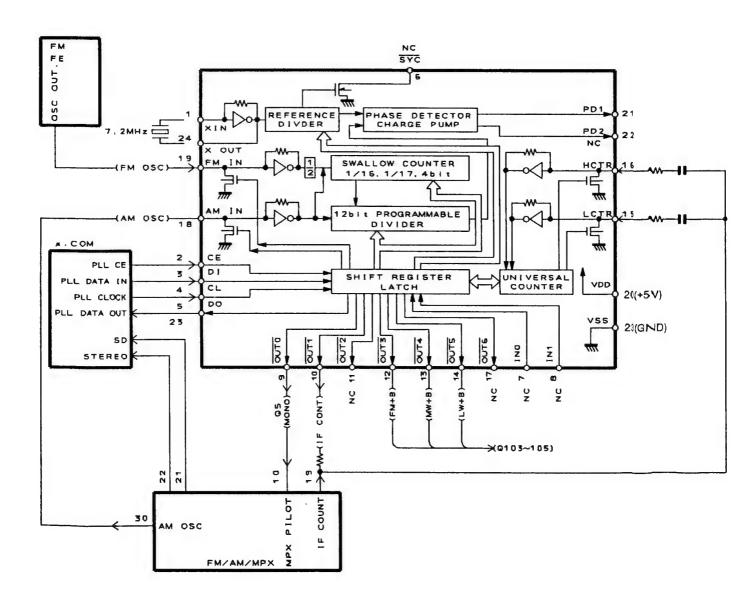
Pin description

Pin No.	Function	Remark
1	FM IF input	Input impedance:330 Ω
2	FM IF bias	_
3	Vcc	_
4	FM AFC output	When FM AFC is detuned, the ST LED goes off and the forced monaural mode is set
5	AM demodulation output	
	MEN AM DET :	MPX section,AM demodulation input.
6	MPX AM DET input	Input impedance:27kΩ
7	FM descrimination output	
8	FM demodulation output	Output impedance:5kΩ
		MPX selection. FM demodulation input.
9	MPX FM DET input	Input impedance
10	MPX Pilot synchronism	MPX VCO stops by shorting the voltage at pin 10 to the VCC line at pin 3. A 3.3
10	detection filter	kΩ current limiting resistor is required.
11	MPX PLL loop filter	_
12	MPX separation control	-
13	MPX VCO	Ceramic oscillator
14	MPX L-ch output	_
15	MPX R-ch output	_
16	AM SD ADJ	_
	AADV AE di	V _{HI} (≥ 1.5V) : Mute ON
17	MPX AF muting drive	VLo(< 1.5V) : Mute OFF
	AA4/5A4 - I	VHI (≥ 1.5V) : FM
18	AM/FM change	VLO(< 1.5V) : AM
	AM/FM	
19	IF count output	V _H (≥ 1.5V) : IF CNT ON
	SW combined use	VLO(< 1.5V): IF CNT OFF
		V _{HI} (≥ 1.5V):LED forced off
20	TU/ST LED	(Forced monaural mode)
18		VLo(<1.5V):Normal
21	AM/FM TU LED	_
22	MPX ST LED	_
23	AM/FM MPX GND	_
24	AM IF input	Input impedance:2kΩ
	AM AGC output	
25	FM S meter output	
26	AM MIXER output	_
27	AM RF input	_
28	V Reg	Vreg=2.3V
29	AM OSC	_
	AM OSC buffer output	
30	FM SD ADJ combined use	_

CIRCUIT DESCRIPTION

PLL IC: LC7218





CIRCUIT DESCRIPTION

Servo Signal Processor: CXA1372Q (IC2)

Outline

CXA1372Q is a bipolar IC developed to be used for processing of the RF signal (Focus OK, mirror, defect, comparator of EFM) and servo control.

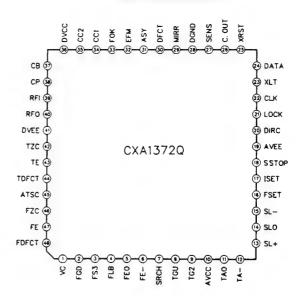
Functions

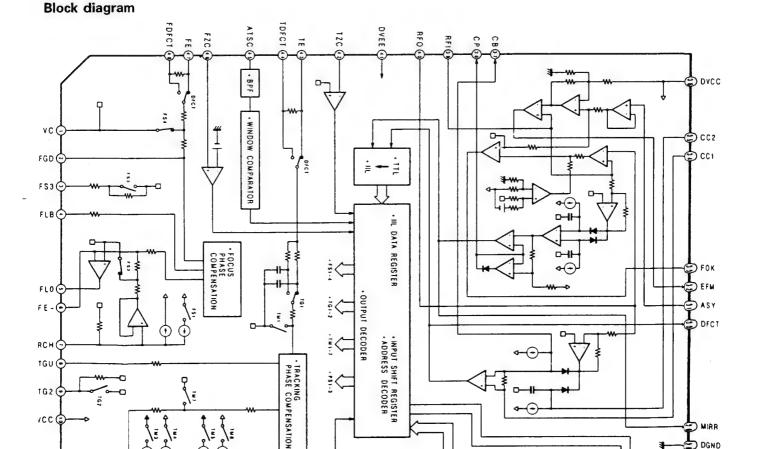
VCC (

TAO

- · Auto asymmetry control
- Focus OK detection circuit
- · Mirror detection circuit
- · Defect detection and countermeasure circuit
- EFM comparator
- Focus servo control
- Tracking servo control
- Thread servo control

Pin connections





DOND

SENS C OUT) XRST

CIRCUIT DESCRIPTION

Pin functions

Pin No.	Pin name	1/0	Function
1	VC	1	Middle-point voltage input terminal.
			When two power sources are used : GND, when single power source is used : (Vcc+GND)/2.
2	FGD	T	When lowering the high-band gain of the focus servo, insert a capacitor between this terminal and terminal No. 3.
3	FS3	1	Change the high-band gain of the focus servo by turning FS3 on and off.
4	FLB	1	Outside terminal of time constant for raising the low-band of the focus servo.
5	FEO	0	Focus drive output.
6	FE-		Inverted input terminal of focus amplifier.
7	SRCH	1	Outside terminal of time constant for making focus search waveform.
8	TGU	1	Outside terminal of time constant for changing high-band gain of tracking.
9	TG2	1	Outside terminal of time constant for changing high-band gain of tracking.
10	AVCC		
11	TAO	0	Tracking drive output.
12	TA-	1	Inverted input terminal of tracking amplifier.
13	SL+	1	Non-inverted input terminal of thread amplifier.
14	SLO	0	Thread drive output.
15	SL-	1	Inverted input terminal of thread amplifier.
16	FSET	1	Terminal for setting the peak for phase compensation of focus tracking.
17	ISET	1	Current for determining the height of the focus search track jump thread kick is applied.
18	SSTOP	1	Terminal for ON/OFF detecting signal of limit switch for detecting the most inside line of disc.
19	AVEE		g are mode into or disc.
20	DIRC	1	Used to jump over one track. $47k\Omega$ pull-up resistor is inserted.
21	LOCK	ı	When "L", thread runaway-preventive circuit operates. $47k\Omega$ pull-up resistor is inserted.
22	CLK	1	Clock input for transferring the serial data from CPU (having no pull-up resistors).
23	XLT	1	Latch input from CPU (having no pull-up resistors).
24	DATA		Serial data input from CPU (having no pull-up resistors).
25	XRST	1	Reset when reset input terminal is at "L" (having no pull-up reisitors).
26	C. OUT	0	Signal output for counting tracks.
27	SENS	0	Outputs FZC, AS, TZC, SSTOP, etc. on receipt of command from CPU.
28	DGND		
29	MIRR	0	Output terminal of MIRR comparator. (DC voltage :- Load of 10kΩ connected)
30	DFCT	0	Output terminal of DEFECT comperator. (DC voltage: Load 10kΩ connected)
31	ASY	1	Input terminal of auto asymmetry control.
32	EFM	0	Output terminal of EFM comparator. (DC voltage : Load of 10kΩ connected)
33	FOK	0	Output terminal of focus OK comparator. (DC voltage: Load of $10k\Omega$ connected)
34	CC1		DEFECT bottom hold output terminal.
35	CC2	0	Terminal in which DEFECT bottom hold output is input after capacitive coupling.
36	DVCC		The state of the s
37	СВ	1	Terminal to which DEFECT bottom hold capacitor is connected.
38	CP	1	Terminal for connecting MIRR hold comparator. Non-inverted input terminal of MIRR comparator.
39	RFI	1	Terminal in which output of RF summing amplifier is input after capacitive coupling.
40	RFO	0	Output terminal of RF summing amplifier. Check point of eye pattern.
41	DVEE		- To person
42	TZC	1	Input terminal of tracking zero cross comparator.
43	TE	T	Input terminal of tracking error.
44	TDFCT	1	Terminal for connecting the capacitor for time constant in case of defect.
45	ATSC	1	Input terminal of window comparator for detecting ATSC.
46	FZC	1	Terminal for inputting the focus zero cross comparator.
47	FE	1	Input terminal of focus error.
48	FDFCT	1	Terminal for connecting capacitor for time constant in case of defect.

CIRCUIT DESCRIPTION

Digital Signal Processor : CXD2500AQ (IC6)

Outline

The CXD2500AQ is a digital signal processing LSI for a compact disc player, which has the following functions.

- A wide frame jitter margin realized by 32-KRAM (±28 frames)
- Bit clocks for strobing EFM signal are generated by the digital PLL, and the capture range is ±150kHz minumum
- Demodulation of EFM data
- Protection and reinforcement of EFM frame sync signal
- Strong error correction by refined super strategy.
 C1 : Double correction, C2 : Quadruple correction
- · Double-speed replay and variable pitch replay
- Reduction of noise generation at track jumps
- Auto zero cross muting
- Demodulation of sub-code and detection of errors in sub-code Q data

- Digital spindle servo (Having over-sampling filter)
- 16-bit traverse counter
- · CPU interface by serial bus
- A built-in servo auto sequencer
- · Output for digital audio interface
- · Built-in digital level meter and peak meter
- · Applicable to bilingual system

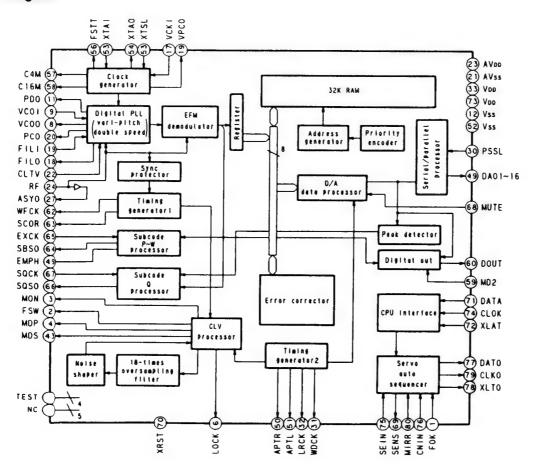
Features

- One chip of this LSI can process all the digital signals used for replay
- Integration level can be heightened because of the built-in RAM

Structure

Silicon gate CMOS

Block diagram



CIRCUIT DESCRIPTION

Pin functions

Pin No.	Pin name	1/0	Function
1	FOK	1	Focus OK input terminal. Used for SENS output and servo auto sequencer.
2	FSW	0	Output for changing output filter of spindle motor.
3	MON	0	ON/OFF control output of spindle motor.
4	MDP	0	Servo control of spindle motor.
5	MDS	0	Servo control of spindle motor.
6	LOCK	0	Outputs "H", when GFS is sampled at 460Hz and it is "H". Output "L", if "L" is detected eight times continuously.
7	NC	-	Not used.
8	VCOO	0	Oscillation circuit output for analog EFM PLL.
9	VCOI	1	Oscillation circuit input for analog EFM PLL. fLock = 8.6436MHz
10	TEST	1	Test terminal, normally grounded.
11	PDO	0	Charge pump output for analog EFM PLL.
12	Vss	_	GND.
13~15	NC	_	Not used.
16	VPCO	0	PLL charge pump output for variable pitch.
17	VCKI	ī	Ciock input fccenter = 16.9344MHz from outside VCO for variable pitch.
18	FILO	0	Filter output for master PLL (Slave = Digital PLL).
19	FILI		Filter input for master PLL.
20	PCO	0	Charge pump output for master PLL.
21	AVss	- 1	Analog GND.
22	CLTV		VCO control voltage input for master.
23	AVDD	- 1	Analog power source (+5V).
24	RF		EFM signal input.
25	TEST2	1	Used for grounding.
26	TEST3		Used for grounding.
27	ASYO	0	EFM full swing output ("L" = Vss, "H" = VDD).
28	TEST4	1	Used for grounding.
29	NC	-	Not used.
30	PSSL	1	Audio data output mode changing input. Set to "L" for serial output and "H" for parallel output.
31	WDCK	0	D/A interface for 48-bit slot. Word clock $f = 2$ Fs
32	LRCK	0	D/A interface for 48-bit slot. LR clock $f = Fs$
33	VDD	-	Source voltage (+5V).
34	DA16	0	Outputs DA16 (MSB) when PSSL = 1. Outputs serial data of 48-bit slot when PSSL = 0. (2s' CONP, MSB first)
35	DA15	0	Outputs DA15 when PSSL = 1. Outputs bit clock of 48-bit slot when PSSL = 0.
36	DA14	0	Outputs DA14 when PSSL = 1. Outputs serial data of 64-bit slot when PSSL = 0. (2s' COMP, LSB first)
37	DA13	0	Outputs DA13 when PSSL = 1. Outputs bit clock of 64-bit slot when PSSL = 0.
38	DA12	0	Outputs DA12 when PSSL = 1. Outputs LR clock of 64-bit slot when PSSL = 0.
39	DA11	0	Outputs DA11 when PSSL = 1. Outputs GTOP when PSSL = 0.
40	DA10	0	Outputs DA10 when PSSL = 1. Outputs XUGF when PSSL = 0.
41	DA09	0	Outputs DA09 when PSSL = 1. Outputs XPLCK when PSSL = 0.
42	DA08	0	Outputs DA08 when PSSL' = 1. Outputs GFS when PSSL = 0.
43	DA07	0	Outputs DA07 when PSSL = 1. Outputs RFCK when PSSL = 0.
44	DA06	0	Outputs DA06 when PSSL = 1. Outputs C2P0 when PSSL = 0.
45	DA05	0	Outputs DA05 when PSSL = 1. Outputs XRAOF when PSSL = 0.
46	DA04	0	Outputs DA04 when PSSL = 1. Outputs MNT3 when PSSL = 0.
47	DA03	0	Outputs DA03 when PSSL = 1. Outputs MNT2 when PSSL = 0.
48	DA02	0	Outputs DA02 when PSSL = 1. Outputs MNT1 when PSSL = 0.
49	DA01	0	Outputs DA01 when PSSL = 1. Outputs MNT0 when PSSL = 0.

CIRCUIT DESCRIPTION

Pin No.	Pin name	1/0	Function
50	APTR	0	Control output for correcting aperture. Set to "H" when Rch.
51	APTL	0	Control output for correcting aperture. Set to "H" when Lch.
52	Vss	-	GND.
53	XTAI	1	X'tal oscillation circuit input of 16.9344MHz, or input of 33.8688MHz.
54	XTAO	0	X'tal oscillation circuit output of 16.9344MHz.
55	XTSL	ı	X'tal selection input terminal. Set to "L" when x'tal is 16.9344MHz, and to "H" when 33.8688MHz.
56	FSTT	0	2/3 division output of terminals 53 and 54. Does not vary as pitch varies.
57	C4M	0	4.2336MHz output. Varies as pitch varies.
58	C16M	0	16.9344MHz output. Varies as pitch varies.
59	MD2	1	Digital-out ON/OFF control. Turns on when "H", and off when "L".
60	DOUT	0	Digital-out output terminal.
61	EMPH	0	Outputs "H" when playing disc has emphasis, and "L" when the latter does not.
62	WFCK	0	WFCK (Write Frame Clock) output.
63	SCOR	0	Outputs "H" when sub-code sync S0 or S1 is detected.
64	SBSO	0	Serial output of Sub P ~ W.
65	EXCK	1	Clock input for SBSO read out.
66	SQSO	0	Sub Q 80-bit and PCM peak, and level data 16-bit output.
67	SQCK	1	Clock input for SQSO read out.
68	MUTE	-	Mutes when "H", and resets when "L".
69	SENS	_	Outputs SENS to CPU.
70	XRST	1	Resets system when "L".
71	DATA		Inputs serial data from CPU.
72	XLAT	I	Latches serial data when latch input from CPU falls.
73	VDD	-	Power supply (+5V).
74	CLOK	1	Serial data transfer clock input from CPU.
75	SEIN	1	Input SENS from SSP.
76	CNIN		Inputs signals for counting number of track jumps.
77	DATO	0	Outputs serial data to SSP.
78	XLTO	0	Outputs serial data latch to SSP, and latches at fall.
79	CLKO	0	Outputs serial data transfer clock to SSP.
80	MIRR		Inputs mirror signal. Auto sequencer uses this for jumping 128 or more tracks.

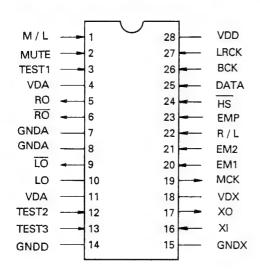
Notes

- The 64-bit slot is 2's compliment output of LSB first, and the 48-bit slot is 2's compliment output of MSB first.
- GTOP is used to monitor the protective condition of the frame sync. ("H": Sync protective window is released.)
- XUGF is the frame sync obtained from the EFM signal, which is a negative pulse. This is the signal before the protection of sync.
- XPLCK is the inverted clock of EFM PLL. PLL is so made that the falling edge will be matched to the change point of the EFM signal.
- The GFS becomes "H" when the frame sync is matched to the internal protection timing.
- RFCK is a signal having the period of 136μ obtained by the accuracy of X'tal.
- C2P0 is a signal indicating the error condition of data
- XRAOF is a signal generated when 32 KRAM exceeds the jitter margin of ±28F.

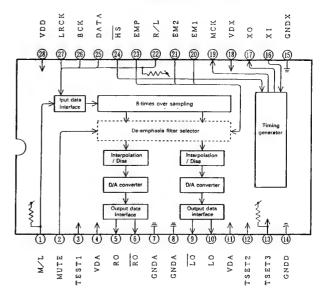
CIRCUIT DESCRIPTION

Converter with Digital Filter: TC9237N (IC7)

Terminal connection diagram



Block diagram



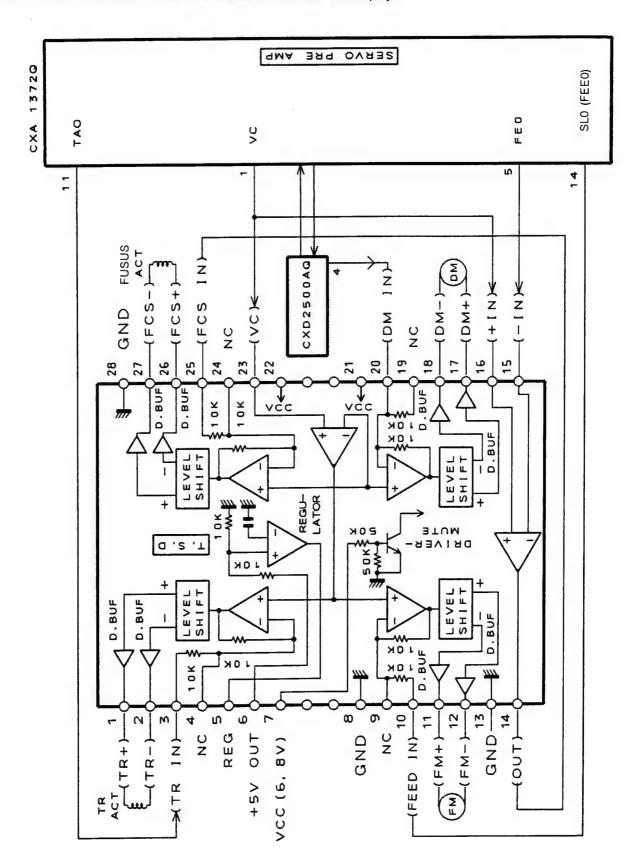
Explanation of terminals

Pin No.	Pin name	I/O	Fun	ction					
1	M/L	1	Selection of MSB first or LSB first. H = MSB, L	election of MSB first or LSB first. H = MSB, L = LSB.					
2	MUTE	i i	Muting control. H = Mute ON	iting control. H = Mute ON					
3	TEST1	1	Test terminal (connect to H level).						
4	VDA	-	Power supply for analog circuit (R-ch).						
5	RO	0	R-ch data output.						
6	RO	0	Inverted R-ch output.						
7	GNDA	-	Ground for analog circuit (R-ch).						
8	GNDA	-	Ground for analog circuit (L-ch).						
9	LO	0	Inverted L-ch data output.						
10	LO	0	L-ch data output.						
11	VDA	-	Power supply for analog circuit (L-ch).						
12	TEST2	1	Test terminal (connect to L level).						
13	TEST3	1	Test terminal (connect to H level or open circui	t).					
14	GND	_	Ground for logic.						
15	GNDX	-	Ground for oscillation.						
16	XI	1	Generation of clock freq (384fs).						
17	XO	0	Generation of clock freq (384fs).						
18	VDX	- 1	Power supply for oscillation.			-			
19	MCK	0	Clock output of system (384fs).						
20, 21	EM1, 2	1	De-emphasis filter selector.	EM1	L	L.	Н	Н	
				EM2	L	Н	Н	L	
				Mode	44.	1kHz	32kHz	48kHz	
22	R/L	1	R / L-ch data selector.	R/L		LF	RCK		
					LC	W	Н	IGH	
				LOW	R-ch	data	L-ch	data.	
				HIGH	L-ch	data	R-ch	dat a.	
23	EMP	1	De-emphasis filter ON / OFF selector (H = ON, L = OFF).						
24	HS		Normal or Double speed selector (H = Normal,	L = Doub	le).				
25	DATA	1	Data input.						
26	BCK	1	Bit clock input.						
27	LRCK	1	LR clock input.						
28	VDD	-	Power supply for logic.						

CIRCUIT DESCRIPTION

Power Driver for CD Player:BA6296FP

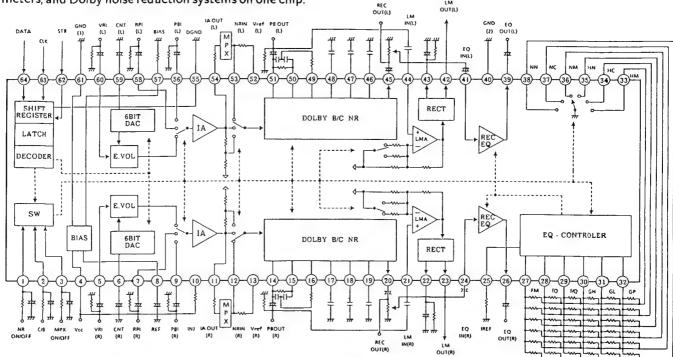
BA6296FP is 4ch BTL driver to drive the actuator and motor of CD player.



CIRCUIT DESCRIPTION

Audio Signal Processing System IC for Cassette Deck (HA12157NT)

The HA12157NT is an audio signal processing LSI chip integrates potentiometers, recording equalizers, level meters, and Dolby noise reduction systems on one chip.



Pin No.						
1	NR ON/OFF					
2	C/B	Mode Control input				
3	MPX ON/OFF					
4	VCC	Power Supply				
5	NR ON/OFF C/B MPX ON/OFF VCC VRI CNT RPI REF PBI INJ IA OUT NR IN VREF PB OUT SS1 SS2 CCR	Volume input				
60	VKI	Volume input				
6	CNT	DAC output Volume control input				
59	CNT	DAC output Volume control input				
7	DDI	Recording input				
58	KFI	Recording input				
8	REF	Ripple filter				
9	PBI	Playback input				
56	FBI	Ріаураск іприт				
10	INJ	Injection current input for I₂L				
11	IA OUT	Input amplifier output				
54	14 001	mpar ampinior output				
12	NDIN	Noise reduction processor input				
53	IVIX IIV	Noise reduction processor input				
13	\/PEE	Reference voltage buffer output				
52	VICI	Reference voltage burier output				
14	PR OUT	Playback (Decode) output				
51	FB 001	Trayback (Decode) output				
15	SS1	Spectral skewing amplifier input				
50	331	Spectral skewing amplifier input				
16	552	Spectral skewing amplifier output				
49		opecial skewing amplifier output				
17	CCR	Current controlled resistor output				
48	CON	Carrent controlled resistor output				
18	HLS DET					
47	TIES DE I	Time constant pin for rectifier				
19	LLS DET	Time constant pin for rectiner				
46	LES DE I					

Pin No.	Name	Function			
20	DEC OUT	Paradia (Francia) attack			
45	RECOUL	Recording (Encode) output			
21	LAAIN	Laurinant			
44	REC OUT Recording (Encode) output LM IN Level meter input LMD Time constant pin for level meter LM OUT Level meter output EQ IN Equalizer input IREF EQ reference current input EQ OUT Equalizer output FM fQ f/Q GH GL GP HM HC HN NC NN GND Ground D GND Digital (Logic) ground BIAS Dolby NR reference current input				
22	LMD	Time constant his factorial mater			
43	LIVID	Time constant pin for level meter			
23	LMOUT	Lavel makes autout			
42	REC OUT Recording (Encode) output LM IN Level meter input LMD Time constant pin for level meter LM OUT Level meter output EQ IN Equalizer input IREF EQ reference current input EQ OUT Equalizer output FM fQ f/Q GH GL GP HM HC HN NC NN GND Ground D GND Digital (Logic) ground BIAS Dolby NR reference current input				
24	EOIN	E-valiancia va			
41	LQTIV	Equalizer input			
25	IREF	EQ reference current input			
26	FOOUT	Equalization autout			
39	LQOUI	Equalizer output			
27	FM	***			
28	fQ	EQ parameter currentinput			
29	f/Q	FO parameter currenting at			
30	GH	EQ parameter currentinput			
31	GL				
32	GP				
33	НМ				
34	HC				
35	HN	FO			
36	NM	Lw parameter selector			
37	NC				
38	NN				
40	CND	Count			
61	GND	Ground			
55	D GND	Digital (Logic) ground			
57	BIAS	Dolby NR reference curen t input			
62	STB				
63	CLK	Mode control input			
64	DATA				

CIRCUIT DESCRIPTION

Operating Mode Control

Electronic switches are used in the HA12157. Noise reduction ON/OFF, C/B, and Multiplex ON/OFF signals controlled by parallel data (DC voltage) and a switch controlled by serial data are provided in the operating mode.

Control Using Parallel Data

Dolby noise reduction and multiplex filter are controlled by the input signal at pins ①, ②, and ③.

Pin No.	Lo	Hi
①	NR-OFF	NR-ON
2	B-NR	C-NR
3	MPX-ON	MPX-OFF

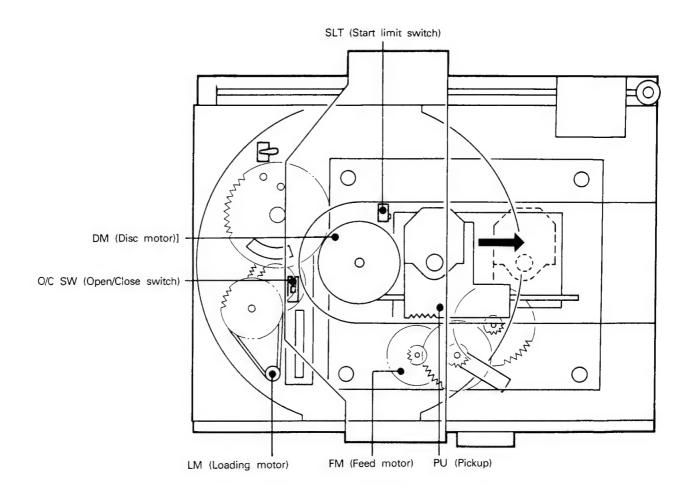
Serial Data Format

An 8-bit shift register is used as the serial data format. The shift register fetches CLK and DATA signals only when the STB signal is high, then latches data at the falling edge of the STB signal.

bit No.			Control regi	ster					Volu	me re	giste	r		
0	TAPE SELECT 1	Н	TS 1	Н	L	DAC 0				bit	No.			Gain
		L	н	METAL	NORMAL			5	4	3	2	1	0	Gain
1	TAPE	Н				DAC 1		L	L	L	L	L	L	High
-	SELECT 2	L	L	CrO₂	NORMAL			L	L	L	L	L	Н	A
2	TAPE	Н	Twice normal	speed selection	n	DAC 2		L	L	L	L	Н	L	
-	SPEED	L	Normal speed	selection		5		L	L	L	L	Н	Н	
3	METER SENSITIVITY	н	Meter sensitivi	ty improved b	oy 20 dB	DAC 3								
,		L	Meter sensitivi	ty normal		DAC 3				_				
_	INPLIT	INPUT H IS1 H I DAGA		Н	Н	н	Н	L	Н	*				
4	SELECT 1		IS 2	Н	L	DAC 4		Н	Н	Н	Н	Н	L	Low
_			н	PBI	VRI		-	Н	Н	Н	Н	Н	Н	Mute
5	INPUT SELECT 2	Н	L	RPI	VRI	DAC 5	Er	ters t	he m	uting	state	when	all bit	s are set high.
_	DEC/DE	Н	Playback mod	e selection		R/L	Н	R-	chan	nel re	gister	selec	tion	
6	REC/PB	REC/PB L	Recording mod	de selection		SELECT	L L-channel register selection							
7	REGISTER SELECT	Н	Control register selection REGISTER SELECT L Volume register selection											

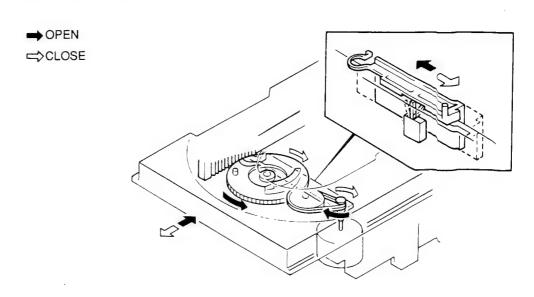
Whether to control the condition or volume control is judged by the content of bit 7.

MECHANISM DESCRIPTION



1. OPEN/CLOSE Function

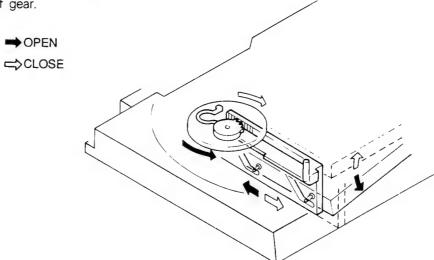
By the rotation of the loading motor, Gear is rotate and the tray starts OPEN/CLOSE operation. The OPEN/CLOSE operation stops when the slide gear travels or open /close switch comes ON.



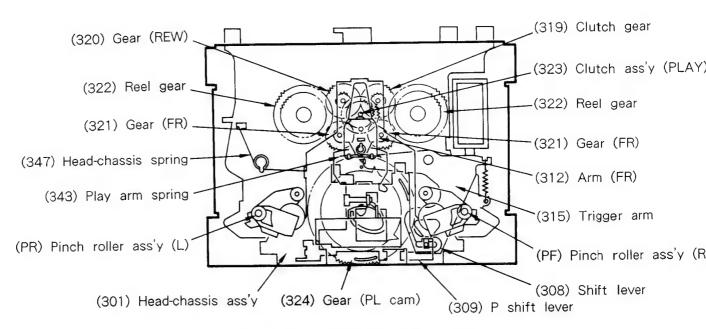
MECHANISM DESCRIPTION

2. Pickup Chassis Traveling

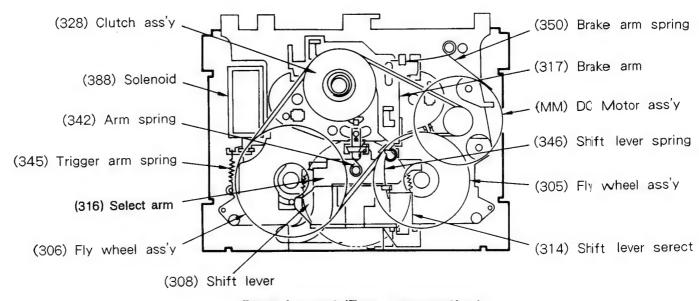
Accompanied with the OPEN/CLOSE operation, the slider-gear moves by rotation of gear.



MECHANISM DESCRIPTION

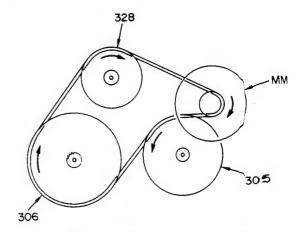


Parts Layout (Front perspective)



Parts Layout (Rear perspective)

Driving power : 130g-cm more
Take up torque : 35~75g-cm
FF/REW torque : 70~160g-cm
Back tension torque : 1.5~6g-cm



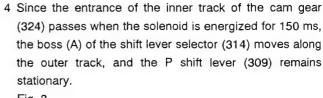
Transmission of Rotation

MECHANISM DESCRIPTION

STOP \rightarrow FWD PLAY / REC (The head is in the forward – transport position when the drive stops.)

- 1 Press the key. The CPM starts running.
- 2 After about 300 ms, the solenoid is energized for 150 ms, and the boss(A) of the trigger arm (315) disengages from the stop lever (B) of the play cam gear (314).

 Fig. 1





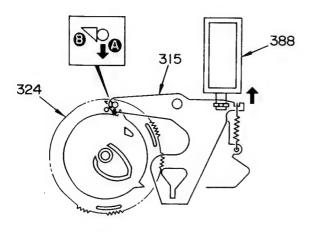


Fig. 1

3 The head chassis assembly (301) is pushed down by the spring (347), so the cam gear (324) rotates slightly, the FW (R) gear engages with the cam gear, and the gears start rotating.

Fig. 2

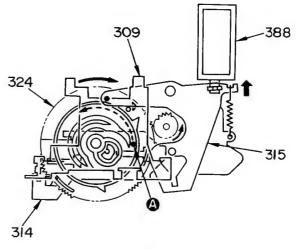


Fig. 3

5 The shift lever is lifted by the cam of the play cam gear, and the head chassis assembly is raised.

Fig. 4

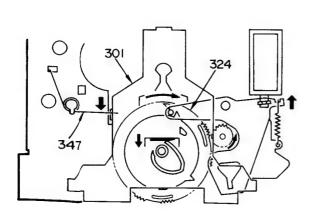


Fig. 2

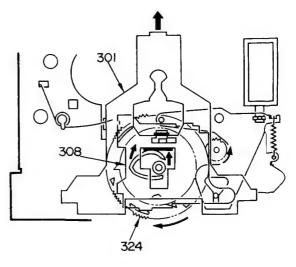


Fig. 4

MECHANISM DESCRIPTION

6 The play arm spring is pushed by the projection (A) of the P shift lever (309), the play arm tilts along the inner surface of the head chassis, the clutch gear (323), play clutchgear, and hub engage, and the hub starts roting.

Fig. 5

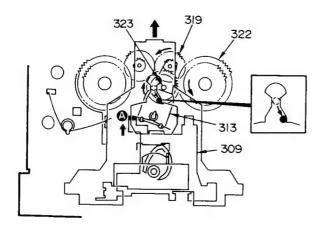


Fig. 5

7 The brake arm (317) is lifted by the bent part (A) of the shift lever (308) to release the brake from the hub.

Fig. 6

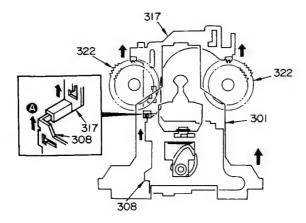


Fig. 6

8 The forward – transport pinch roller (PR) is raised by the bent part (A) of the P shift lever (309), and the pinch roller contacts the capstan.

Fig. 7

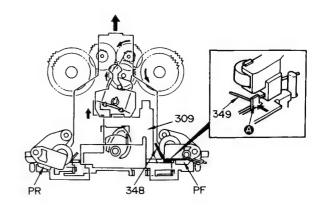
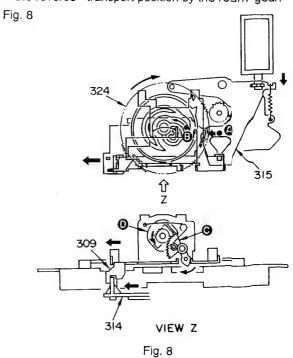


Fig. 7

STOP → RVS PLAY / REC

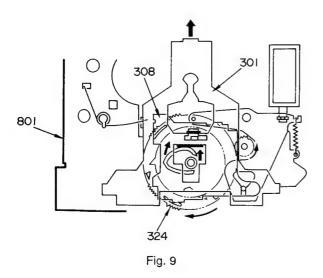
- 1 Press the key.
- 2 After about 300 ms, the solenoid is energized for 50 ms, then deenergized.
- 3 Since the solenoid is deenergized immediately after the play cam gear starts turning, the boss (A) of the shift lever selector (314), pushed by the trigger arm boss (B), passes along the inner track of the play cam gear, so the play shift lever also moves, and the head is rotated into the reverse—transport position by the return gear.



MECHANISM DESCRIPTION

4 The rotating play cam gear lifts the shift lever (308), raising the head chassis assembly.

Fig. 9



5 The play arm spring pushed by the projection (A) of the P shift lever (309), the play arm tilts along the inner surface of the head chassis, the REW gear (320), play clutch gear, and hub engage, and the hub starts rotating. Fig. 10

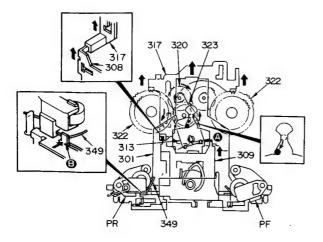


Fig. 10

- 6 The brake arm (317) is lifted by the shift lever to release the brake from the hub.
- 7 The spring of the reverse transport pinch roller (PR) is lifted by the bent part (B) of the P shift lever (309), and the pinch roller contacts the capstan.

STOP → FF

- 1 Press the ▶▶ key.
- 2 After 300 ms, the solenoid is energized for 300 ms, the selector arm (316) is held by the concave part of the trigger arm (315), and the head chassis assembly starts rising.

Fig. 11

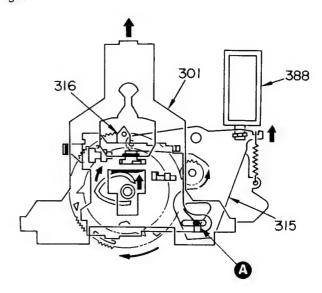


Fig. 11

3 The boss (A) of the selector arm (316) enters the holder on the mechanism base, and is lifted further.

Fig. 12

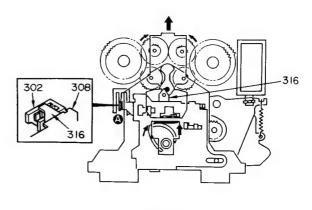
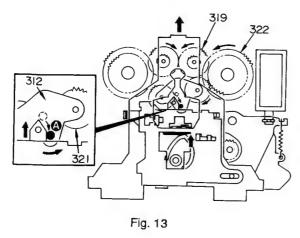


Fig. 12

MECHANISM DESCRIPTION

4 The projection of the selector arm touches the boss (A) of the FR arm (312), the selector arm rises and tilts, the FR gear (321), clutch gear, and hub gear engage, and the forward – transport take – up hub starts rotating.

Fig. 13



5 When the shift lever is raised to its limit by the play cam gear cam, the selector arm hole and head chassis assembly boss (A) are positioned as shown in the figure below. The pinch roller does not touch the capstan, and fast – forwarding takes place.

Fig. 14

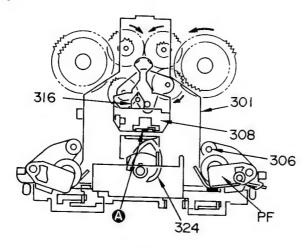


Fig. 14

STOP → RWD

- 1 Press the 44 key.
- 2 After 300 ms, the solenoid is energized for 450 ms, the select arm (316) is held by the concave part of the trigger arm, and the head chassis assembly starts rising.
- 3 Since the selector arm is held, the projection touches the FR arm boss, the FR arm tilts, the FR gear (321), REW gear, and hub gear engage, and the RWD hub starts rotating.

Fig. 15

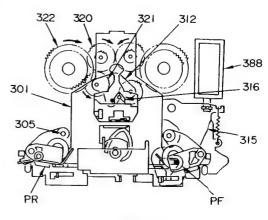


Fig. 15

4 The pinch roller does not touch the capstan, as in the fast forward operation, and rewinding takes place.

F PLAY → CUE / RWD

- 1 The solenoid is energized for 50 ms during forward play to stop operation.
- 2 The solenoid is energized for 300 ms to fast forward (CUE).

The solenoid is energized for 50 ms to rewind (RWD).

R. PLAY → RWD

- 1 The solenoid is energized for 50 ms during reverse play to stop operation.
- 2 The solenoid is energized for 50 ms to rotate the head into the reverse transport position.
- 3 The solenoid is deenergized for 100 ms, then reenergized for 150 ms to fast forward.

RVS PLAY → CUE

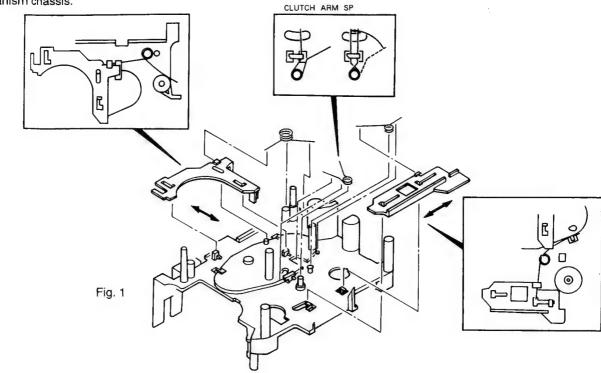
- 1 The solenoid is energized for 50 ms during reverse play to stop operation.
- 2 The solenoid is energized for 50 ms to rot ate the head into the reverse − transport position.
- 3 The solenoid is deenergized for 100 ms, then reenergizend for 300 ms to rewind.

MECHANISM DESCRIPTIO

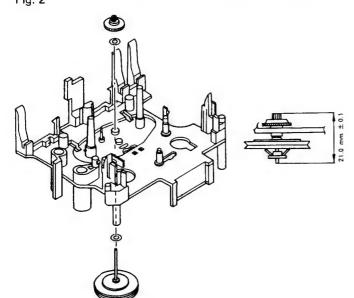
Assembly procedure

1 Install the brake arm and its spring the clutch arm spring, and the shift lever selector, and the spring on the mechanism chassis.

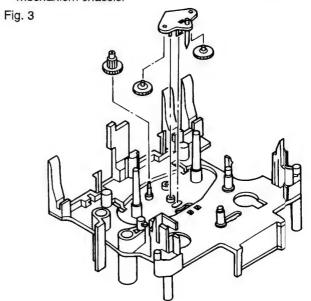
Fig. 1



2 Install the clutch assembly on the mechanism chassis. Fig. 2



3 Install the FR arm, FR gear, and REW gear on the mechanism chassis.



MECHANISM DESCRIPTION

4 Install the hub assemblies (left and right).

Fig. 4

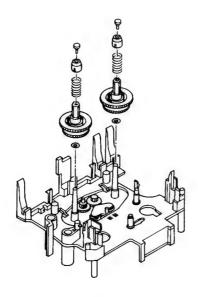
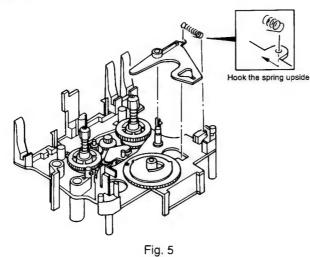


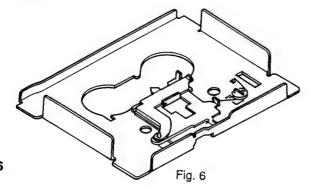
Fig. 4

5 Install the play cam gear and the trigger arm and its spring on the mechanism chassis.

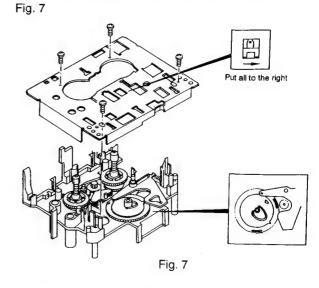
Fig. 5



6 Install the shift assembly and its sporing on the chassis. Fig. 6



7 Install the chassis on the mechanism chassis.



8 Install the housings (left and right) on the chassis. Fig. 8

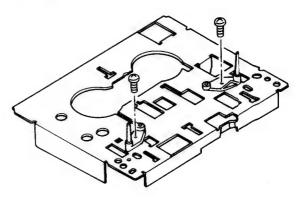
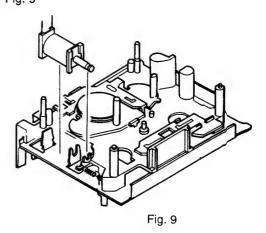


Fig. 8

9 Install the solenoid on the mechnism chassis. Fig. 9



10 Install the flywheels (left and right).

MECHANISM DESCRIPTION

11 Install the play arm, play clutch assembly, and the head chassis and its spring on the chassis, and install the cassette guide.

Fig. 10

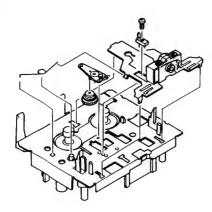
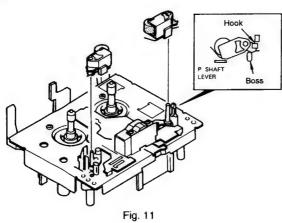


Fig. 10

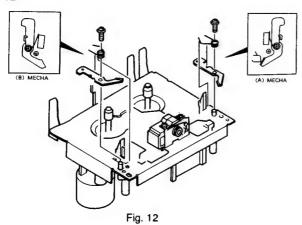
12 Install the pinch rollers (left and right).

Fig. 11



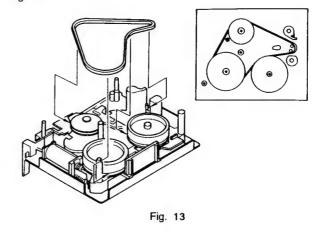
13 Install the interlock and its spring.

Fig. 12



14 Install the belt temporarily

Fig. 13



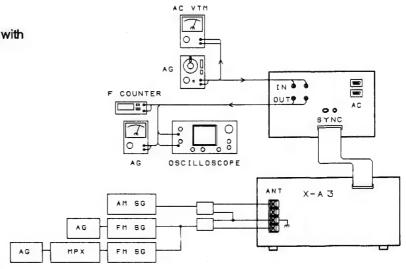
15 Install the PCB.

16 Install the CPM and belt.

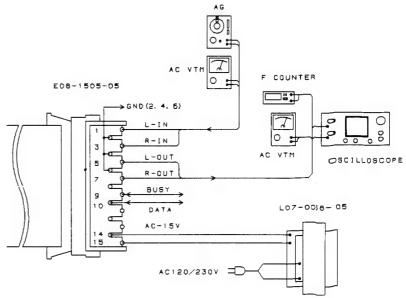
ADJUSTMENT

Preparation for adjustment F COUNTER (1) When using the A-A3. OSCILLOSCOPE Apply the signal generator (AG) OUTPUT to the 0 AUX INPUT of the A-A3. 00 0 : AC VTM A-A3 **=**○**=** AC <u>ه</u>ا **皮皮皮皮** SP OUT ANT X-A3 AM SG A G FM SG

(2)When RM-90PS is used
Use the PLAY/REC socket in the same way as with
the normal deck.



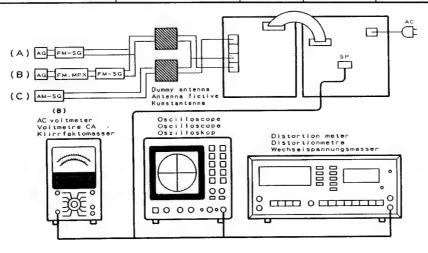
(3)Apply 15 VAC and each signal with a 15-pin connector.



ADJUSTMENT

X-A3 . A3L

		INPUT	OUTPUT	TUNER	ALIGNMENT		
No	ITEM	SETTINGS	SETTINGS	SETTINGS	POINTS	ALIGN FOR	FIG
	FM SECTION		SELECTOR:FM				
1	DISCRIMINATOR	(A) 98.0MHz 1kHz, ± 75kHz dev (M,X type) 1kHz, ± 40kHz dev (E,T type) 60dB μ (ANT input)	Connect a DC voltmeter between TP3 and TP4. (X05-)	MONO 98.0MHz	L3	ov	(a)
2	DISTORTION (STEREO)	(C) 98.0MHz 1kHz,±68.25kHz dev Pilot:±7.5kHz dev (M,X type) 1kHz,±40kHz dev Pilot:±6kHz dev (E,T type) 60dB µ (ANT input)	(B) or P58 (1-3)	AUTO 98.0MHz	L1 IFT (W02-)	Minimum distortion	
3	SEPARATION (E,T type only)	(C) 98.0MHz 1kHz,±40kHz dev Pilot ±6kHz dev Selector:L or R 60dBµ (ANT input)	(B) or P58 (1-3)	AUTO 98.0MHz	VR3 (X05-)	Minimum crosstalk	
4	TUNING LEVEL	(A) 98.0MHz 1kHz, ± 75kHz dev (M,X type) 1kHz, ± 45kHz dev (E,T type) 14dB μ (ANT input)75 Ω	(B) or P58 (1-3)	AUTO or MONO 98.0MHz	VR1 (X05-)	Adjust VR1 and stop at the point where ED1(TUNED)goes on.	
	AM(MW) SECTION	DN .	SELECTOR:AM(MW)	1	1		1
(1)	TUNING LEVEL	(D) 990kHz 400Hz,30% mod 26dB µ (ANT input)	(B) or P58 (1-3)	990kHz	(X05-) VR2	Adjust VR2 and stop at the point where ED1(TUNED) goes on.	



REGLAGE

X-A3 . A3L

		REGLAGE DE	REGLAGE DE	REGLAGE	POINTS DE		
Ν.	ITEM	L'ENTREE	LA SORTIE	DU TUNER	L'ALIGNE- MENT	ALIGNER POUR	FIG
	SECTION FM			SELECTEUR	:FM		
1	DISCRIMI- NATEUR	(A) 98,0MHz 1kHz, ±75kHz dV (Type M, X) 1kHz, ±40kHz dV (Type E, T) 60dB µ (Entrée ANT)	Connecter un voltmètre CC entre TP3 et TP4. (X05-)	MONO 98,0MHz	L3	ov	(a)
2	DISTORSION (STEREO)	(C) 98,0MHz 1kHz, ±68,25kHz dV Pilote:±7,5kHz dV (Type M, X) 1kHz, ±40kHz dV Pilote:±6kHz dV (Type E, T) 60dB μ (Entrée ANT)	(B) où P58 (1-3)	AUTO 98,0MHz	L1 IFT (W02-)	Distorsion minimum	
3	SEPARATION (Type E, T seulement)	(C) 98,0MHz 1kHz, ± 40kHz dV Pilote:±6kHz dV Sélecteur: G ou D 60dB μ (Entrée ANT)	(B) où P58 (1-3)	AUTO 98,0MHz	VR3 (X05-)	Diaphonie minimum	
4	NIVEAU D'ACCORD	(A) 98,0MHz 1kHz, ± 75kHz dV (Type M, X) 1kHz, ± 45kHz dV (Type E, T) 14dB μ (Entrée ANT)75Ω 18dB μ (Entrée ANT)300Ω	(B) où P58 (1-3)	AUTO ou MONO 98,0MHz	VR1 (X05-)	Régler VR1 et arrêter au point où ED1/ACCORDE) s'allume.	
	SECTION AM(O	M)	SELECTEUR:AM(OM)				
(1)	NIVEAU D'ACCORD	(D) 990kHz 400Hz, 30% mod 26dB µ (Entrée ANT)	(B) où P58 (1-3)	990kHz	(X05-) VR2	Ajuster VR2 et arêter au point où ED1(ACCORDE) s'allume.	

ABGLEICH

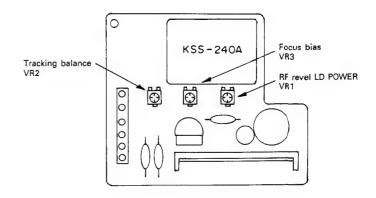
X-A3 . A3L

Nr.	GEGENSTAND	EINGANGSEINSTEL- LUNG	AUSGANGSEIN- STELLUNG	TUNER-EIN- STELLUNG	ABGLEICH- PUNKTE	ABGLEICHEN FÜR	ABB.
	UKW-TEIL		WÄHLER:FM				
1	DEMODULATOR	(A) 98,0MHz 1kHz, ± 75kHz Abw. (M-, X-Typ) 1kHz, ± 40kHz Abw. (E-, T-Typ) 60dB μ (ANT-Eingang)	Einen Gleichspan- nungsmesser zwis- chen TP3 und TP4 schließen. (X05-)	MONO 98,0MHz	L3	OV	(a)
2	VERZERRUNG (STEREO)	(C) 98,0MHz 1kHz, ± 68,25kHz Abw. Pilot: ± 7,5kHz Abw. (M-, X-Typ) 1kHz, ± 40kHz Abw. Pilot: ± 6kHz Abw. (E-, T-Typ) 60dB \(\(\text{ANT-Eingang} \)	(B) oder P58 (1-3)	AUTO 98,0MHz	L1 IFT (W02-)	Minimale Verzerrungen	
3	TRENNUNG (Nur E-, T-Typ)	(C) 98,0MHz 1kHz, ± 40kHz Abw. Pilot:±6kHz Abw. Wähler:L oder R 60dB μ (ANT-Eingang)	(B) oder P58 (1-3)	AUTO 98,0MHz	VR3 (X05-)	Minimales Übersprechen	
4	ABSTIMMPEGEL	(A) 98,0MHz 1kHz, ±75kHz Abw. (M-, X-Typ) 1kHz, ±45kHz Abw. (E-, T-Typ) 14dBμ (ANT-Eingang)75Ω 18dBμ (ANT-Eingang)300Ω	(B) oder P58 (1-3)	AUTO oder MONO 98,0MHz	VR1 (X05-)	VR1 auf die Position einstellen, in der ED1(TUNED) aufleuchtet	
	AM-(MW-) TELL		WÄHLER:AM(MW)	1			
(1)	ABSTIMMPEGEL	(D) 990kHz 400Hz, 30% Mod 26dB μ (ANT-Eingang)	(B) oder P58 (1-3)	990kHz	(X05-) VR2	VR2 auf die Position einstellen, in der ED1(TUNED)aufleuche t.	

ADJUSTMENT

CAUTION

Pickup (KSS-240A) is adjustment free in repairing, please don't disassemble and adjust it.



		INPUT	OUTPUT	PLAYER	ALIGNMENT		
No.	ITEM	SETTING	SETTING	SETTING	POINT	ALIGN FOR	FIG.
1	LASER POWER ※		Apply the sensor section of the optical power meter on the pickup lens.	Short-circuit pins TEST and enter the test mode. Press the play key to check that the LD emits light. Then,confirm that the display is "05"		On the power from 0.08 to 0.15mW, when the diffraction grating is correctly a ligned with the RF level of 1.5Vp-p or more aid the TE (servo open) level of 1.5Vp-p or more, the pickup is acceptable.	(a)
2	FOCUS GAIN	Test disc Type 4 Apply signal of 1.0kHz, 0.1Vrms to CN3 pin 2 and 3.	Connect a LPF to CN3 pin 2-3, to which connect an oscilloscope or two AC voltmeters.	Press the PLAY key. Confirm that the display is "05".	FOCUS GAIN VR2	Two VTVMs should read the samevalue.	(ь)
3	TRACKING GAIN	Test disc Type 4 Apply signal of 1.0kHz, 0.1Yrms to CN3 pin 5 and 6.	Connect a LPF to CN3 pin 5-6, to which connect an oscilloscope or two AC voltmeters.	Press the PLAY key. Confirm that the display is "05".	TRACKING GAIN VR1	Two VTVMs should read the should value.	(ъ)

(NOTE) Type 4 disc : SONY YEDS-18 TEST DISC or equivalent.

LPF: around 47kohms+390pF or so.
Adjustment proedures are in TEST MODE.

(a) Laser Power

(b) Focus Gain and Tracking Gain



REGLAGE

N·	ITEM	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DU LECTEUR	POINTS DE L'ALIGNEMENT	ALIGNER POUR	FIG.
1	ALIMENTATION LASER **	_	Appliquer la section de capteur du compteur d'alimentation optique sur l'optique de lecture.	Court-circuiter les broches TEST et passer dans le mode d'essai. Appuyer sur la touche de lecture pour vérifier que le LD émet de la lumière. Ensuite, vérifier que l'affichage est "05".	_	Sur l'alimentation de 0,08 à 0,15mV lorsque la grille de diffraction est correctement alignée avec le niveau RF de 1,5Vc-c ou plus et le niveau TE (servo ouvert) de 1,5Vc-c ou plus, la lecture est acceptable.	(a)
2	GAIN DE FOCUS	Disque d'essai Type 4 Appliquer un signal de 1,0kHz,0,1Vrms aux broches CN3 2 et 3.	Connecter un LPF aux broches CN3 2 et 3 auxquelles connecter un oscilloscope ou deux voltmètres AC.	Appuyer sur la touche de lecture. Vérifier que l'affichage est "05".	GAIN DE FOCUS VR2	Deux VTVM doivent indiquer la même valeur.	(b)
3	GAIN D'ALIGNE- MENT	Disque d'essai Type 4 Appliquer un signal de 1,0kHz, 0,1 Vrms aux broches CN3 5 et 6.	Connecter un LPF aux broches CN3 5 et 6 auxquelles con- necter un oscilloscope ou deux voltmètres AC.	Appuyer sur la touche de lecture. Vérifier que l'affichage est "05".	GAIN D'ALIGNE- MENT VR1	Deux VTVM doivent indiquer la meme valeur.	(b)

(NOTE) Disque type 4:DISQUE D'ESSAI SONY YEDS-18 ou équivalent.

LPF:environ 47 kohms+390 pF à peu près.

Les procédures de réglage se font dans le MODE D'ESSAI.

ABGLEICH

Nr.	GEGENSTAND	EINGANGSEIN- STELLUNG	AUSGANGSEIN- STELLUNG	PLAYER-EIN- STELLUNG	ABGLEICH- PUNKT	ABGLEICHEN FÜR	ABB.
1	LASER-LEISTUNG ※	_	Den Sensorabschnitt des Optikleistungsme- ters am Abtaster ansetzen.	TEST-Pins Kurzsch- ließen und auf Test-Modus schalten. Die Wiedergabe- taste drücken,um sicherzustellen,daß LD Licht ausstrahlt. Dann sicherstellen,daß "05" angezeigt wird.	—	Wenn das optische Gitter richtig auf den HF-Pegel von 1,5Vss oder höher und den TE-Pegel(Servo auf) von 1,5Vss oder höher abgeglichen ist, ist der Abtaster bei einer Leistung zwischen 0,08 und 0,15 W in Ordnung.	(a)
2	FOKUSVER- STÄRKUNG	Test-Disc Typ 4 Ein Signal mit 1,0kHz, 0,1Vrms CN3-Pin 2 und 3 zuführen.	LPF an CN3-Pin 2-3 anschließen, woran ein Oszilloskop oder zwei Wechselspan- nungsmesser anges- chlossen werden.	Die PLAY-Taste drücken. Sicherstellen, daß "05" angezeigt ist.	FOKUSVER- STÄRKUNG VR2	Zwei VTVMs sollen denselben Wert anzeigen.	(b)
3	TRACKING- VERSTÄRKUNG	Test-Disc Typ 4 Ein signal mit 1,0kHz, 0,1 Vrms CN3-Pin 5 und 6 zufuhren.	LPF an CN3-Pin 5-6 anschließen, woran ein Oszilloskop oder zwei Wechselspan- nungsmesser angeschlossen Werden.	Die PLAY-Taste drücken. Sicherstellen,daß "05" angezeigt ist.	TRACKING- VERSTÄRKUNG VR1	Zwei VTVMs sollen den Sollwert anzeigen.	(b)

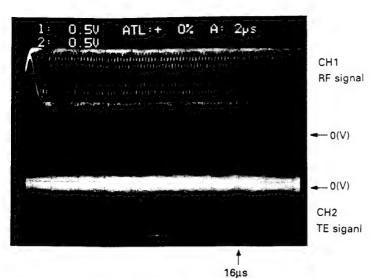
(HINWEIS)

Disc Typ 4:SONY YEDS-18-TEST-DISC oder gleichwertige.

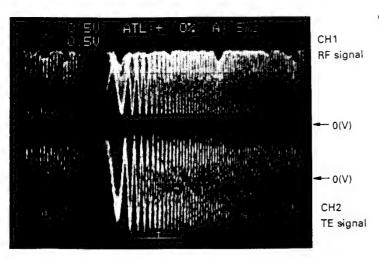
LPF:ca, 47Kohm+390 pF.

Einstellungen erfolgen im TEST-MODUS.

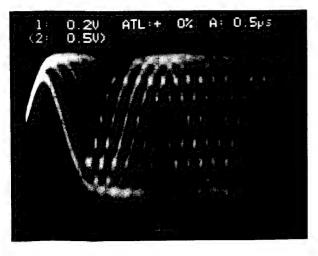
ADJUSTMENT



- RF signal and E.Spot signal in test mode (PLAY).
- If the diffraction grating has been adjusted properly, the influence of triggering is observed on the E.Spot waveform of approx. 16μs after RF signal, in the form of a projection.



 RF signal and T.Error signal, in test mode (Focusing ON). (Disc type 4)



• RF signal in test mode (PLAY).

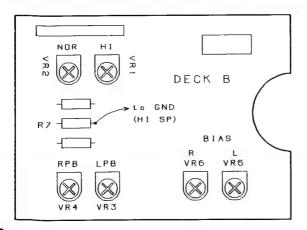
RF signal

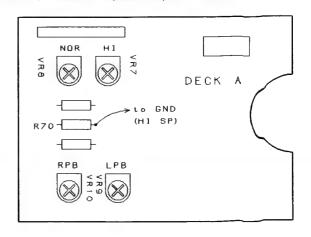
ADJUSTMENT

CSSSETTE DECK

NO.	ITEM	INPUT SETTINGS	OUTPUT SETTINGS	CASSETTE TAPE DECK SETTINGS	ALIGNMENT POINTS	ALIGN FOR	FIG.
CAS	SSETTE DECK SECTIO	N TAPE:NORMAL,	DOLBY:OFF,	INPUT:LINE,OR AUX	<		
1	REC/PLAY HEAD						
[1]	DEMAGNETIZATION	_	_	POWER:OFF	REC/PLAY head	Demagnetize the REC/PLAY head with a head demagnetizer.	
[2]	CLEANING	_		PLAY	REC/PLAY head erase head, capstan, pinch roller.	Clean the REC/PLAY head erase head,capstan and pinch roller using a cotton swab slightly damped with alchol.	
[3]	AZIMUTH	SCC1727 MTT-114, TCC-153 10kHz, -10dB	P58 (1~3)	PLAY	Azimuth adjustment screw	Maximum output.	
F	C board adjustment(X2	28-2450)					
<1>	TAPE SPEED (HI SPEED)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	Connect R70 and GND (DECK A) or · R7 and GND (DECK B) PLAY	DECK A: VR 7 DECK B: VR 1	Adjust the tape speed so that a 3kHz signal is produced at the center of the tape.	
⟨2⟩	TAPE SPEED (NORMAL)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	Disconnect. PLAY.	DECK A : VR8 DECK B : VR2	Adjust the lape speed so that a 3kHz signal is produced at the center of 'the tape.	
⟨3⟩	PLAYBACK **	MTT-256U TCC-160 315Hz (0dB) MTT-256 SCC1727	P58 (1~3)	PLAY	A VR9 (L) VR10 (R) B VR3 (L)	Output level : -5.0dBm	
		315Hz (-4dB)			VR4 (R)	Output leve : -9.0dBm	
〈4〉	BIAS CURRENT **	(A) 1kHz,-28dBm 10kHz,-28dBm (-28dBm=30mV)	P58 (1~3)	Adjust AG output so that the SPEAKER output becomes -28dBm at 1kHz, then record and reproduce signal of 1kHz and 10kHz in alternation.	VR 5 (L) VR 6 (R)	Record 1kHz and 10kHz in alternation and adjust the variable resistors which control the lias current so that the same playback level is obtained.	

^{* *} Set VOLUME knob to the condition NB:MIN. GE EFFECT:OFF, AUX input:200mV, speaker output 200mV/8 Q.





REGLAGE

MAGNETOPHONE A CASSETTE

N.	ITEM	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DU MAGNETOPHONE A CASSETTE	POINTS DE L'ALIGNE- MENT	ALIGNER POUR	FIG.
SE	CTION MAGNETOPHO	NE A CASSETTE B	ANDE:NORMAL	DOLBY:OFF INPUT	LINE OU AUX		
1.T	ETE D'ENREGISTREM	ENT/LECTURE					
[1]	DEMAGNETISATION	_	_	ALIMENTATION COUPEE	Tête d'enre- gistrement/ lecture	Démagnetiser la tete d'enre- gistrement/lecture avec l'effaceur de tête.	
[2]	NETTOYAGE	_	-	PLAY	Tête d'enre- gistrement/ lecture, tête d'effacement, cabestan, galet presseur	Nettoyer la tête d'enregistre- ment/lecture, la tête d'effacement, la cabestan et le galet presseur avec un coton-tige légèrement trempé de l'alcool.	
[3]	AZIMUT	SCC1727 MTT-114, TCC-153 10kHz, -10dB	P58 (1~3)	PLAY	Vis d'ajustement de l'azimut	Puissance maximum	
II A	justement de la plaque	te de circuits imprimé	s(X28-2450)				
<1>	VITESSE DE LA BANDE (GRANDE VITESSE)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	Connecter R70 et GND(PLATINE A) ou R7 et GND (PLATINE B) PLAY	PLATINE A:VR7 PLATINE B:VR1	Ajuster la vitesse de la bande pour qu'un signal de 3kHz soit produit au centre de la bande.	
(2)	VITESSE DE LA BANDE(NORMALE)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	Déconnecté. PLAY.	PLATINE A:VR8 PLATINE B:VR2	Ajuster la vitesse de la bande de sorte qu'un signal de 3kHz soit produit au centre de la bande.	
⟨3⟩	NIVEAU DE	MTT-256U TCC-160 315Hz (0dB)	P58	PLAY	A VR9 (L) VR10 (R)	Niveau de sortie:-5,0dBm	
(3)	LECTURE**	MTT-256 SCC1727 315Hz (-4dB)	(1~3)	FLAT	B VR3 (L) VR4 (R)	Niveau de sortie:-9,0dBm	
〈 4〉	COURANT DE POLARISATION**	(A) 1kHz,-28dBm 10kHz,-28dBm (-28dBm≃30mV)	P58 (1~3)	Ajuster la sortie de AG de sorte que a sortie de HAUT-PIARLEUR deviennent -28 dBm a 1kHz, puis enregistrer et reproduire un signal de 1kHz et 10kHz alternativement.	VR 5 (L) VR 6 (R)	Enregistrer alternativement 1kHz et 10kHz et ajuster les résistances variables qui contrôlent le courant de polarisation de sorte que le même niveau de lecture soit obtenu.	

^{* *} Régler le bouton VOLUME à la condition. NB:MIN GE EFFECT:OFF, éntree AUX:200mV, sortie haut-parleur 200 mV/8Ω

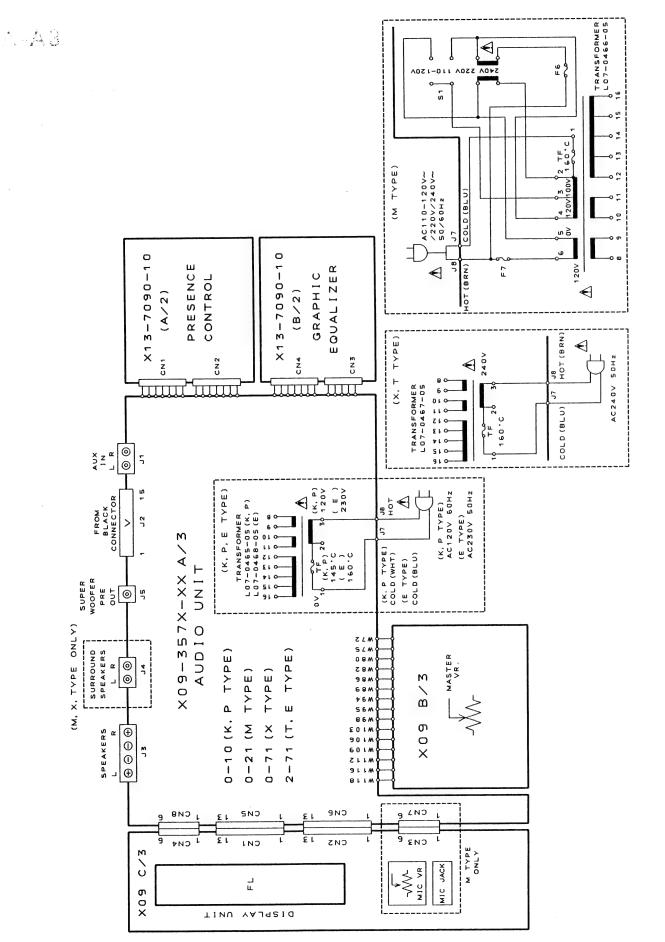
ABGLEICH

CASSETTENDECK

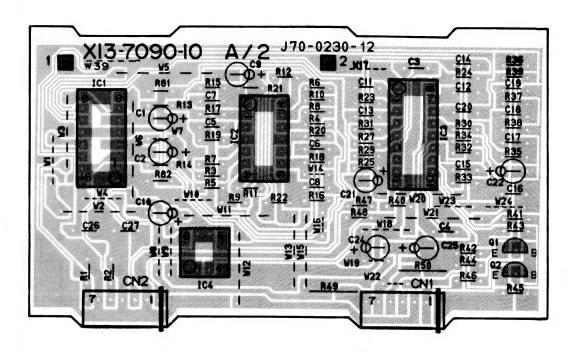
Nr.	GEGENSTAND	EINGANGSEIN- STELLUNG	AUSGANGS- EINSTEL- LUNG	CASSETTEN- DECK-EINSTEL- LUNG	ABGLEICH- PUNKTE	ABGLEICHEN FUR	ABB.
CAS	SSETTENDECK-TEIL	BAND:NORMAL, DO	DLBY:OFF, EIN	GANG:LINE ODER AU	X		
1.A	UFNAHMEWIEDERGA	BEKOPF					
[1]	ENTMAGNETISI- ERUNG		_	POWER:OFF	Aufnahme/ Wiedergabekopf (REC/PLAY)	Den REC/PLAY-Kopf mit einem Tonkopf-Entmagnetisierer entmagnetisieren.	
[2]	REINIGUNG	_	_	PLAY	REC/PLAY- Kopf, Löschkopf, Tonwelle und Andruckrolle.	REC/PLAY- Kopf, Löschkopf,Tonwelle und Andruckrolle mit einem leicht mit Alkohol angefeuchteten Wattestäbchen reinigen.	
[3]	AZIMUT	SCC1727 MTT-114, TCC-153 10kHz, -10dB	P58 (1~3)	PLAY	Azimut-Ein- stellschraube	Höchstleistung.	
P	latinen-Einstellung(X28	-2450)					
(1)	BANDGESCHWIN- DIGKEIT (SCHNELL)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	R70 und GND (DECK A) oder R7 und GND(DECK B) verbinden PLAY	DECK A:VR7 DECK B:VR1	Die Bandgeschwindigkeit so einstellen, daßein 3-kHz- Signal in der Mitte des Bands erzeugt wird.	
〈2 〉	BANDGESCHWIN- DIGKEIT (NORMAL)	SCC1727 MTT-111, TCC-110 3kHz -4dB	P58 (1~3)	Abtrennen. PLAY.	DECK A:VR8 DECK B:VR2	Die Bandgeschwindigkeit so einstellen, daßein 3-kHz- Signal in der Mitte des Bands erzeugt wird.	
40 \	WIEDERGA-	MTT-256U TCC-160 315Hz (0 dB)	P58	PLAY	A VR9 (L) VR10 (R)	Ausgangspegel:-5,0dBm	
⟨3⟩	BEPEGEL**	MTT-256 SCC1727 315Hz (-4 dB)	(1~3)	PLAT	B VR3 (L) VR4 (R)	Ausgangspegel:-9,0dBm	
4	VORMAGNETISI- ERUNGSSTROM* *	(A) 1kHz, -28dBm 10kHz, -28dBm (-28dBm=30mV)	P58 (1~3)	Den AG-Ausgang so einstellen, daß der SPEAKER-Ausgang bei 1kHz -28 dBm wird, dann abwechselnd ein Signal mit 1kHz und 10kHz aufzeichnen und reproduzieren.	VR 5 (L) VR 6 (R)	Abwechselnd 1kHz und 10kHz aufzeichnen und die Vormagnetisierung sstrom- Stellwiderstände so einstellen, daß derselbe Wiederga- bepegel erhaten wird.	

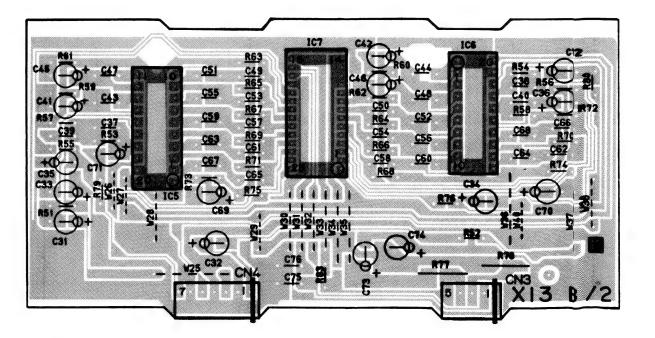
^{**}Den VOLUME-Knopf entsprechend einstellen NB:MIN, GE EFFECT:OFF, AUX-Eingang:200mv, Lautsprecher-Ausgang 200mV/80

WIRING DIAGRAM

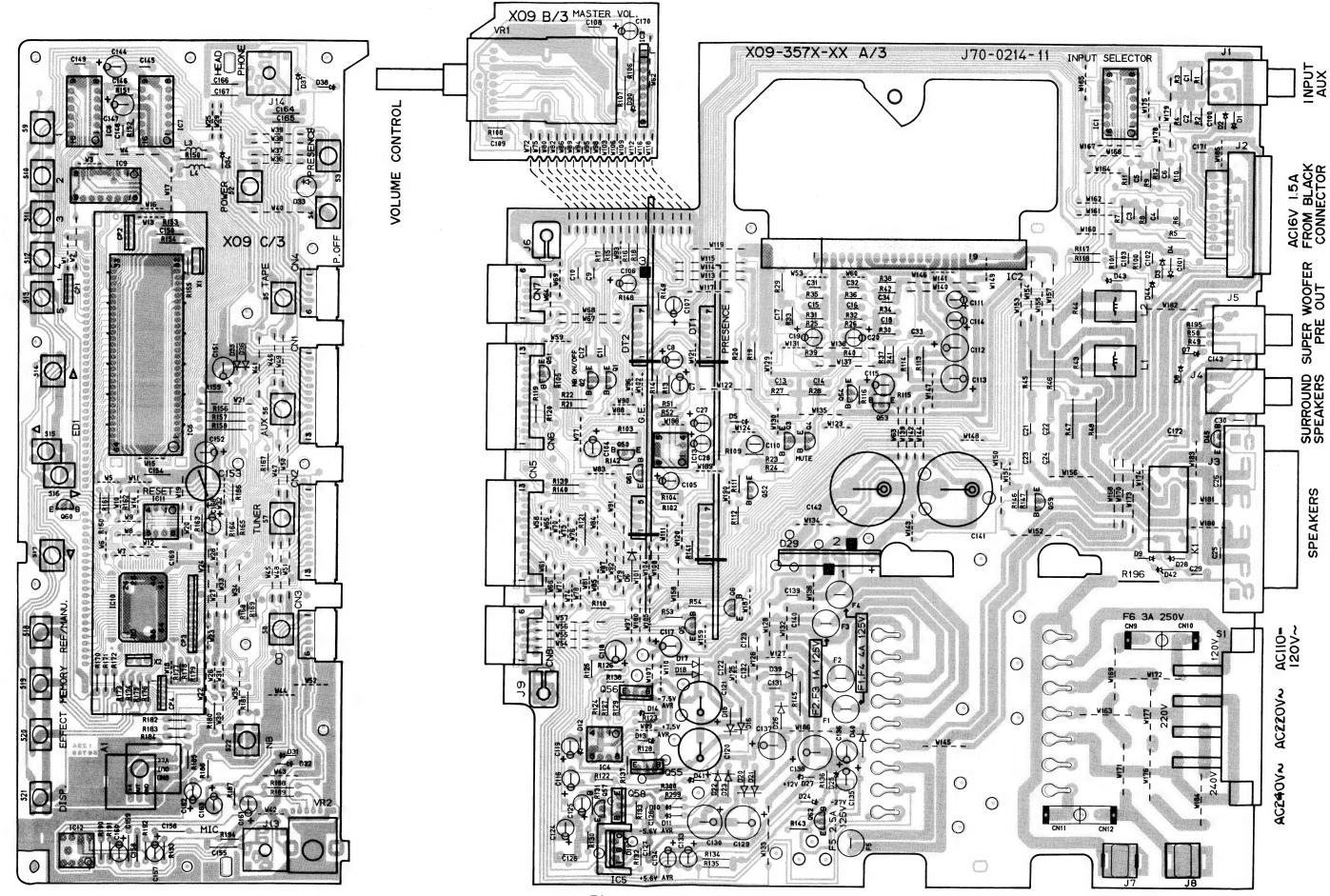


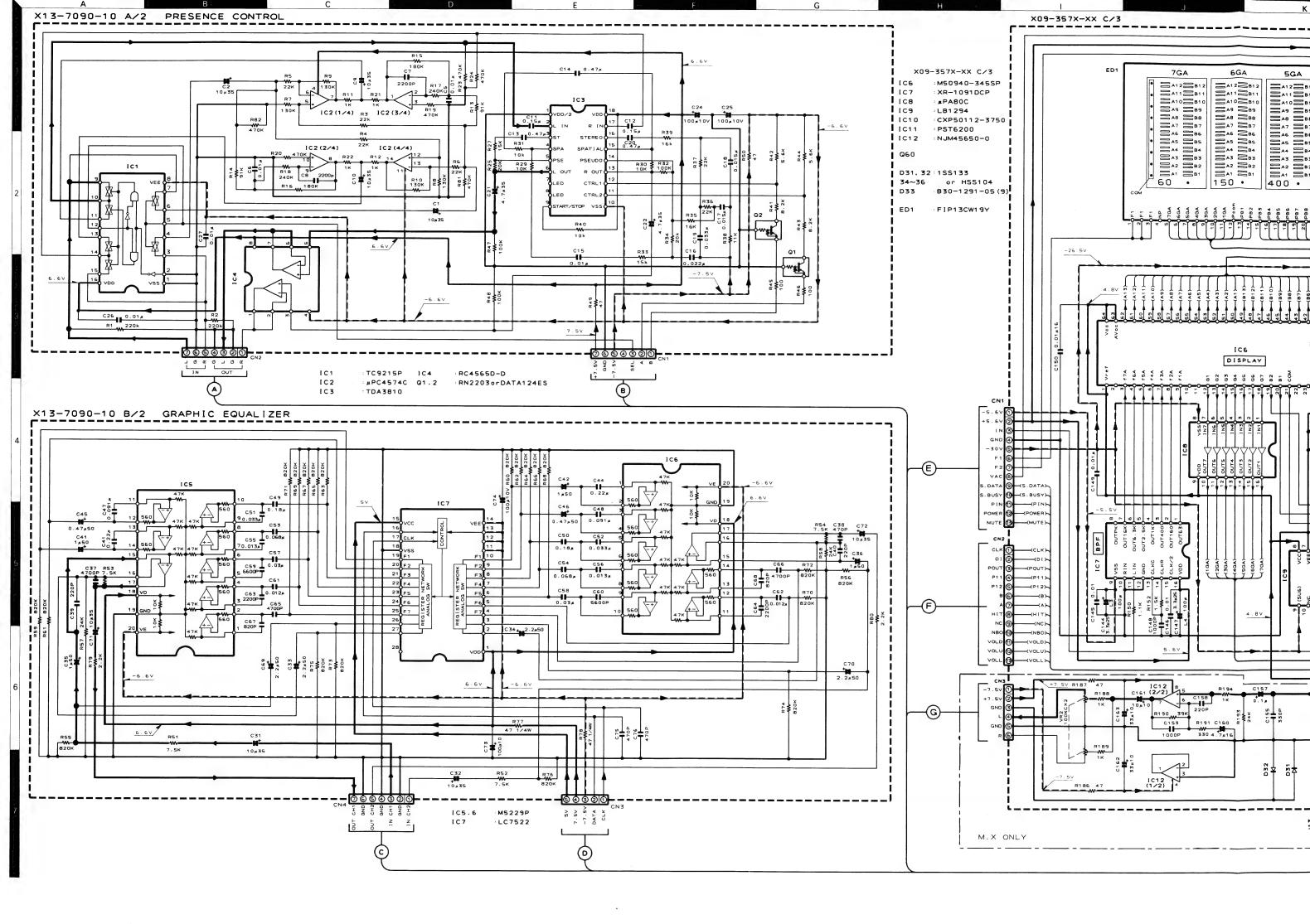
PC BOARD (Component side view)

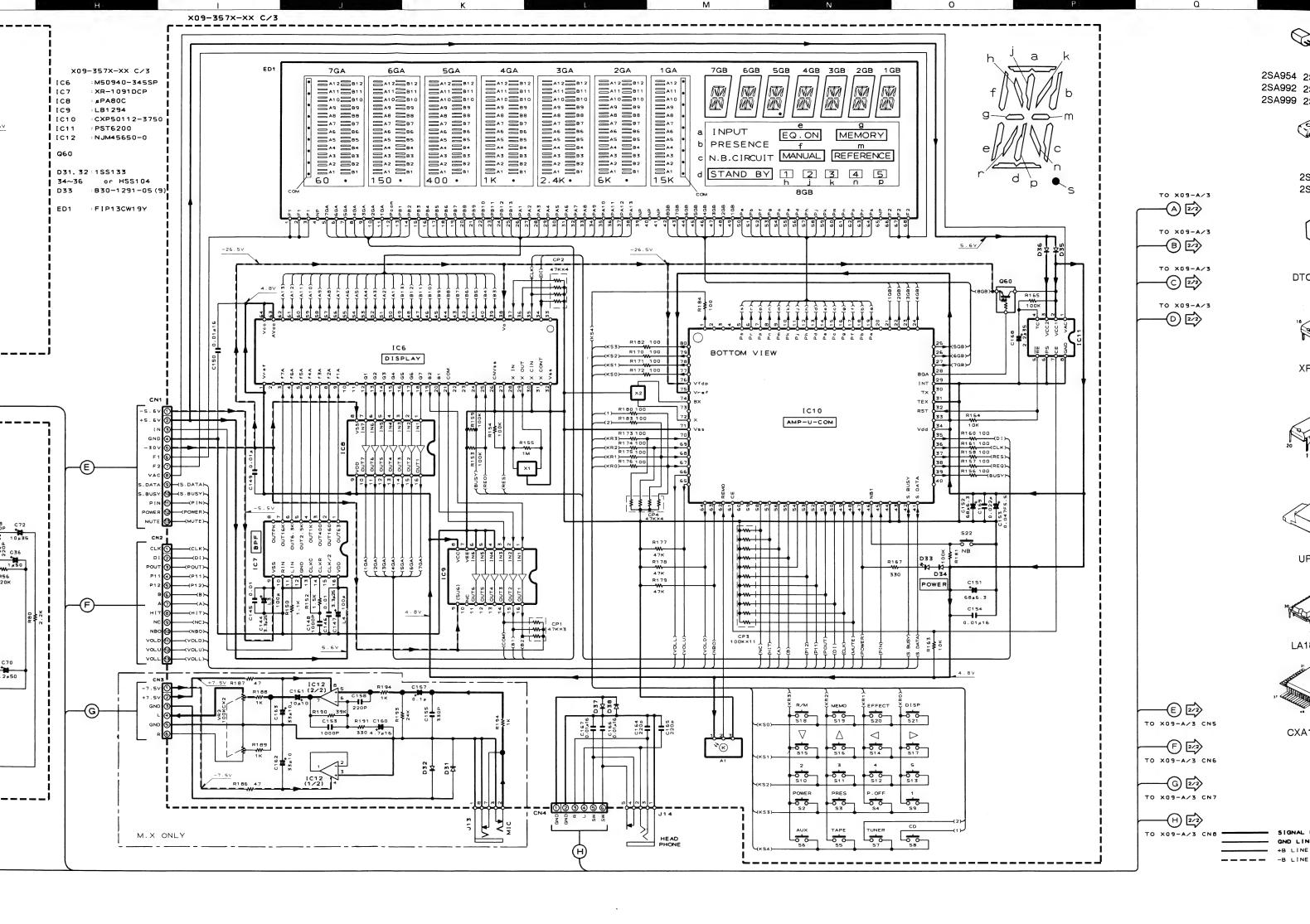


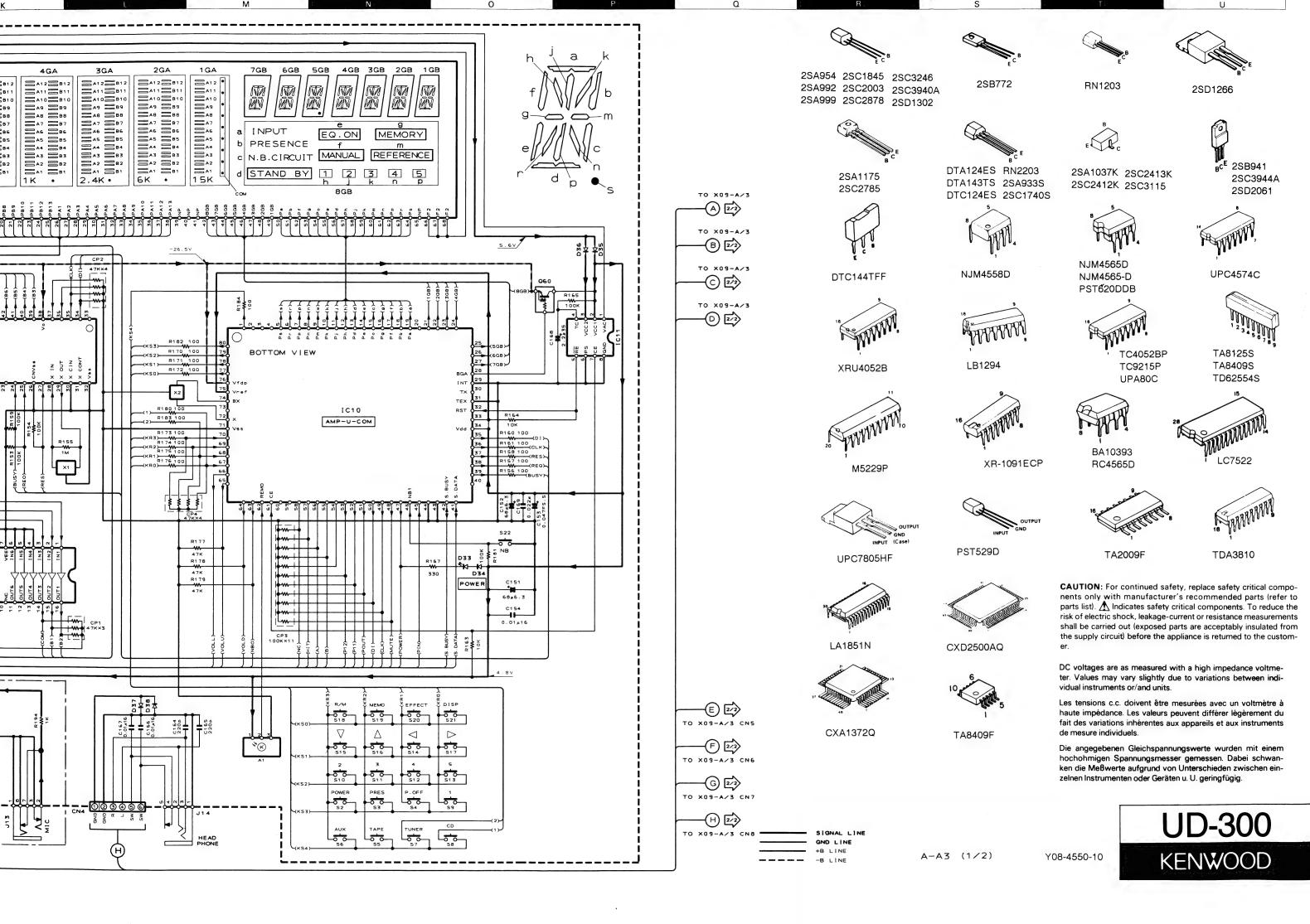


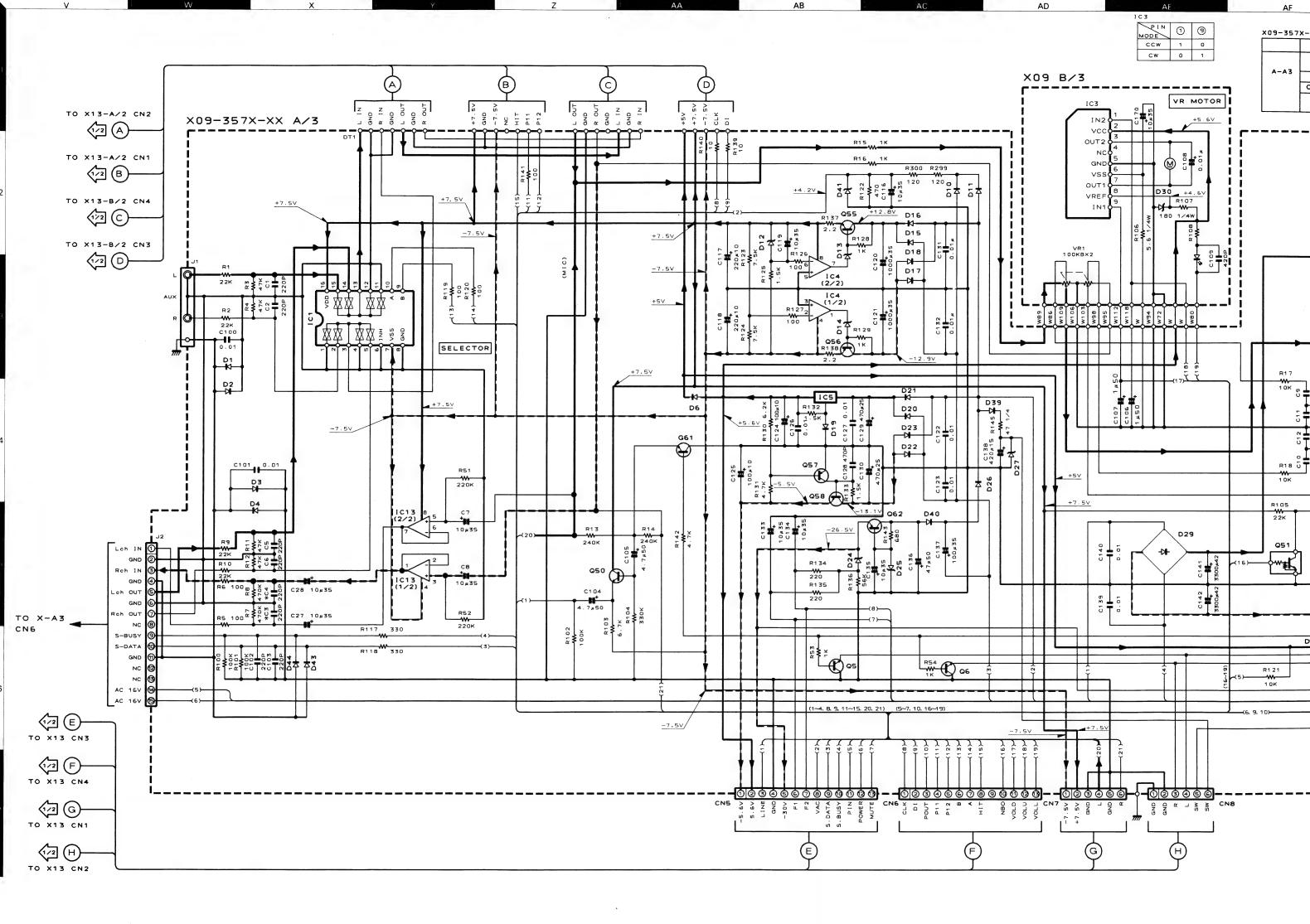
PC BOARD (Component side view)

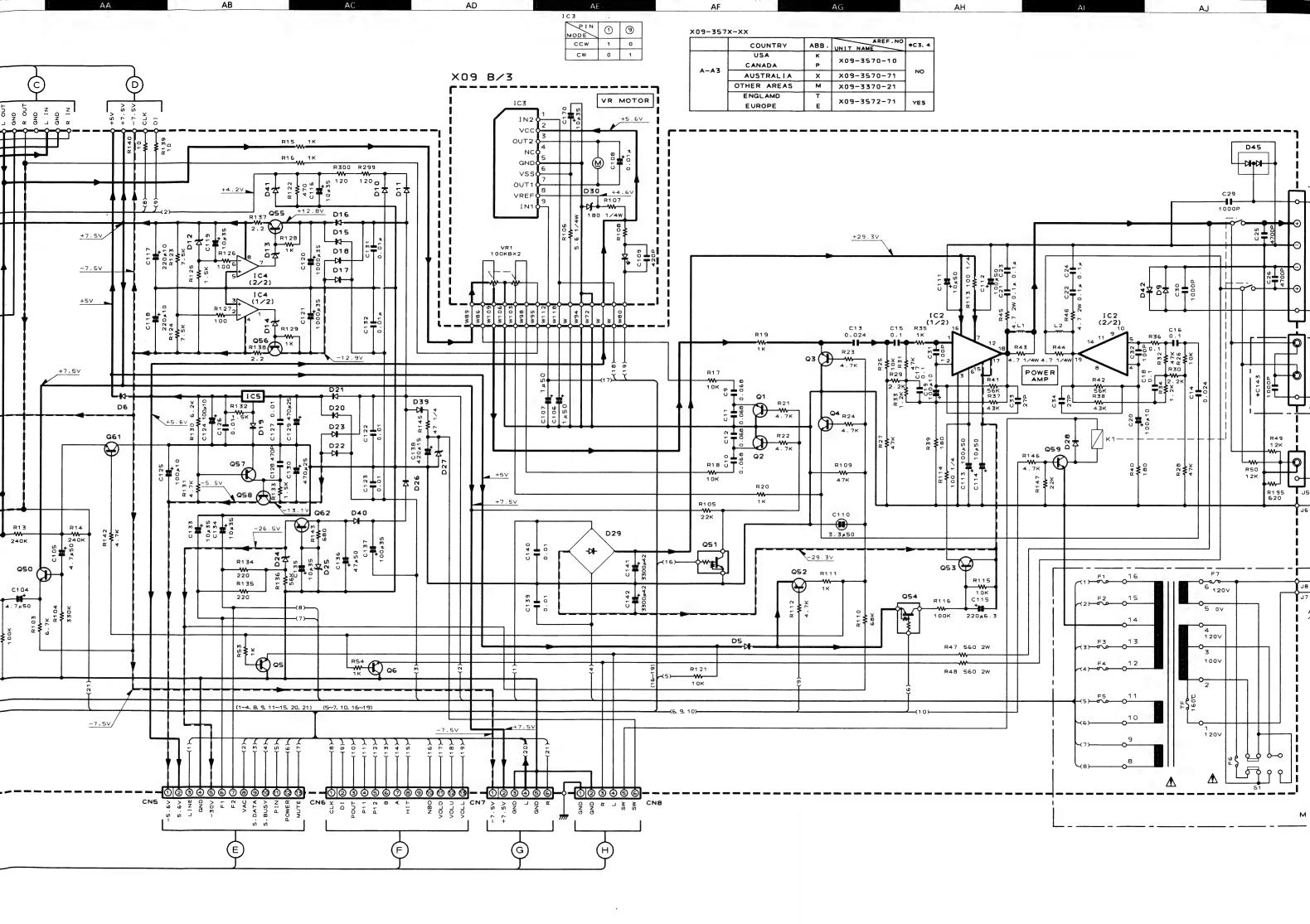


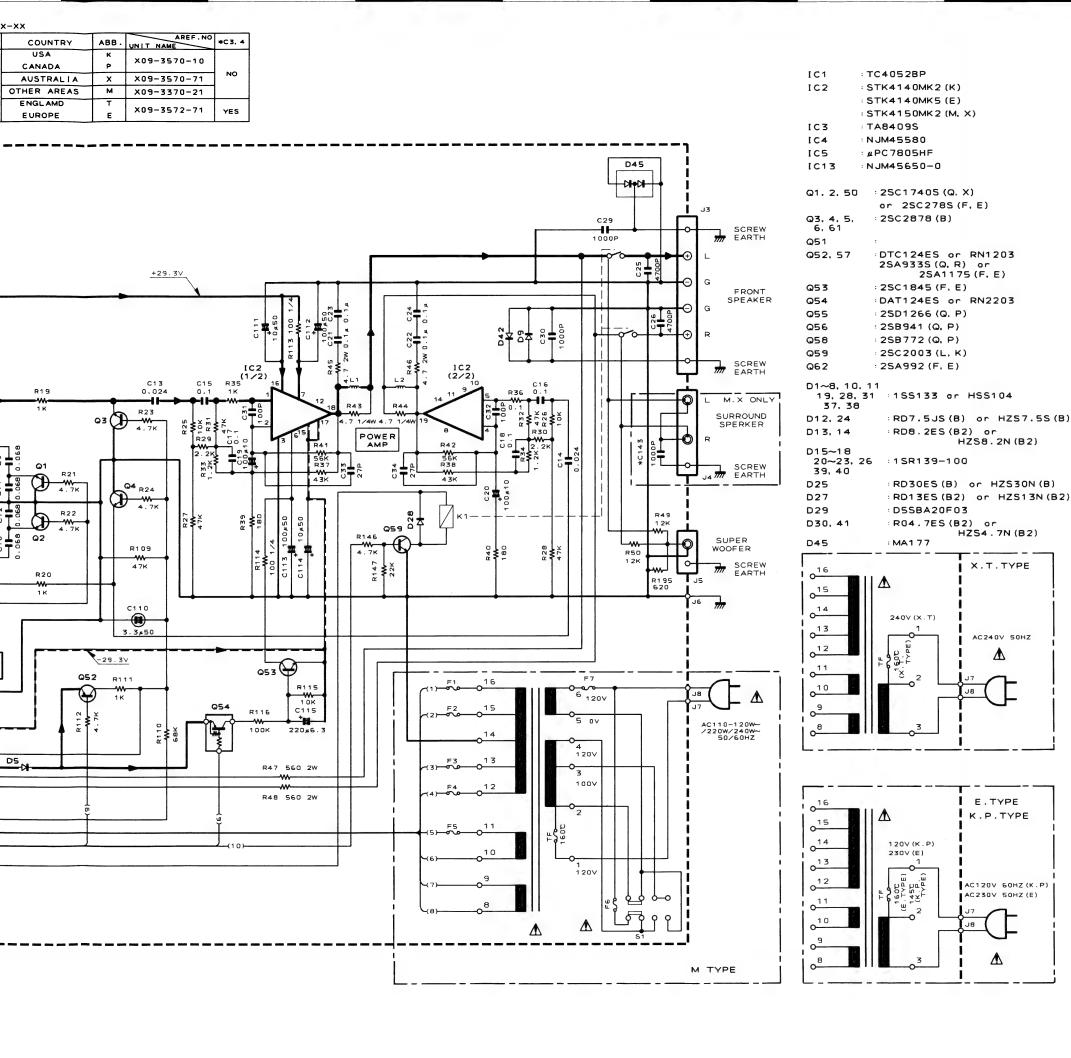












CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). A Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the custom-

DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u. U. geringfügig.

A-A3 2/2

-- -B LINE

SIGNAL LINE

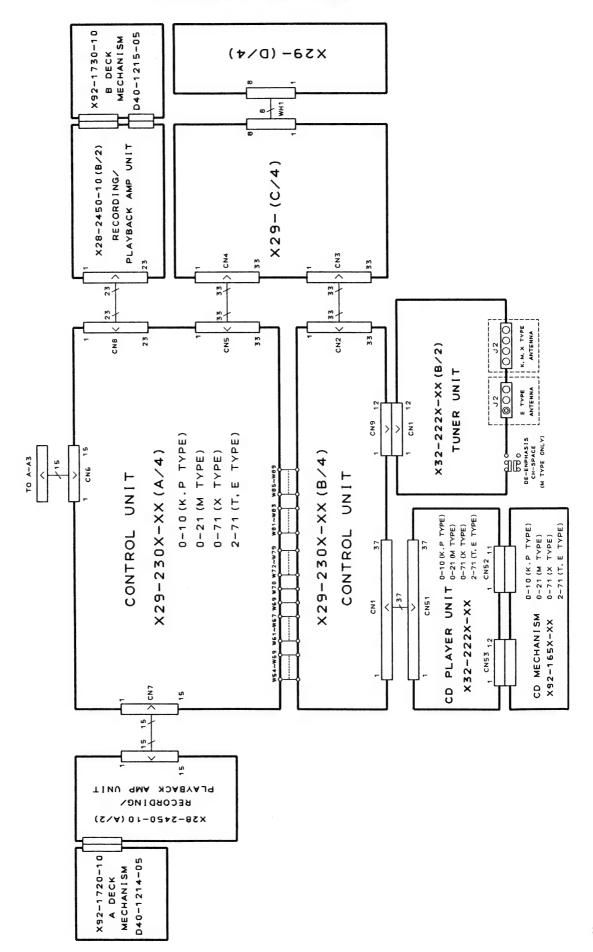
GND LINE

UD-300 KENWOOD

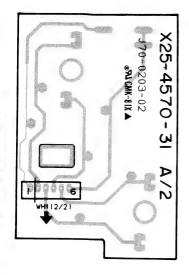
Y08-4550-10

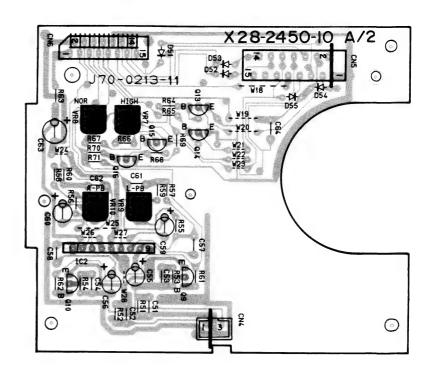
WIRING DIAGRAM

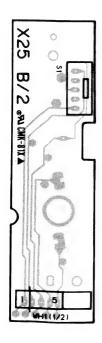
X-A3

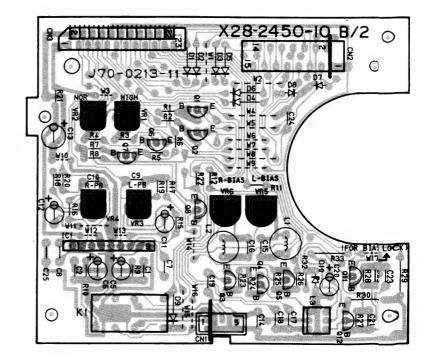


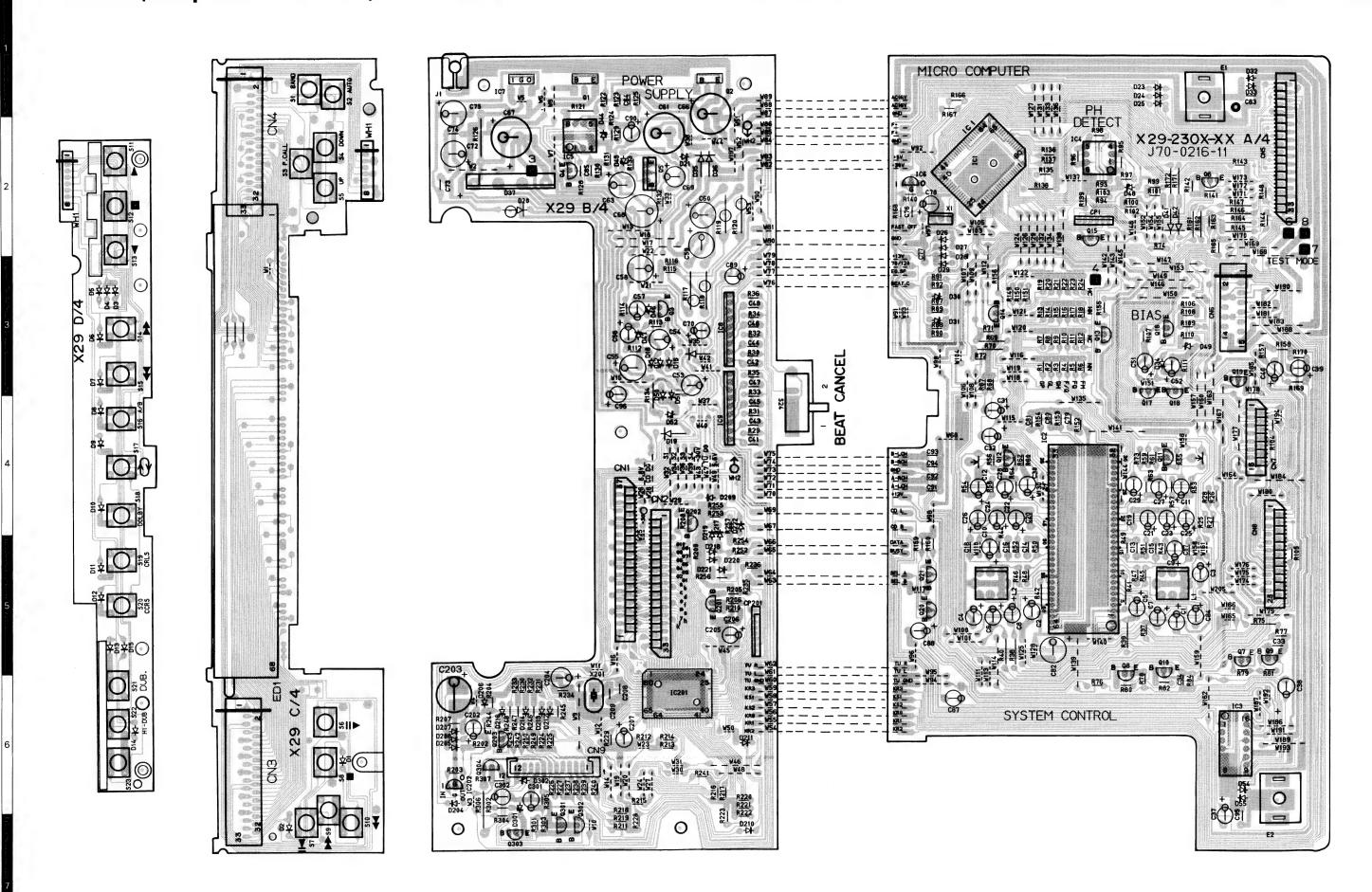
PC BOARD (Component side view)

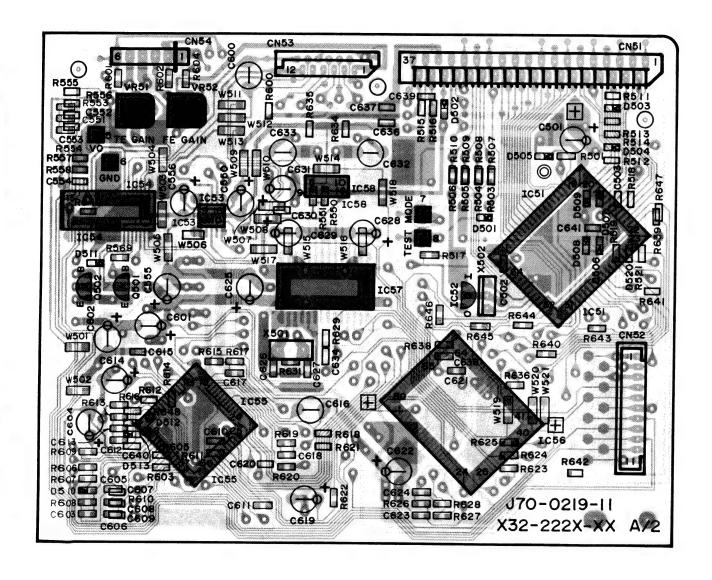


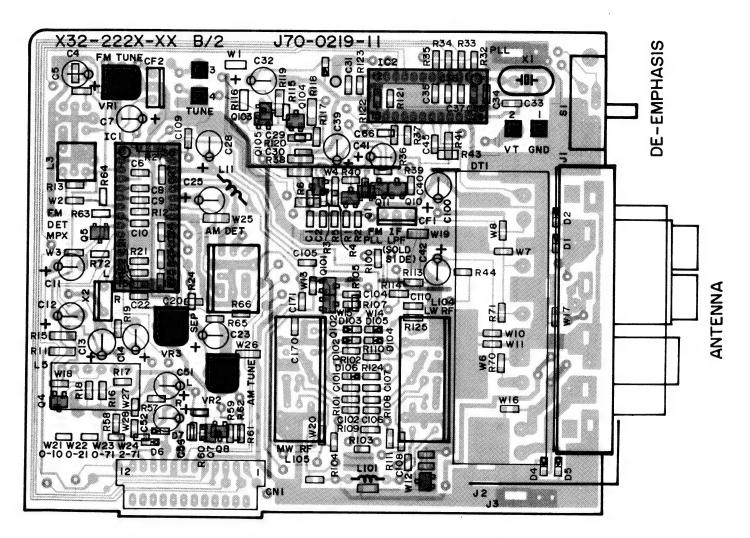


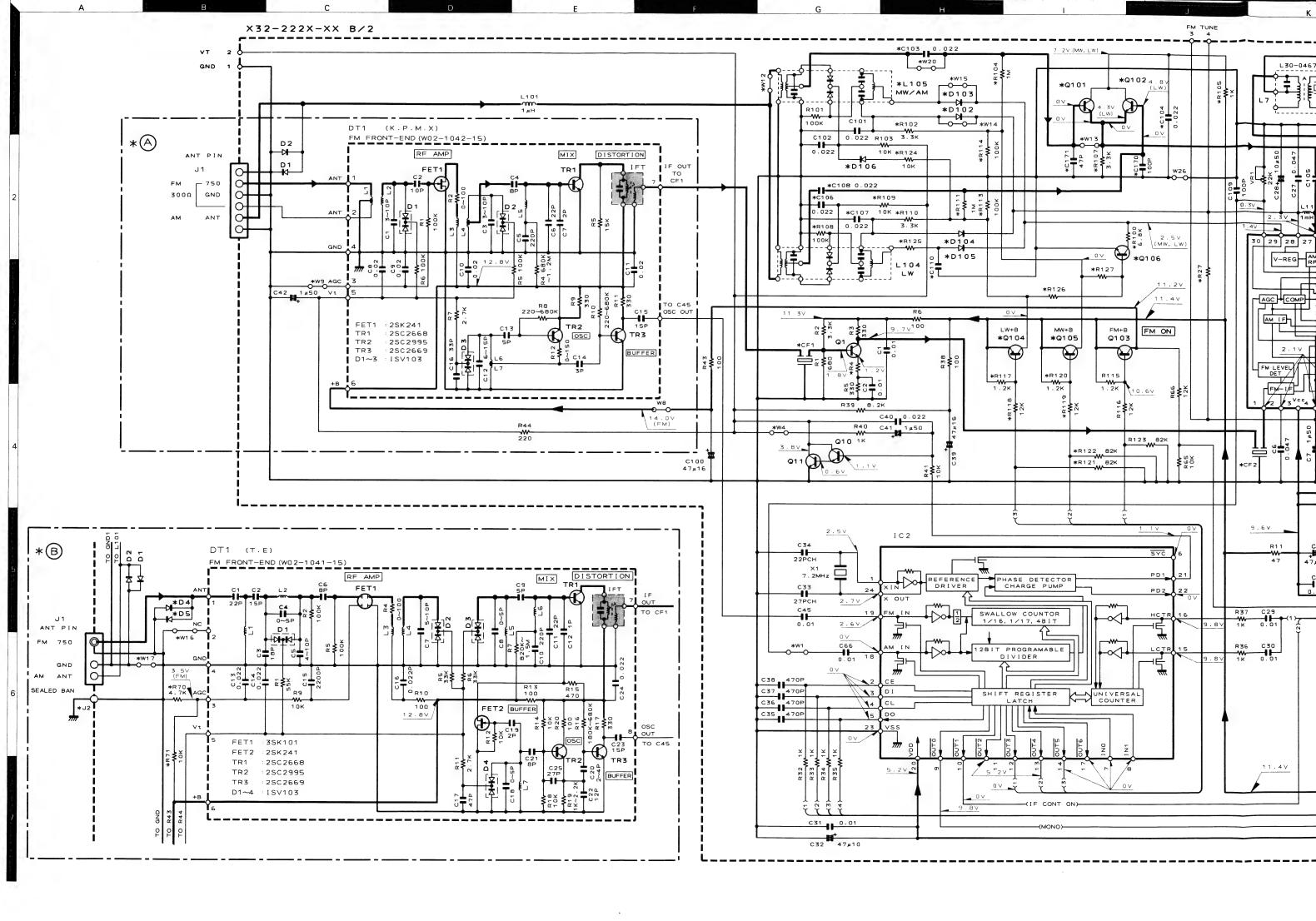


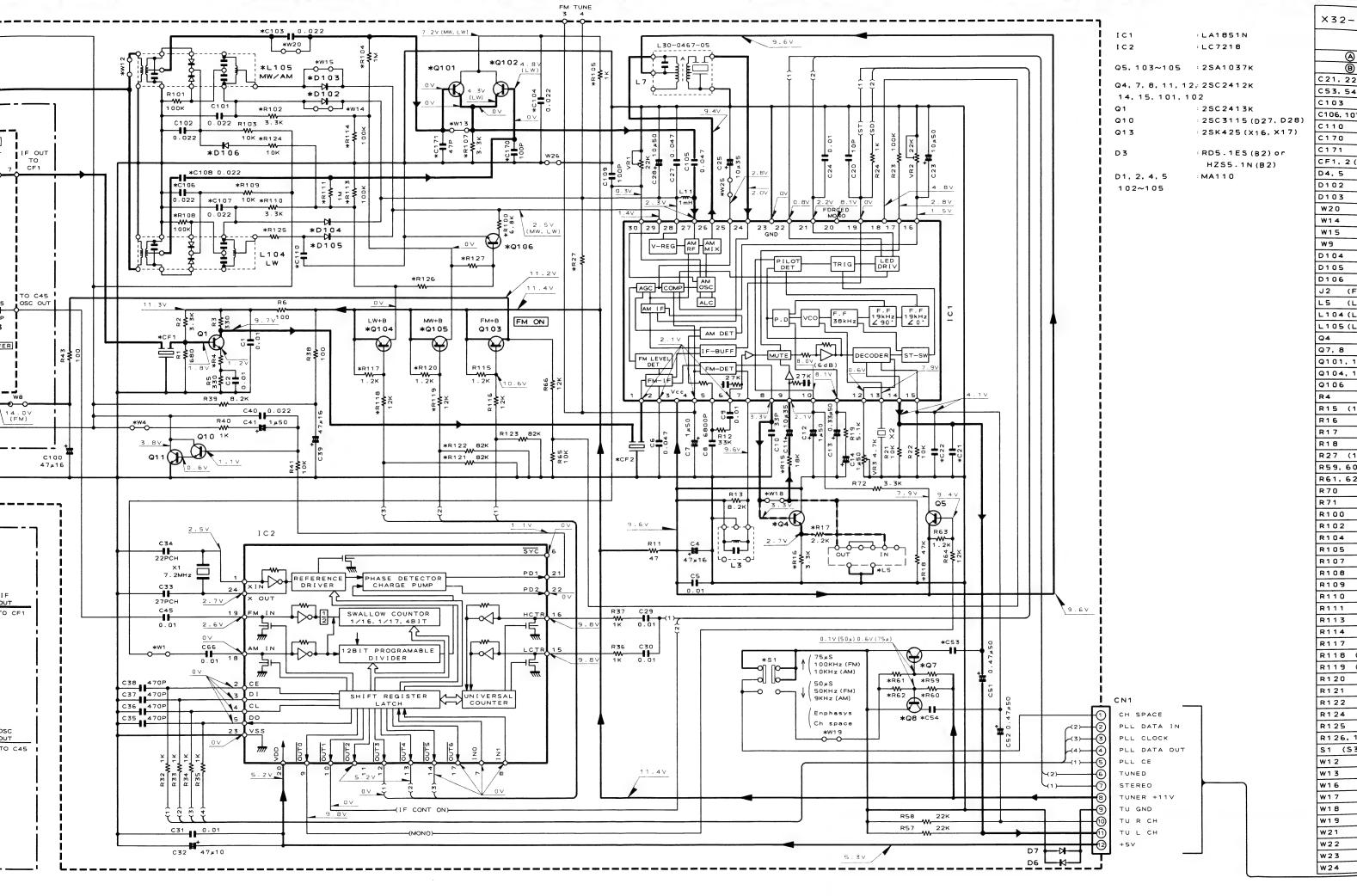








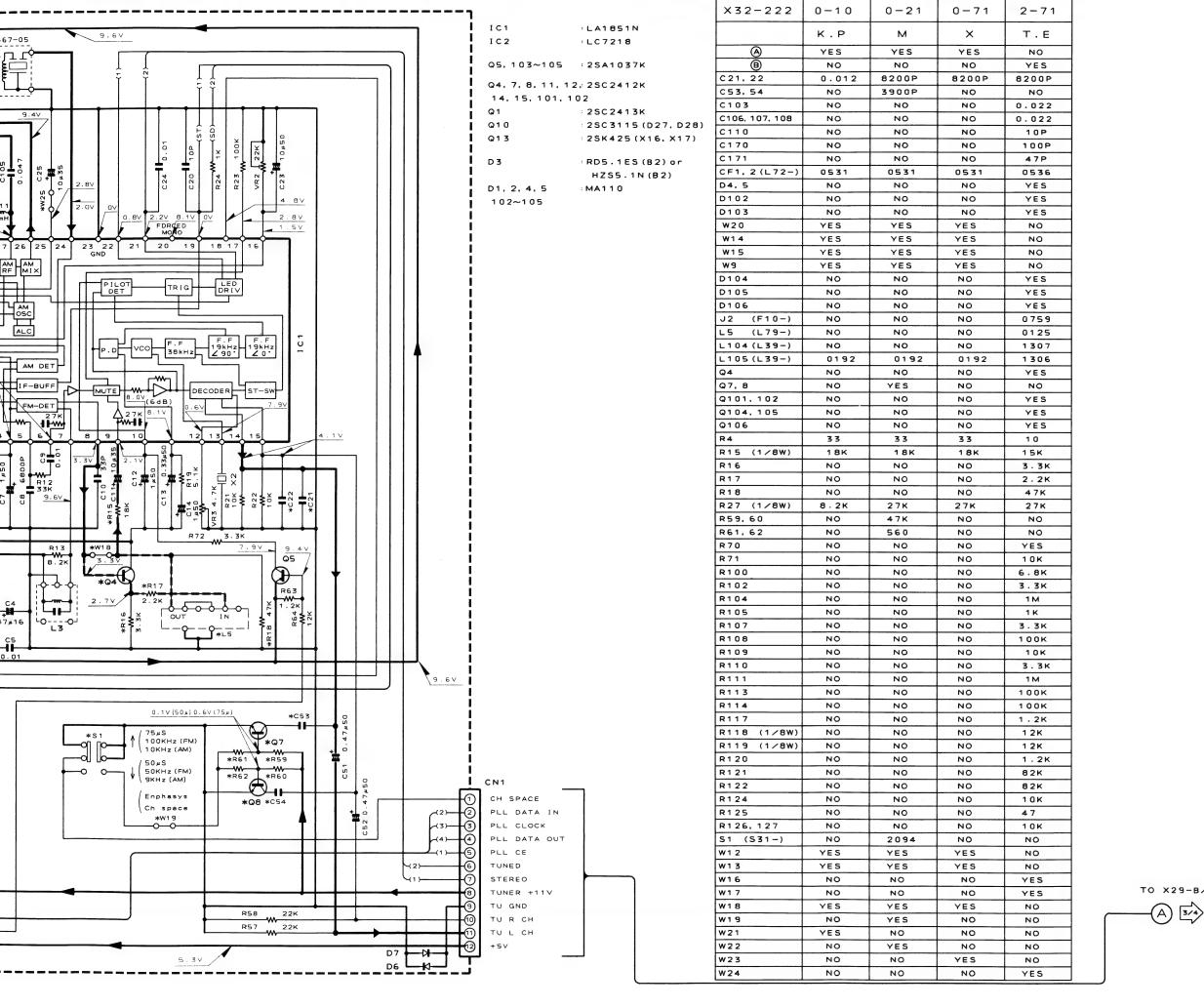




(A) (B) C106, 10

C170 CF1, 2(

J2 (F L5 (L L104 (L L105 (L Q7, 8



Q

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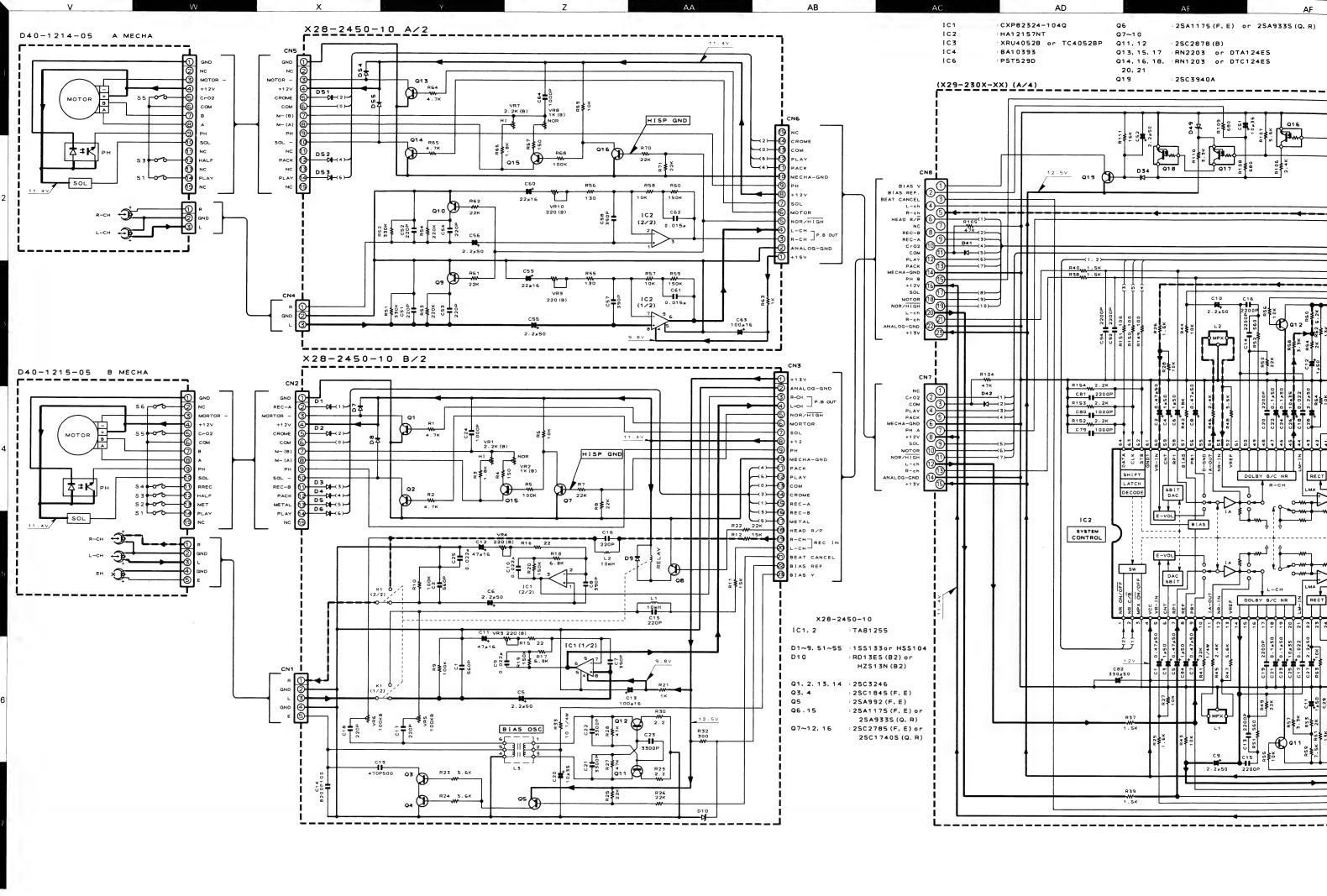
> SIGNAL LINE REG LINE GND LINE +B LINE

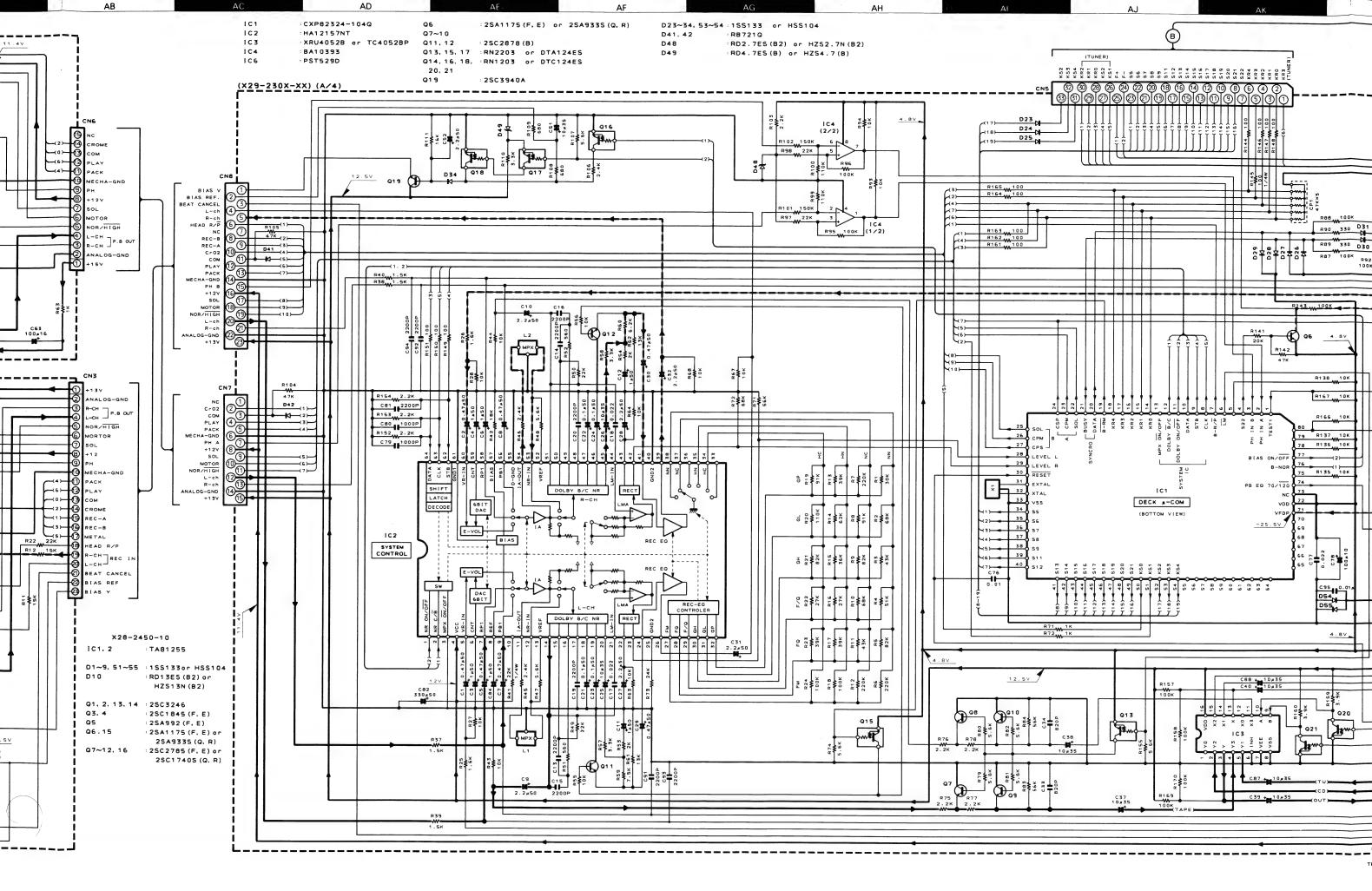
TO X29-B/4 CN9

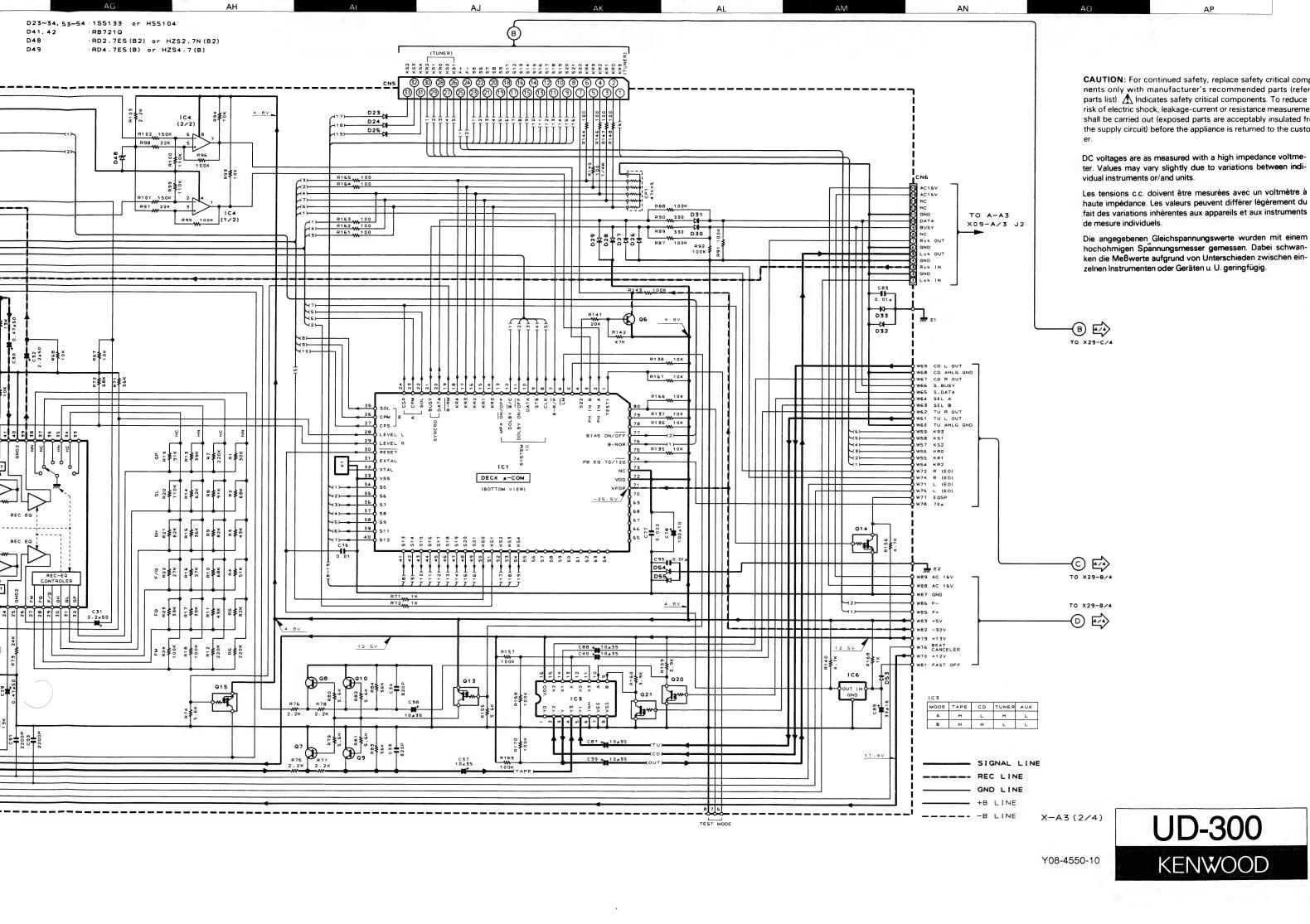
X-A3 1/4

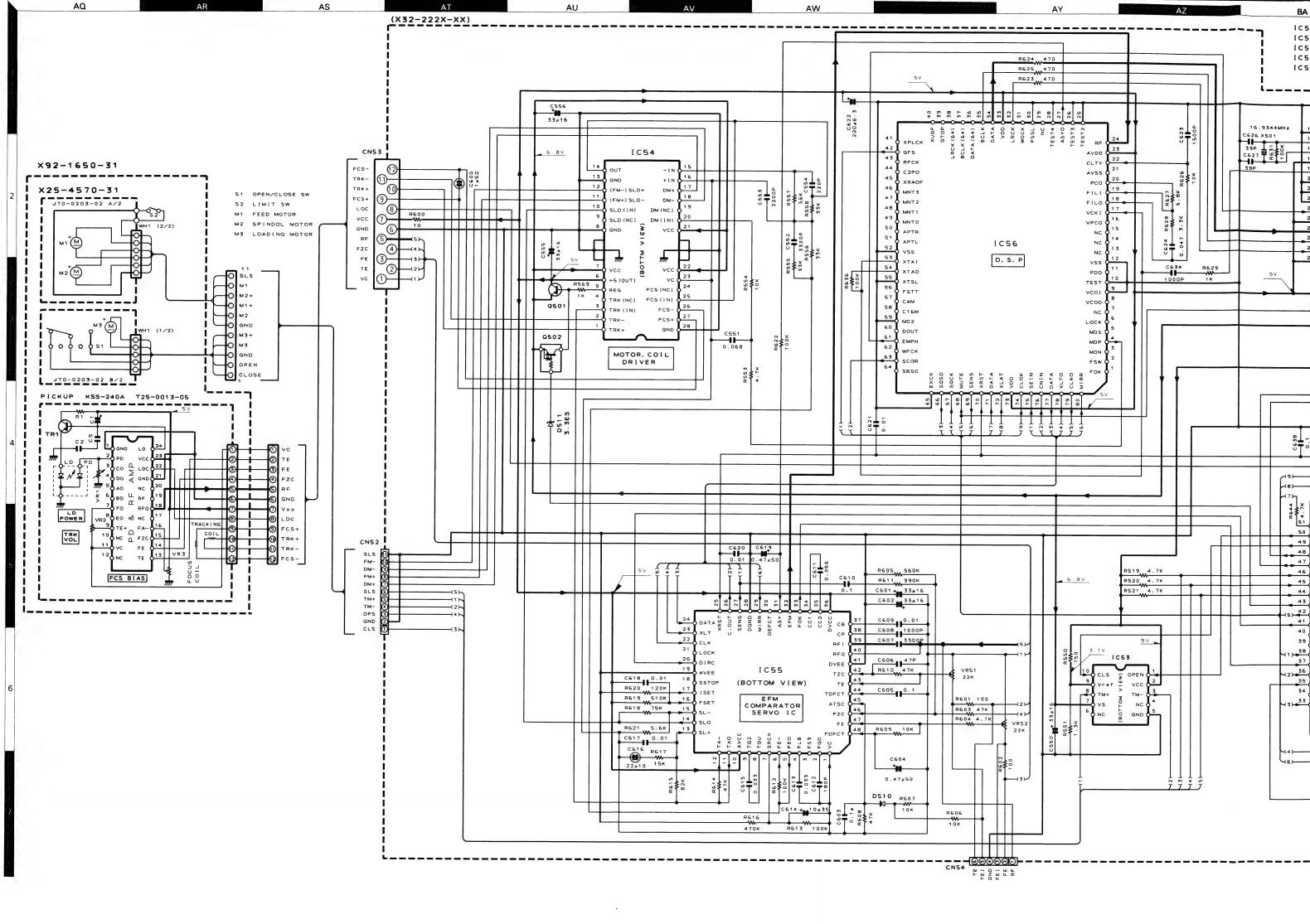
Y08-4550-10

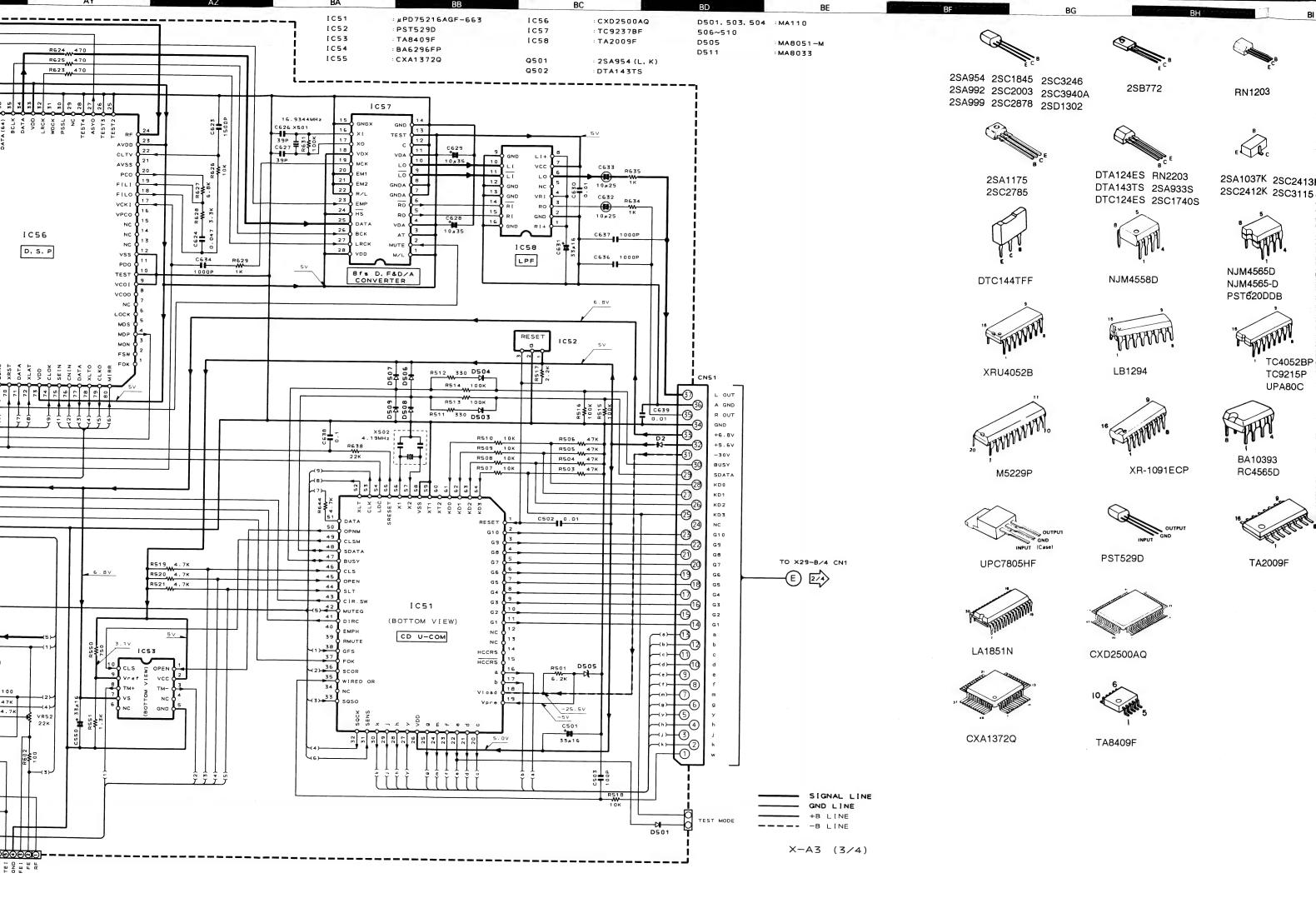
KENWOOD

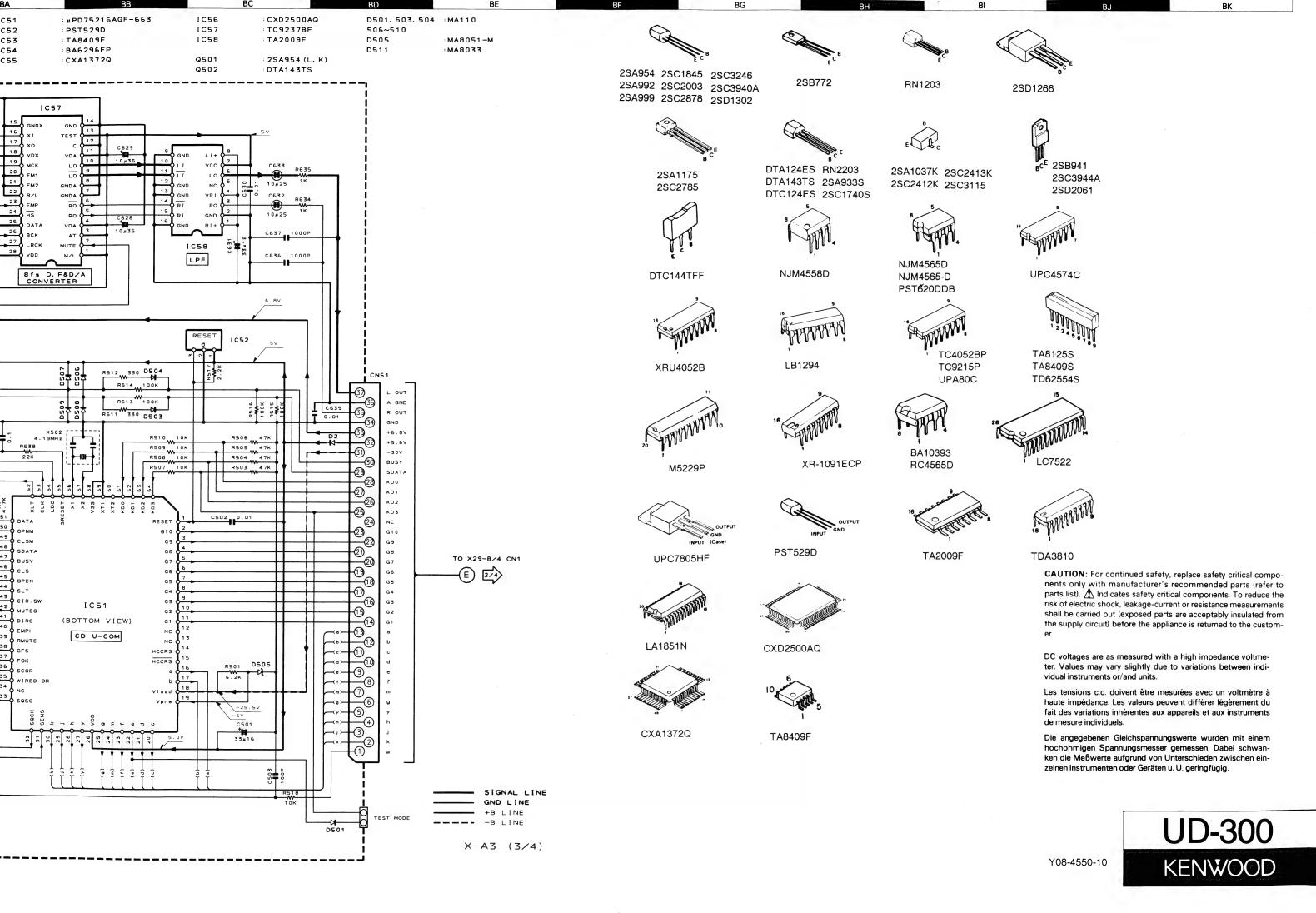


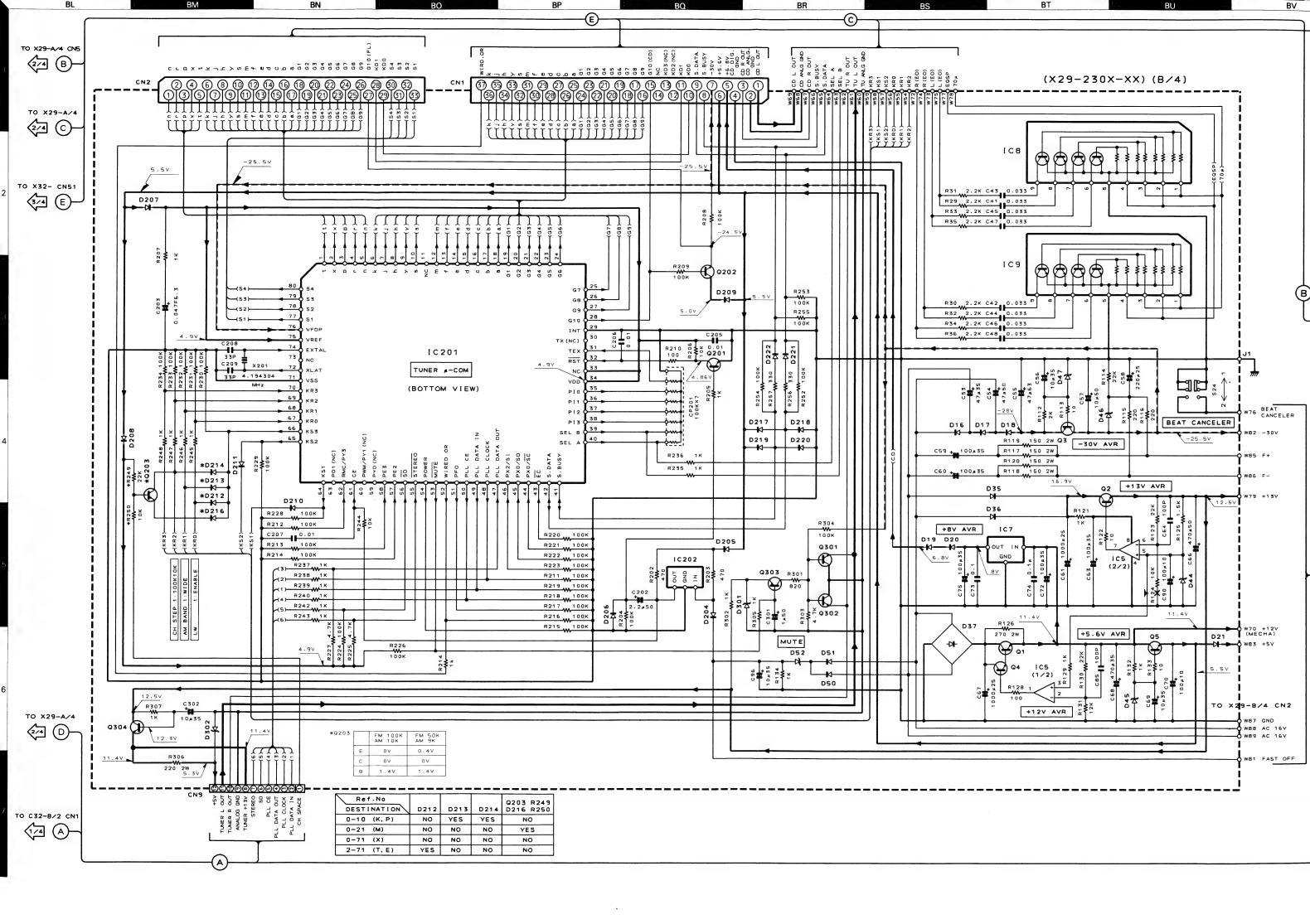


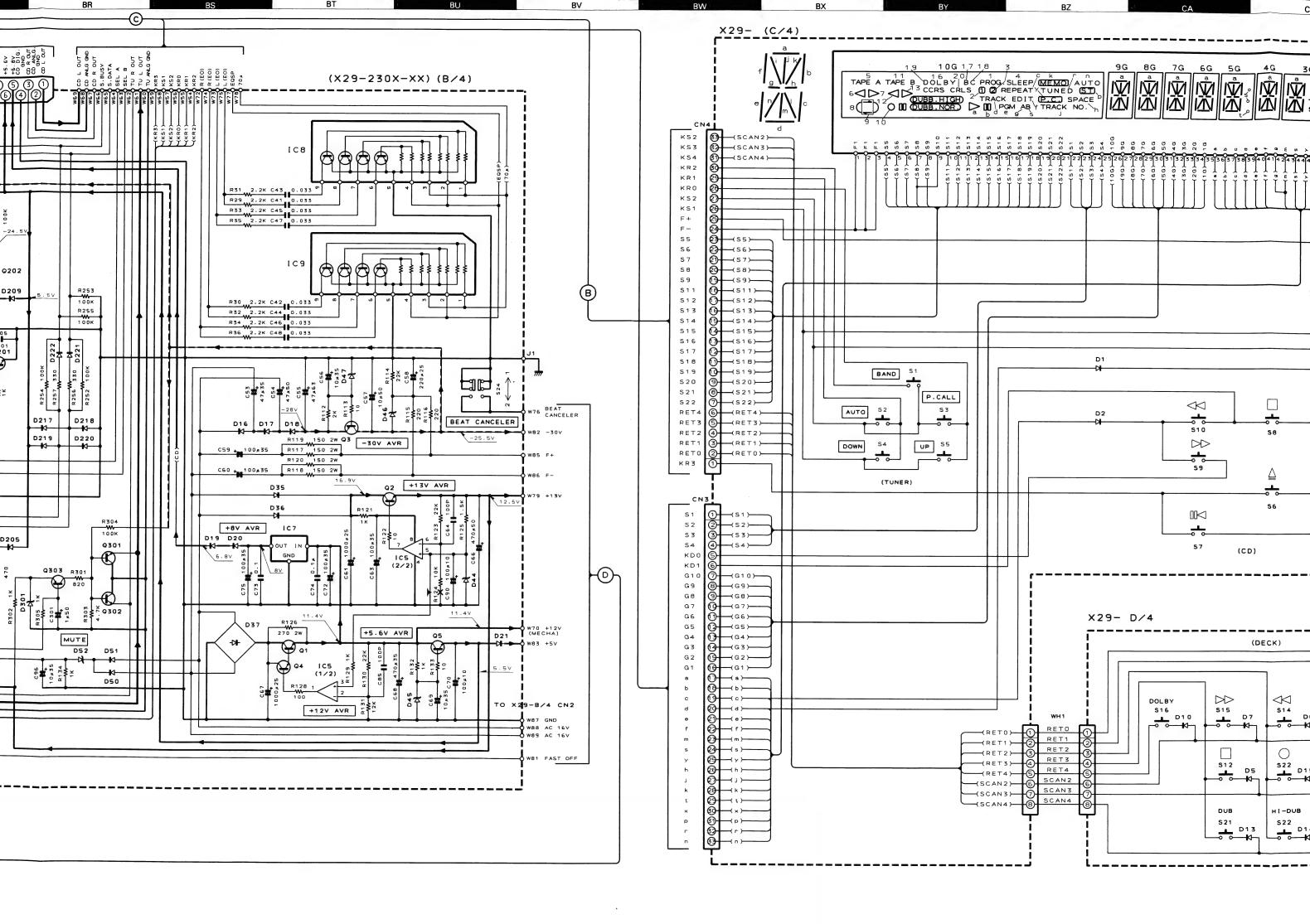


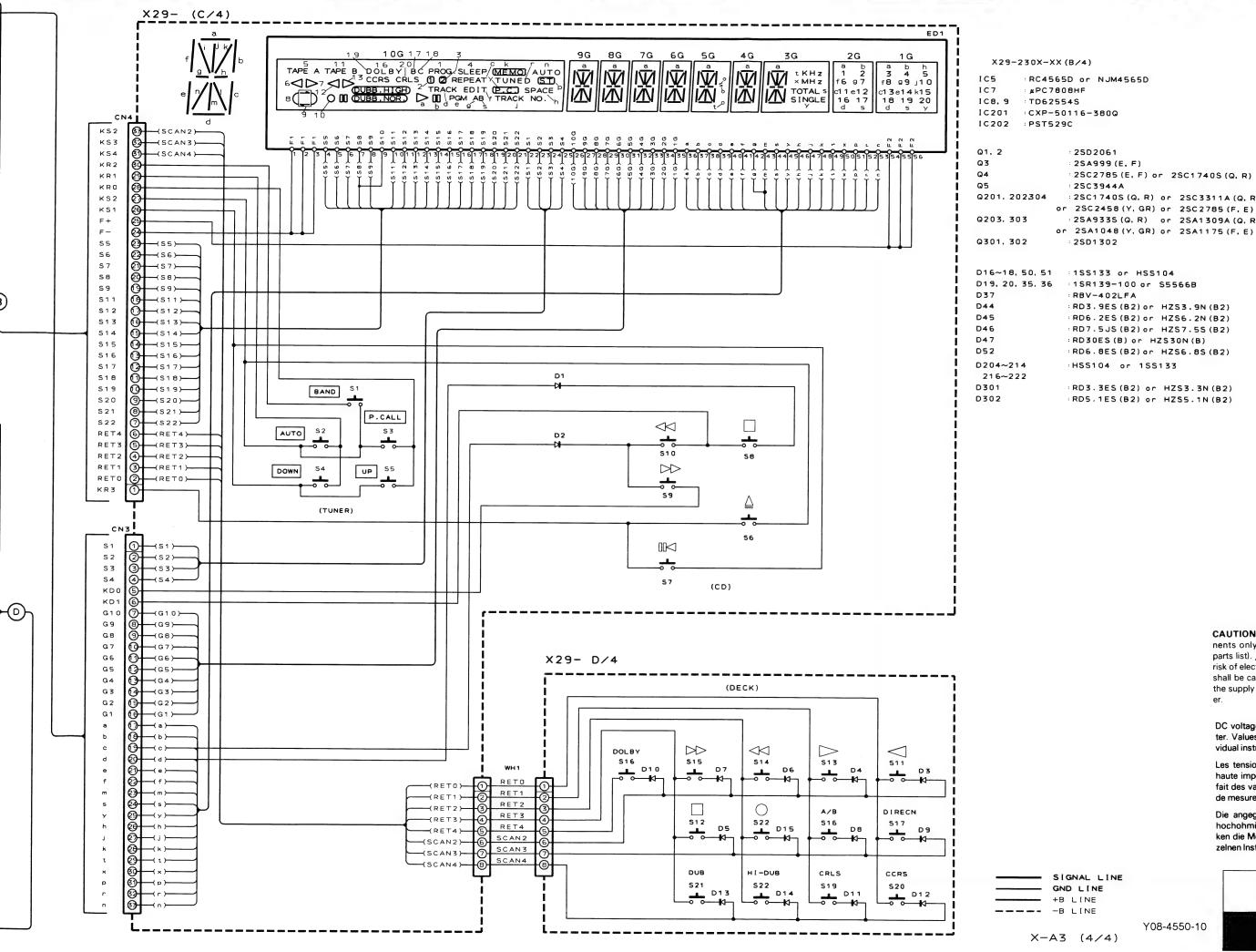












СВ

BX

2SC2785 (E, F) or 2SC1740S (Q, R)

:2SC1740S(Q, R) or 2SC3311A(Q, R) or 2SC2458(Y, GR) or 2SC2785(F, E) :2SA933S(Q, R) or 2SA1309A(Q, R)

:1SS133 or HSS104 :1SR139-100 or S5566B

RBV-402LFA

RD3.9ES(B2) or HZS3.9N(B2) RD6.2ES(B2) or HZS6.2N(B2) :RD7.5JS(B2)or HZS7.5S(B2) : RD30ES (B) or HZS30N (B) :RD6.8ES(B2)or HZS6.8S(B2)

RD3.3ES(B2) or HZS3.3N(B2) RD5.1ES(B2) or HZS5.1N(B2)

> CAUTION: For continued safety, replace safety critica nents only with manufacturer's recommended parts parts list). A Indicates safety critical components. To rerisk of electric shock, leakage-current or resistance meas shall be carried out (exposed parts are acceptably insula the supply circuit) before the appliance is returned to the

CF

DC voltages are as measured with a high impedance vo ter. Values may vary slightly due to variations between vidual instruments or/and units.

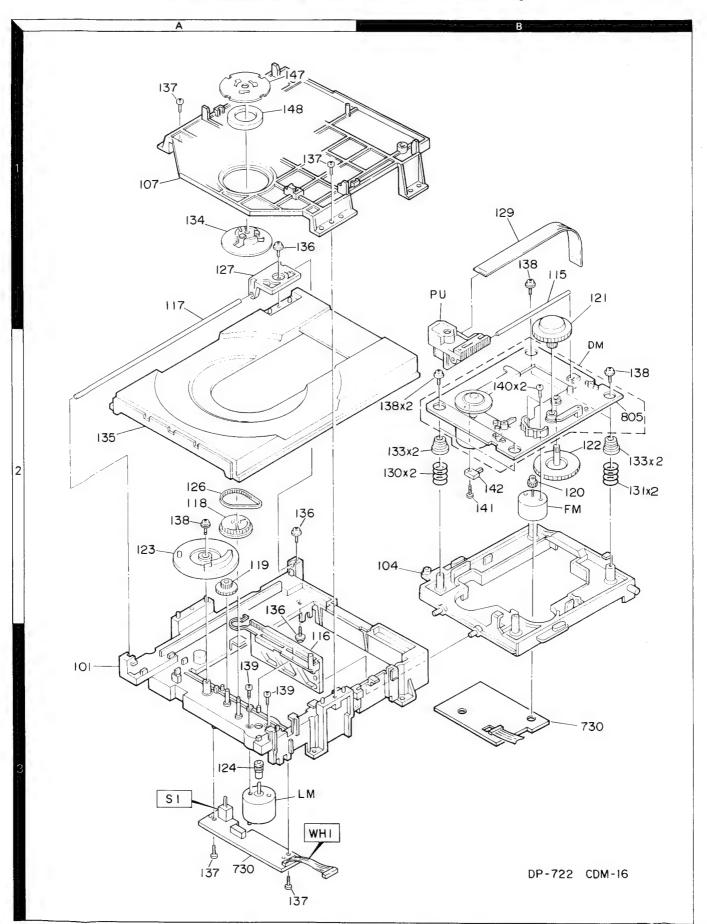
Les tensions c.c. doivent être mesurées avec un voltm haute impédance. Les valeurs peuvent différer légèreme fait des variations inhérentes aux appareils et aux instru de mesure individuels.

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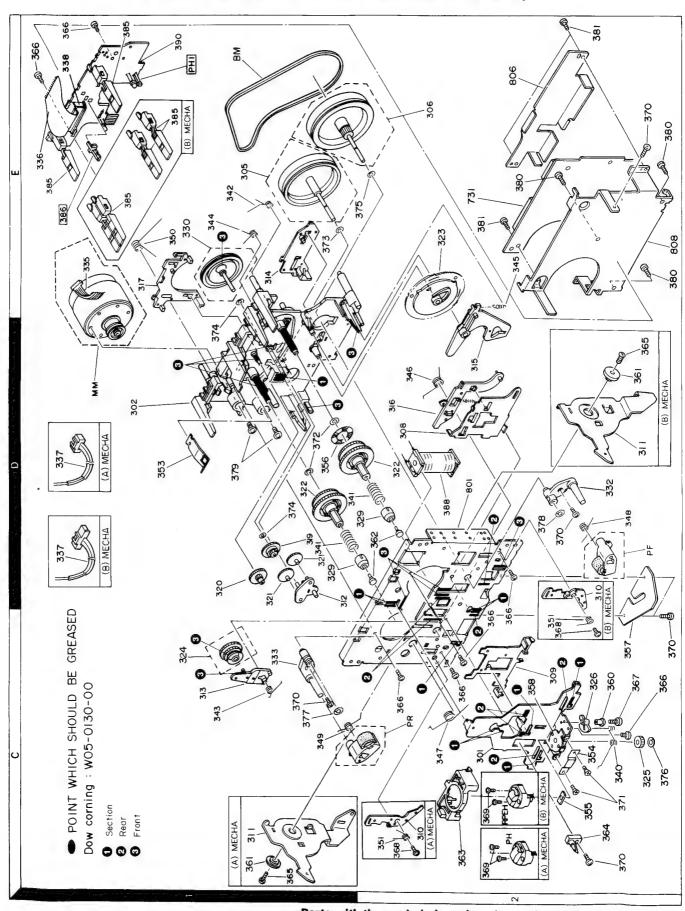
Y08-4550-10

UD-300 KENWOOD

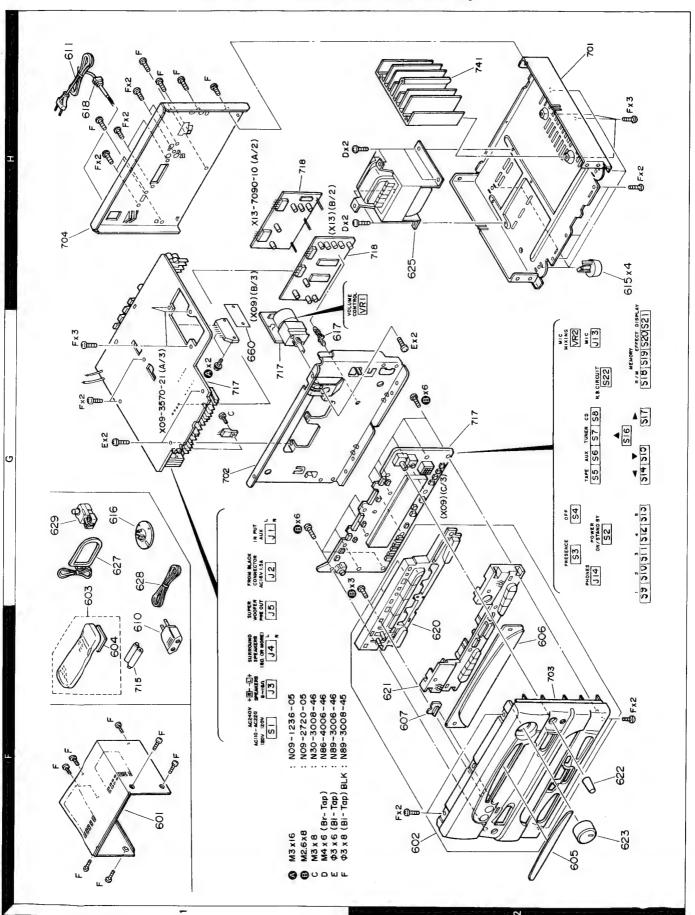
EXPLODED VIEW (MECHANISM)



EXPLODED VIEW (MECHANISM UNIT)

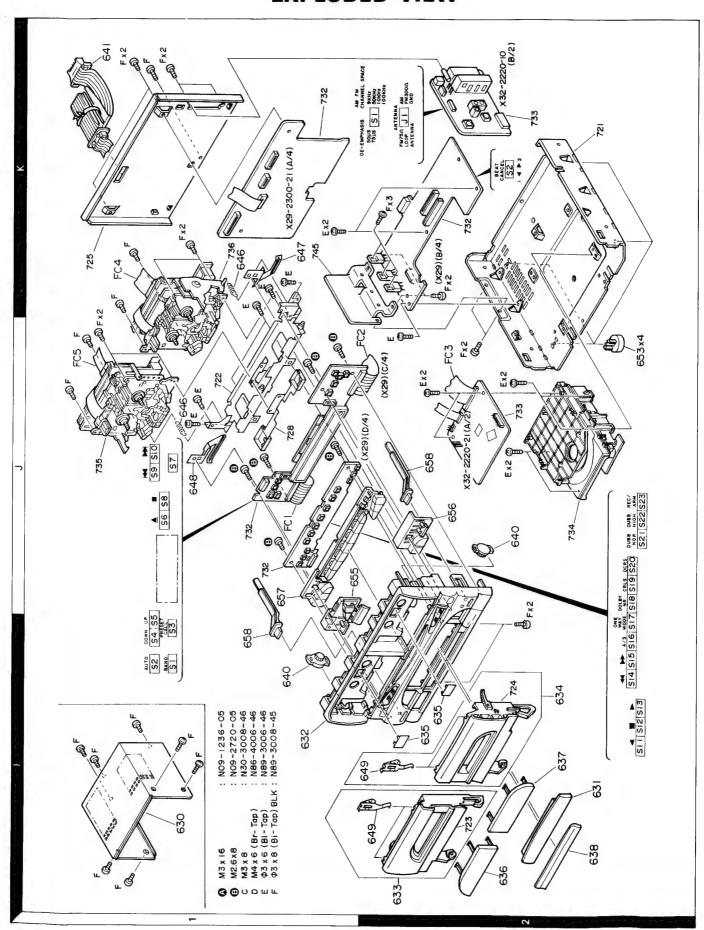


EXPLODED VIEW (UNIT)



Parts with the exploded numbers larger than 700 are not supplied. 105

EXPLODED VIEW



SCREW

SCREW (2.6X8)
HEAD TAPTITE SCHEAD TAPTITE SC

TAPTITE S BINDING H BINDING H

N09-2720-05 N89-3006-46 N89-3008-45

1J 11,1J 1J,1K

STOP, PAUSE CASSETTE CONTROL EJECT

KNOB KNOB KNOB KNOB

K29-4360-13 K29-4361-03 K29-4362-13 K29-4363-14

11 23 13 11,23

655 656 657 658

indicates safety critical components.

M:Other Areas E:Europe

X:Australia T:England K:USA

Y:PX(Far East, Hawaii)

:Scandinavia

Y: AAFES(Europe)

indicates safety critical components.

M:Other Areas

T:England K:Australia N.C.

Y:PX(Far East, Hawaii)

L:3candinayia

r.Canada E:Europe

PARTS LIST

CORD WITH CONNECTOR POWER CORD FLAT CABLE X29CN2-CN3.CN4-CN5 FLAT CABLE X29CNN1)-X92CCN5) FLAT CABLE X29CNN1)-X92CCN3) FLAT CABLE X29(CN7)-X92CCN5)

E30-2686-05 E35-0300-05 E35-0301-05 E35-0302-05 E35-0305-05

11211

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CASSETTE HOLDER CASSETTE HOLDER

A53-1340-23 A53-1338-23

21 21

533

KPMX TE KPMX ы

METALLIC CABINET
CD TRAY PANEL
PANEL
CASSETTE HOLDER ASSY

A01-2922-01 A29-0310-03 A60-0190-11 A60-0192-11 A53-1336-23

11 21 21 21 21

4 8

CASSETTE HOLDER CASSETTE HOLDER TUNER DISPLAY

ESCUTCHEON FRONT CLASS FRONT CLASS FRONT CLASS WARRANTY CARD

B07-1720-04 B10-1914-03 B10-1915-03 B10-1916-03 B46-0122-23

2222

636 637 638

WARRANTY CARD

B46-0143-13 039-0198-05

DAMPER

11,23 23

640

m «

EXTENSION SPRING FLAT SPRING FLAT SPRING FLAT SPRING

G01-3461-14 G02-0998-14 G02-0999-14 G02-1001-24

13,1K 1K 1J 2I

ITEM CARTON CASE
ITEM CARTON CASE
ITEM CARTON CASE
POLYSTYRENE FOAMED FIXTURE
POLYSTYRENE FOAMED FIXTURE

H50-0241-14 H50-0301-04 H50-0313-14 H10-5263-01 H10-5264-01

POLYSTYRENE FOAMED FIXTURE (L)
PROLYSTYRENE FOAMED FIXTURE (R)
PROFECTION COVER
PROTECTION BAG (0235 PRINTED)
PROTECTION BAG (0232 PRINTED)

H10-5270-12 H10-5271-12 H20-0574-04 H25-0232-04 H25-0651-04

PROTECTION

FOOT

302-0370-05

23

H25-0672-04

Parts without Parts No. are not supplied.

Parts No.

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参照番号 Š

New Parts

Address

Ref.

Destir Renation marks 在向鐵券

名/類 Description

ng

TAPTITE SCREW (2.6X8)
BINDING HEAD TAPTITE SCREW
BINDING HEAD TAPTITE SCREW
BINDING HEAD TAPTITE SCREW

N09-2720-05 N66-4006-46 N89-3006-46 N89-3008-45

16,26 1H 16,26 1F,1H

LOOP ANTENNA T TYPE ANTENNA ANTENNA ADAPTOR

T90-0174-05 T90-0175-05 T90-0185-05

555

627 628 629

NO.2

Les articles non mentionnes dans le Parts No. ne sont pas fournis. Telle ohne Parts No. werden nicht geliefert. NO.1

Les articles non mentionnes dans le Parts No. ne sont pas fournis

Telle ohne Parts No. werden nicht gellefert. Parts without Parts No. are not supplied.

Description Desti- Re- 路 G 名/ 激 本 仕 向 編巻	3	METALLIC CABINET RANEL ASSY PANEL ASSY PANEL ASSY MX REMOTE CONTROL ASSY UNIT	DRESSING PLATE FRONT GLASS INDICATOR KARRANTY CARD X	WARRANTY CARD WARRANTY CARD WARRANTY CARD WARRANTY CARD INSTRUCTION MANUAL (ENGLISH) INSTRUCTION MANUAL (FRENCH)	INSTRUCTION MANUAL (GERMAN) E INSTRUCTION MANUAL (DUTCH) E INSTRUCTION MANUAL (ITALIAN) E INSTRUCTION MANUAL (SPANISH) ME	INSTRUCTION MANUAL (ARABIC) M	AC POUG ADAPTER KAC POWER CORD KAC POWER CORD X AC POWER CORD T AC POWER CORD ME	TIEM CARTON CASE	POLYSTYRENE FOAMED FIXTURE (L) TE POLYSTYRENE FOAMED FIXTURE (R) TE CARTON BOARD PROTECTION BAG (235X350X0.03) E PROTECTION BAG	PROTECTION BAG (0232 PRINTED) T	FOGT NYTENNA HOLDER UNIT HOLDER POWER CORD BUSHING	KNOB INPUT SELECTOR NNOB GE CONTROL NNOB MIC MIXING NX NOB MIC MIXING NX NOB WOLUME CONTROL	
Parts No. 唐岳中与	A-A	A01-2922-01 A60-0180-12 A60-0181-12 X94-1010-11 A09-0126-03	B03-2764-04 B10-1906-03 B12-0190-04 B46-0092-13 B46-0096-33	B46-0121-13 B46-0122-23 B46-0143-13 B60-0751-00 B60-0752-00	B60-0753-00 B60-0754-00 B60-0755-00 B60-0756-00 B60-0757-00	B60-0758-00	E03-0115-05 E30-2274-15 E30-2275-15 E30-2276-15	H50-0241-14 H50-0242-04 H50-0313-14 H10-5263-01 H10-5264-01	H10-5294-12 H10-5295-12 H13-0086-04 H25-0232-04 H25-0632-24	H25-0651-04 H25-0671-04	J02-0370-05 J19-2815-04 J19-3329-05 J42-0083-05	K29-4351-02 K29-4352-02 K29-4357-04 K29-4358-04	L07-0465-15 L07-0466-15
Parts	+	****	***	**	****	*	****	****	***	*		****	* *
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Ref. No.		601 602 602 603 604	605 606 607	1111	11111	1	610 6111 6111 6111	1 1	† }]]]	1 1	615 616 617 618	620 621 623 623	625 625 625 625 625

Y:AAFES(Europe)

107

PARTS LIST

Parts without Parts No. are not supplied. Les articles non mentionnes dans le Parts No. ne sont pas foumis. Teile onne Parts No. werden nicht geliefert.

*	Ref. No. 物理毒丸	Add #	Address 位 首	New Parts	Parts No. 地由 电电路	E D	Description 品名/規	梅	Desti- nation 仕 向	Re- marks
100000 141000	9,140 1,142 3 4 5,146			*	CK45FF1H103Z CC0-1961-05 CK45FB1H102K C90-3231-05 CK45FF1H103Z	CERAMIC ELECTRO CERAMIC ELECTRO CERAMIC	0.010UF 3300UF 1000PF 3.3UF 0.010UF	2 45WV K 25WV Z	XX	
014 015 015 015 015	7 8 9 0 1,152			*	C90-3231-05 CQ92FM1H102J CK45FF1H103Z C91-0769-05 C90-3213-05	BLECTRO MYLAR CERAMIC CERAMIC BLECTRO	3.3UF 1000PF 0.010UF 0.01UF 68UF	25WV J Z K K		
015	455000				C91-0769-05 C91-0753-05 C91-0757-05 C90-3248-05 C91-0749-05	CERAMIC CHIP C CERAMIC ELECTRO	0.01UF 470PF 1000PF 0.1UF 220PF	XXXX X X X X X	XXXX	
00000	9 0 1 2,163 4,165			*	C91-0757-05 C90-3224-05 C90-3240-05 C90-3219-05 C91-0757-05	CERAMIC BLECTRO BLECTRO BLECTRO CERAMIC	1000PF 4.7UF 2.2UF 33UF 1000PF	K 164V 354V 104V K	****	
20 20 20 20 20 20 20 20 20 20 20 20 20 2	6,167 8 9 0 1,172			*	C91-0769-05 C90-3240-05 CK45FF1H223Z CE04LW1V100M CK45FF1H103Z	CERAMIC BLECTRO CERAMIC BLECTRO CERAMIC	0.01UF 2.2UF 0.022UF 10UF 0.010UF	X 35WV 2 35WV Z		
12222 13432				* **	E63-0040-05 E08-1509-05 E70-0013-05 E63-0041-05 E13-0138-05	PHONO JACK RECTANGULAR R LOCK TERMINAL PHONO JACK S	AUX L BOARD S SURROUND S SUPER W	E BLACK SPEAKER SPEAKER WOOFER	X	
J13	,14			**	E11-0234-05 E11-0234-05	PHONE JACK PHONE JACK	MCI, HE HEAD F	MCI,HEAD PHONE HEAD PHONE	MX KPTE	
4444 777776	m m	16		*	F20-1352-05 F53-0022-05 F53-0036-05 F53-0016-05 F53-0030-05	INSULATING S FUSE FUSE FUSE FUSE	SHEET		MXTE KP MXTE KP	
###### 44000					F53-0022-05 F53-0036-05 F53-0020-05 F53-0034-05 F05-1623-05	FUSE FUSE FUSE FUSE FUSE FUSE (SEMKO)	(250V	T1.6A)	MXTE KP MXTE KP XTE	
F6	٠,				F06-3027-05 F05-1623-05	FUSE (UL) FUSE (SEMKO)	(250V (250V	3A) T1.6A)	××	
CN9 CN9	-12 ,10				J13-0075-05 J13-0075-05 J21-5159-04	FUSE CLIP FUSE CLIP MQUNTING HAR	HARDWARE		M KPXTE	
KX X X X X X X X X X X X X X X X X X X	5.4				L39-0085-05 L40-1011-17 L78-0244-05 L78-0267-05	PHASE-COMPENS SMALL FIXED RESONATOR RESONATOR	SATION CO INDUCTOR(4MHz 4.19MH	NSATION COIL INDUCTOR(100UH,K) 4MHz 4.19MHz		
∢ ∪		16			N09-1236-05 N30-3008-46	TAPPING SCREW (PAN HEAD MACHIN	4 (3X16) HIN SCREW			

0.3	Re- marks
ž	Desti- nation 仕 向

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	Description	品 名/ 拠		ASSY.	CONNECTING WI	FOAMED SHEET	2.2	1,0-71:X	220PF 220PF 220PF 10UF 0.068UF	0.022UF 0.024UF 0.10UF 220UF 0.10UF	4700PF 10UF 1000PF 180PF 27PF	0.010UF 220PF 4.7UF 1.0UF 0.010UF	470PF 3.3UF 10UF 100UF	330UF 10UF 220UF 10UF 1000UF	0.010UF 100UF 0.010UF 470PF 470UF	0.010UF 10UF 47UF 100UF 470UF
		额	-A3	FRONT PANEL	OUTSIDE CON	CARTON CASE CARTON CASE POLYSTYRENE PROTECTION	LOUDSPEAKER	K,P,0-21	CERAMIC CERAMIC CERAMIC CERAMIC CERAMIC ELECTRO	MF MF ELECTRO	MYLAR ELECTRO CERAMIC CERAMIC CERAMIC	CERAMIC CERAMIC BLECTRO ELECTRO	CERAMIC NP-ELEC BLECTRO ELECTRO BLECTRO	ELECTRO ELECTRO ELECTRO ELECTRO ELECTRO	CERAMIC ELECTRO MYLAR CERAMIC ELECTRO	CERAMIC BLECTRO BLECTRO BLECTRO BLECTRO
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	Ref	*		,	,	1 1	SP1 SP2	1	01 01 05 07 09	C113 C13 C15 C19 C21	C25 C27 C29 C31 C33	C100 C102 C104 C106 C106	C1109 C1111 C1111 C1112	C115 C116 C117 C119 C120	C122 C124 C126 C126 C129	C131 C133 C135 C135 C137

 \triangle indicates safety critical components.

M:Other Areas

T:England X:Australia

Y:PX(Far East, Hawaii)
Y:AAFES(Europe)

A indicates safety critical components.

Parts without **Parts No.** are not supplied. Les articles non mentionnes dans le **Parts No.** ne sont pas fournis.

A indicates safety critical components.

E:Europe M:Other Areas

T:England X:Australia K:USA

Y:PX(Far East, Hawaii) Y:AAFES(Europe) L:Scandinavia

PARTS LIST

fournis. Telle ohne Parts No. werden nicht geliefert.

NO.6

	* New Parts
	Parts without Parts No. are not supplied.
0	Les articles non mentlonnes dans le Parts No. ne sont pas f
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Re- marks 蘇恭							** **					
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Description 晶名/規格	GULATOR/ +5V) SSOR> FILTER) STOR ARRAY)	GTON DRIVER) SSOR) SSOR) ET)		ISTOR	ISTOR		ISTOR	Møbul	100F 39PF 100PF 0.010UF 2200PF J	10UF 35WV 0.15UF J 0.47UF J 0.015UF J	0.470F 3 4.70F 35WV 100UF 10WV 0.010UF Z 10UF 35WV	2.2UF 50WV 1.0UF 50WV 470PF K 220PF J
De se	IC(OP AMP X2) IC(OLTGE REGULATOR/ +5V) IC(MICROPROCESSOR) IC(EQUALIZER FILTER) IC(7CH TRANSISTOR ARRAY)	ICCECH DARLINGTON DRIVER) ICCHICROPROCESSOR) ICCHICROPROCESSOR) ICCSYSTEM RESET) ICCOP ANP X2)	ICCOP AMP X2) TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR	TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR	UNIT (X13-7090-10)	ELECTRO CERAMIC CERAMIC MF	BLECTRO MF MF MF	MF ELECTRO CERAMIC ELECTRO	BLECTRO BLECTRO CERAMIC CERAMIC
Parts No. 略品報号	NJM4558D PC78054F M50940-345SP XR-1091ECP UPA80C	LB1294 CXP50112-3759 CXP50112-3889 PST620DDB NJM4565D-D	NJM4565D-D 2SC1740S(Q,R) 2SC2785(F,E) 2SC2878(B) 2SC2878(B)	2SC1740S(Q,R) 2SC2785(F,E) DTC124ES RN1203 2SA1175(F,E)	2SA933S(Q,R) 2SC1845(F,E) DTA124ES RN2203 2SD1266(Q,P)	2SB941(q,P) 2SA1175(F,E) 2SA933S(q,R) 2SB772(q,P) 2SC2003(L,K)	DTC144TFF 2SC2878(B) 2SA992(F,E)	ACCESSORY UI	CEO4LW1V100M CC45FSL1H390J CC45FSL1H101J CF92FV1H103J CF92FV1H222J	CEO4LW1V100M CF92FV1H154J CF92FV1H474J CF92FV1H153J CF92FV1H333J	CF92FV1H474J CE04LW1V4R7M CE04LW1A101M CK45FF1H103Z CE04LW1V100M	CE04LW1H2R2M CE04LW1H010M CK45FB1H471K CC45FSL1H221J
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	幸	SCREW	J 1/6W J 1/6W J 1/6W	J 2W J 2W J 1/4W M 1/2W	IN VOLUME IC MIXING	AGE SELECTOR BOARD							TUBE
Description	名/規	TAPTITE	4.7KX3 47KX4 100KX11 47KX4 4.7	4.7 560 100 22 2.2M	X2 MA	Y VØLT/ KEY							INDICATOR E-MPX) AMP)
ă	罐	NG HEAD	-000mp	PROOF RS	VR 100KB	MAGNETIC RELA SLIDE SWITCH PUSH SWITCH	DIODE	DIODE	DIODE DIODE DIODE DIODE	DIODE		DIODE	SCENT MPX/D OWER
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K:USA T:England X:Australia

E:Europe M:Other Areas P:Canada

PARTS LIST

A indicates safety critical components.

M:Other Areas E:Europe

K:USA

Y:PX(Far East, Hawaii) Y: AAFES (Europe)

A indicates safety critical components.

M:Other Areas E:Europe

X:Australia

Y:PX(Far East, Hawaii) Y:AAFES(Europe)

Parts without Parts No. are not supplied. Les articles non mentionnes dans le Parts No. ne sont pas fournis. Telle onne Parts No. werden nicht geliefert. NO.7

No. Address New Parts No. Description Descript	Re- marks 無处			00 00 00 ≪	44		············								_
## ## ## Parts No. Address New Parts No. Ball A	Desti- ation									7					_
## ## Barts No. Address New Barts No. Address New Barts & Barts No. CE04LWIC470H (CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CK45FBIHI02K CE04LWIC10H CK45FBIHI02K CE04LWIHR47M CE04LWIHRA7M CE04LWIHR47M CE	Description 晶 名/機	47UF 1 0.015UF J 100UF 1	MALL FIXED INDUCTOR(10MH, IAS OSCILATING COIL	RIMMING POT. (2.2K) HIGH SRIMMING POT. (1K) NORMAL SRIMMING POT. (1220) PLAY LEWHING POT. (100K) HOLS RIMMING POT. (2.2K) HOGH SRIMMING POT. (2.2K) HOGH SRIMMING POT.	RIMMING POT.(1K) NORMAL RIMMING POT.(220) PLAY	C RE		DINDE PRE STOR	RANSI RANSI RANSI RANSI	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	ANSISTOR DIK P. 0-21-M 0-71-X 2-71-T	0.470F 50WV 1.00F 50WV 2.20F 50WV 1.00F 50WV	2200PF 2 0.022UF 2 2200PF 3 0.1UF 5	LECTRO 2.2UF LECTRO 0.47UF LECTRO 2.2UF BRANIC 820PF LECTRO 10UF	
## # # # # # # # # # # # # # # # # # #	Parts No. 語 典	000-0	-1035-2 -0542-0	2-1617-0 2-1616-0 2-0605-0 2-5072-0 2-1617-0	2-1616-0 2-0605-0	51-2089-0	HSS104 1SS133 HSS104 1SS133 HZS11N(B2)	RD11ES(B2) HSS104 1SS133 TA8125S 2SC3246	SC1845(F,E) SA992(F,E) SA1175(F,E) SA933S(Q,R) SC1740S(Q,R)	SC2785(F,E) SC3246 SA1175(F,E) SA9335(Q,R) SC1740S(Q,R)	5(F,E) (X29-230	604LW1HR47M E04LW1H010M E04LW1HR47M E04LW1H2R2M	7.	E04LW E04LW K45FB1	
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Description	郎 昭 允 / 燕 祐	1.00F 50WV 0.22UF 0.47UF 50WV 0.091UF J	0.033UF J 0.068UF J 0.013UF J 5.000PF J	0.012UF J 2200PF J 4700PF K 820PF K 2.2UF 50WV	100F 35WV 100UF 10WV 470PF K	47 J 1/4W 47 J 1/4W	G SWITCH X 6) IP X4) M STEREG CIRCUIT) IP X2) RAPHIC EQUALIZER)	RAPHIC EQUALIZER) TRANSISTOR OR	990-10)	2.7UF 100WV	r TERMINAL (X25-4570-31)	SWITCH LOADING	R UNIT	560PF K 2.2UF 50WV 390PF K 0.022UF J 47UF 16WV	100UF 16WV 8200PF J 220PF J 470PF K 4.7UF 35WV	3300PF K 0.010UF Z 0.000PF K 0.022UF Z 220PF J	390PF K 2.2UF 50WV 390PF K	
		ELECTRO MF ELECTRO MF	E E E E E E	MF MF MF CERAMIC ELECTRO	ELECTRO ELECTRO CERAMIC	R0 R0	IC(ANALOG IC(OP AMP IC(PSEUDO IC(OP AMP	IC(7CH GRAPHIC DIGITAL TRANSI TRANSISTOR	SY (X21-5990	NP-ELEC	INPUT TE	LEVER SW	느	CERAMIC ELECTRO CERAMIC MF ELECTRO	ELECTRO MYLAR CERAMIC CERAMIC ELECTRO	CERAMIC CERAMIC CERAMIC CERAMIC CERAMIC	CERAMIC ELECTRO CERAMIC	
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Parts without **Parts No.** are not supplied. Les articles non mentionnes dans le **Parts No.** ne sont pas fournis. Teile ohne **Parts No.** werden nicht geliefert.

ZENER DIØDE ZENER DIØDE FLUORESCENT INDICATOR TUBE

HZS5.1N(B2) RD5.1ES(B2) FIP10BRM7

DIODE DIODE DIODE ZENER DIODE ZENER DIODE

1SS133 HSS104 1SS133 HZS3.3N(B2) RD3.3ES(B2)

D216-222 D217-222 D217-222 D301 D301

HHXXX BBGG

DIODE DIODE DIODE DIODE DIODE

HSS104 1SS133 HSS104 1SS133 HSS104

D204-212 D204-212 D213,214 D213,214 D216-222

ZENER DIØDE DIØDE DIØDE DIØDE

RD6.2ES(B2) HSS104 1SS133 HSS104 1SS133

D52 D53 -55 D53 -55 D204-211 D204-211

ZENER DIØDE ZENER DIØDE DIØDE DIØDE ZENER DIØDE

HZS4.7N(B) RD4.7ES(B) HSS104 1SS133 HZS6.2N(B2)

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indicates safety critical components.

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PARTS LIST

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ZENER ZENER ZENER ZENER ZENER

HZS3.9N(B2) RD3.9ES(B2) HZS6.2N(B2) RD6.2ES(B2) HZS7.5S(B2)

DIODE DIODE DIODE DIODE DIODE

ZENER ZENER ZENER ZENER ZENER

RD7.5JS(B2) HZS3ON(B) RD3OES(B) HZS2.7N(B2) RD2.7ES(B2)

NO.10

Desti- Re-nation marks 仕 向 編書

RESONATOR 10MHz CRYSTAL RESONATOR 4.194304MHz

品名/规

Description

Parts No.

Address New Parts

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Ref.

参照奉史

報品を

BINDING HEAD TAPTITE SCREW

N89-3008-45

28

L78-0294-05

1/6W 1/4W 2W 2W 2W

47KX5 100KX7 150 820 220

MULTI-COMP MULTI-COMP FL-PROOF RS FL-PROOF RS FL-PROOF RS

R90-0818-05 R90-0803-05 RS14KB3D151J RS14KB3D821J RS14KB3D221J

CP1 CP201 R117-120 R126 R306

KEY BOARD BEAT CANCELER

PUSH SWITCH SLIDE SWITCH

S40-1064-05 S31-2094-05

-23

DIODE DIODE DIODE DIODE DIODE

HSS104 1SS133 S5566B 1SR139-100 HSS104

-18 -18 -30 -34

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15S133 S5566B 15R139-100 RBV-402LFA RB7219

Parts without **Parts No.** are not supplied. Les articles non mentionnes dans le **Parts No.** ne sont pas fournis. Felle ohne Parts No. werden nicht geliefert. NO.9

* New Parts Parts without Parts No. are not supplied.

M:Other Areas E:Europe P:Canada T:England K:USA I:PX(Far East, Hawaii) L. Scandinavia

X:Australia

Y:AAFES(Europe)

indicates safety critical components.

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M:Other Areas E.Europe P.Canada T:England X:Australia Y:PX(Far East, Hawaii) Y:AAFES(Europe)

Ref. No.	Address		Parts No.		Description		Desti-	Re-
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C43 ,44 C45 ,46 C47 ,48 C51			CK45FF1H333Z CK95FF1H333J CK45FF1H333 CE04LW1V100M	CERAMIC NE CERAMIC ELECTRO	0.033UF 0.033UF 0.033UF 10UF 2.2UF	Z Z 354V 504V		
00000000000000000000000000000000000000			CEO4LW1H101M CEO4LW1H470M CEO4LW1H101M CEO4LW1V100M CEO4LW1H100M	ELECTRO BLECTRO BLECTRO BLECTRO BLECTRO	100UF 47UF 100UF 10UF	20MV 50MV 350MV 50MV		
C58 ,60 C61 C63 C64			CEO4LW1E221M CEO4LW1V101M CEO4LW1E102M CEO4LW1V101M CC45FSL1H101J	ELECTRO ELECTRO ELECTRO ELECTRO CERAMIC	220UF 100UF 1000UF 100UF 100PF	2588V 3588V 3588V 7		,
C666 C677 C688 C699			CEO4LW1H471M CEO4LW1E102M CEO4LW1E471M CEO4LW1V471M	BLECTRO BLECTRO BLECTRO BLECTRO BLECTRO	470UF 1000UF 470UF 470UF 10UF	5084V 2584V 3584V 3584V	KPXTE	
C70 C73 C73 .74 C75			CEO4LW1A101M CEO4LW1V.01M CF92FV1H104J CEO4LW1V101M CK45FF1H103Z	ELECTRO ELECTRO MF ELECTRO CERAMIC	100UF 100UF 0.10UF 100UF 0.010UF	10WV 35WV 35WV 2		
C77 C78 C79,80 C81			CK45FF1H223Z CEO4LW1A101M CK45FB1H102K CK45FB1H222K CEO4LW1H331M	CERAMIC CERAMIC CERAMIC CERAMIC	0.022UF 100UF 1000PF 2200PF 330UF	Z 104V K K 504V		
C83 C84 C85 C86 -88			CK45FF1H103Z CE04LW1H010H CC45FSL1H101J CE04LW1V100M CE04LW1C330M	CERAMIC ELECTRO CERAMIC ELECTRO	0.010UF 1.0UF 100PF 10UF 33UF	Z 50WV 35WV 16WV		
C90 -94 C91 -94 C95 C96 ,97 C98			CEO4LW1A101M CK45F81H222K CK45FF1H103Z CK45FF1H223Z CEO4LW1V100M	BLECTRO CERAMIC CERAMIC CERAMIC ELECTRO	100UF 2200PF 0.010UF 0.022UF 10UF	108V Z Z 358V		
C99 -101 C102 C202 C203 C204			C91-0753-05 CEO4LW1H220M CEO4LW1H2R2M C90-1827-05 CEO4LW0J221M	CHIP C BLECTRO BLECTRO BACKUP BLECTRO	470PF 22UF 2.2UF 0.047F 220UF	SOUNC SOUNC		
C205 C206 C207 C208 C209			CEO4LW1H010M CK45FF1H103Z CEO4LW1H010M CC45FCH1H270J CC45FCH1H330J	BLECTRO CERAMIC ELECTRO CERAMIC CERAMIC	1.0UF 0.010UF 1.0UF 27PF 33PF	50WV Z 50WV J		
C301 C302			CE04LW1H010M CE04LW1V100M	ELECTRO ELECTRO	1.0UF 10UF	50WV 35WV		
L1 ,2 L3 ,4			L79-0720-05	LC FILTER	TABLICTOR			

PARTS LIST

NO.12

tes articles non mentionnes dans le Parts No. ne sont pas fournis. felle ohne Parts No. werden nicht gellefert. Parts without Parts No. are not supplied.

Desti- Re-nation marks 在 向舊地 KP MXTE TE KPMX KPMX Ш 35WV K 50WV K 10WV X 50WV **20M**V 1641 16WV 16WV 50WV 16WV 李 27PF 22PF 47PF 47UF 0.022UF 1.00F 0.010UF 0.47UF 3900PF 0.010UF 47UF 0.022UF 0.022UF 0.047UF 0.022UF 0.010UF 100PF 33UF 0.033UF 1500PF Description 品 名/規 2200PF 220PF 33UF 1.0UF 33UF 0.10UF 4.7UF 0.10UF 47PF 3300PF 100PF 10PF 100PF 47PF 33UF ELECTRO CHIP C ELECTRO CHIP C BLECTRO BLECTRO BLECTRO CHIP C CHIP C CHIP C ELECTRO ELECTRO CHIP C ELECTRO CHIP C ELECTRO CHIP C CHIP C CHIP C ELECTRO ELECTRO CHIP C ELECTRO CHIP C CHIP C CHIP C ELECTRO CHIP C CHIP C CHIP C ELECTRO NP-ELEC CHIP C CHIP C CHIP C CHIP C CHIP CHIP CHIP CHIP CHIP CC73FSL1H100D CK73FB1H123K CK73FB1H822K CE04LW1H100M CK73FB1H103K CC73FSL1H330J CE04LW1V100M CE04LW1H010M CE04LW1HR33M CE04LW1HR33M фr CC73FCH1H270J CC73FCH1H220J CC73FSL1H471J CE04LW1C470M CK73FB1H223K 73FSL1H101J 73FSL1H100D 73FSL1H101J 73FSL1H470J CE04LW1V100M CK73FB1H473K CE04LW1H100M CK73FB1H103K 3FB1H103K 3FSL1H101J 4LW1C330M 3FB1H333K 3FB1H352K CE04LW1H010M CK73FB1H103K CE04LW1HR47M CK73FB1H392K CK73FB1H392K CB04LW1C470M CK73F81H223K CK73F81H223K CK73F81H473K CK73F81H473K 3FB1E104K 4LW1V4R7M 3FB1E104K 3FSL1H470J 3FB1H332K 3FB1H222K 3FB1H221K 4LW1C330M 4HW1H010M 4LW1C330M 3FB1H102K 3FB1H103K 3FB1E104K 3FB1E563K 3FB1H561K Parts No. 幸品 Address New Parts 参照奉号 ģ 52 22 -31 -38 Ref. C20 C21 C21 C23 C24 C25 C27 C28 C29 C29 C41 C45 C51 C53 C66

K7	0
CK73 CK73 CK73 CK73	C608 C609 C610 C611 C611
CK73 CE043 CK73 CK73	C603 C604 C605 C606 C607
CK77 CE004 CE044	C553 C554 C555,556 C600 C601,602
CK77	C502 C503 C550 C551 C551
0007 0007 0007 0007	C109 C110 C170 C171 C501
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	C101-104 C101,102 C105 C106-108

NO.11

No.	Address	New	Parts No.	Description	Desti-	Re-
吹	符	***	中 中 中	部 品 名/想 祐	白	編
		*	CXP82324-104Q HA12157NT TC4052BP XRU4052B BA10393	ICCMICROPROCESSOR) ICCOOLBY B/C NR) ICCACH MRX/DEFEMRX) ICCMULTPLEXER/DEMUTIPLEXER) ICCOUAL COMPALATOR)		
			NJM4565D RC4565D PST529D UPC7808HF TB62554S	IC(OP AMP X2) IC(OP AMP X2) IC(SYSTEM RESET) IC(VOUTAGE REGULATOR/ +8V) IC(4CH TRANSISTOR ARRAY)		
		*	CXP50116-380Q PST529D 2SD2061 2SA999(E,F) 2SC1740S(Q,R)	IC(MICROPROCESSOR) IC(SYSTEM RESET) TRANSISTOR TRANSISTOR TRANSISTOR		
0			2SC2785(F,E) 2SC3944A 2SA1175(F,E) 2SA933S(Q,R) 2SC1740S(Q,R)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR		
00			2SC2785(F,E) 2SC2878(B) DTA124ES RN2203 DTC124ES	TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR		
			RN1203 DTA1246S RN2203 DTC1246S RN1203	DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR		
			DTA124ES RN2203 DTC124ES RN1203 2SC3940A	DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR		
1 02 02			DTC124ES RN1203 2SC1740S(Q,R) 2SC2785(F,E) 2SA1175(F,E)	DIGITAL TRANSISTOR DIGITAL TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	Σ	
302			2SA933S(Q,R) 2SD1302(S,T) 2SA1175(F,E) 2SA933S(Q,R) 2SC1740S(Q,R)	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR	Σ	
- 6	PLAY	ш	2SC2785(F, E) R UNIT(X32-222(TRANSISTOR 0-10:K P. 0-21:M 0-71:X 2-71:T		
			CK73FB1H103K CE04LW1C470M CK73FB1H103K CK73FB1H473K CE04LW1H010M	0.0100F K 470F 16WV 0.0100F K 0.0470F K		
			CK73FB1H682K CK73FB1H103K	CHIP C 6800PF K CHIP C 0.010UF K		

indicates safety critical components.

M:Other Areas

indicates safety critical components.

P:Canada E:Europe

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht gellefert. Parts without Parts No. are not supplied.

PARTS LIST

IC(MOTOR DRIVER)
IC(POWER DRIVER)
IC(CD RF SERVO)
IC(SIGNAL PROCESSOR)
IC(D/A CONVERTER)

TA8409F BA6296FP CXA13729 CXD2500A9 TC9237BF

IC(FILTER)
TRANSISTOR
TRANSISTOR
TRANSISTOR
TRANSISTOR

TA2009F 2SC2413K 2SC2412K 2SA1037K 2SC2412K

IC58 Q1 Q4 Q5 Q7

DIODE IC(AM, FM TUNER) IC(MICROPROCESSOR) IC(SYSTEM RESET)

MA110 LA1851N LC7218 UPD75216AGF-663 PST529D

D512,513 IC1 IC2 IC51 IC51

DIODE

ZENER ZENER DIØDE ZENER I ZENER I

DTZS.1A MA8051-M MA110 DTZ3.9A MA8039

D505 D505 D506-510 D511 D511

TE KPMX TE

TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR

2SC3115(D27, D28 2SC2412K 2SC2412K 2SA1037K 2SA1037K

910 911 9101,102 9103 9103-105

TE

MECHANISM ASS'Y (X92-1650-31:K, P, 0-32:M, 0-33:X, 0-34:T, E)

CHASSIS SUB CHASSIS SUB CHASSIS ROD SLIDER

A10-2879-11 A11-0719-05 A11-0732-02 D10-2490-04 D10-3196-03

101 104 107 115 116

FM FRONT-END ASSY

W02-1041-15

00

ROD PULLEY GEAR INTERMEDIATE GEAR MOYOR GEAR GEAR

D10-3197-04 D13-0876-04 D13-0877-04 D13-0894-05 D13-0895-05

22A 28 18

117 118 119 120

GEAR MAIN GEAR

D13-0896-05

2B

122

TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR

2SC2412K 2SA954(L,K) DTA143TS DTA124ES

9106 9501 9502 9503

NO.14

Desti-Re-nation marks 任 向 審书

品名/規

粉

岩 田 華 哈

Address New Parts

参照番号

Ref. No.

Parts No.

Description

Ξ

DE-EMPHASYS

SLIDE SWITCH

531-2094-05

MA110 MA110 MA110 MA110 MA110

D1 ,2 D4 ,5 D6 ,7 D102-106 D501-504

OHW OHW

000

R92-0679-05 R92-0670-05 R92-0679-05

W25 ,26 W27 ,28 W501-521

Les articles non mentionnes dans le Parts No. ne sont pas fournis. Telle ohne Parts No. werden nicht geliefert.

Parts without Parts No. are not supplied.

* New Parts

NO.13

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht gellefert. Parts without Parts No. are not supplied.

Re-	Barks 編 編												
Desti-							TE	KPMX TE TE	TE KPMX TE		X X X	ETTY WE SEE SEE SEE SEE SEE SEE SEE SEE SEE	EXEXT XQ m
	堆	X 2 3 3 5 4 0 4 0 4 4 0	50WV K 6.3WV K	6.3WV J 35WV K K 16WV	00 X X X X 30 Y 34 Y	***	ANTENNA		(1.0MH,K)	.2MHz 9.000KHz 6.9344MHz .19MHz	TUNE LEVEL MPX SEPARA TE, FE GAIN		
Description	品 名/拠	0.033UF 10UF 0.033UF 22UF 0.010UF	0.470F 0.010UF 220UF 1500PF 0.047UF	220UF 39PF 10UF 0.010UF 33UF	0.470F 1000PF 1000PF 0.100F	1000PF 0.10UF 2200PF	NAL BOARD NAL BOARD	LTER	INDUCTOR INDUCTOR COIL COIL COIL	ESONATOR 7. 15 ESONATOR 16	0T.(22K) T 0T.(2.2K) V 0T.(22K) T 0 0HM	00 00 00 00 00 00 00 00 00 00 00 00 00	WHH00000000000000000000000000000000000
	节	CHIP C ELECTRO CHIP C NP-ELEC CHIP C	ELECTRO CHIP C ELECTRO CHIP C	ELECTRO CHIP C ELECTRO CHIP C ELECTRO	NP-ELEC CHIP C CHIP C CHIP C	CHIP C CHIP C CERAMIC	LOCK TERMI	CERAMIC FIL CERAMIC FIL FM IFT LC FILTER	SMALL FIXED SMALL FIXED COMBINATION COMBINATION	CRYSTAL RESENATOR CRYSTAL RESENATOR RESONATOR	TRIMMING POTENTIAMING POTENT REPORTED TO THE POTENT	CHIP R CHIP R CHIP R CHIP R	CHIP R CHIP R CHIP R CHIP R
Parts No.	14 中	CK73FB1H333K CE04LW1V100M CK73FB1H333K CE04HW1A220M CK73FB1H103K	CEO4LW1HR47M CK73FB1H103K CEO4LW0J221M CK73FB1H152K CK73FB1E473K	CEO4LW0J221M CC73FCH1H390J CEO4LW1V100M CK73FB1H103K CEO4LW1C330M	CEO4HW1HR47M CK73FB1H102K CK73FB1H102K CK73FB1E104K CK73FB1H103K	CK73FB1H102K CK73FB1E104K CK45FB1H222K	E20-0321-05 E70-0016-05	L72-0531-05 L72-0536-05 L30-0498-05 L79-0125-05 L30-0467-05	L40-1021-14 L40-1091-17 L39-1307-05 L39-0192-05 L39-1306-05	L77-1122-05 L78-0295-05 L77-1164-05 L78-0218-05	R12-3686-05 R12-1619-05 R12-3686-05 R92-0679-05 R92-0670-05	R92-0670-05 R92-0670-05 R92-0670-05 R92-0670-05 R92-0679-05	R92-0670-05 R92-0670-05 R92-0670-05 R92-0670-05 R92-0670-05
New	¥#						*		* *				
Address	台												
Ref. No.	专工事中	0613 0614 0615 0616 0617,618	C619 C620, 621 C622 C623 C623	C625, 627 C626, 627 C628, 629 C630 C631	0632,633 0634 0636,637 0638 0638	C640 C641 C642	J1 J1	CF1 ,2 CF1 ,2 L5 L5	L111 L101 L105 L105	X1 X2 X501 X502	VR1 ,2 VR3 ,52 VR51,52 W1 -4 W7 -15	W7 -8 W10 ,11 W16 ,17 W18	W20 W20 ,21 W22 W23

M:Other Areas P:Canada T:England

A indicates safety critical components.

KEUSA L:Scandinavia

X:Australia Y:PX(Far East, Hawaii) Y:AAFES(Europe)

indicates safety critical components.

M:Other Areas E:Europe

C.Australia F.England K:USA

f:PX(Far East, Hawaii) Y:AAFES(Europe) L:Scandinavia

113

A

m «

PARTS LIST

A indicates safety critical components.

M:Other Areas P:Canada

T:England K:USA

> Y:PX(Far East, Hawaii) Y:AAFES(Europe)

> > A indicates safety critical components.

M:Other Areas

P.Canada E:Europe

K:USA

T:England X:Australia

Y:PX(Far East, Hawaii) Y:AAFES(Europe)

L:Scandinavia

L:Scandinavia

∢ m

NO.16

Les articles non mentionnes dans le **Parts No.** ne sont pas fournis. Telle ohne **Parts No.** werden nicht geliefert.

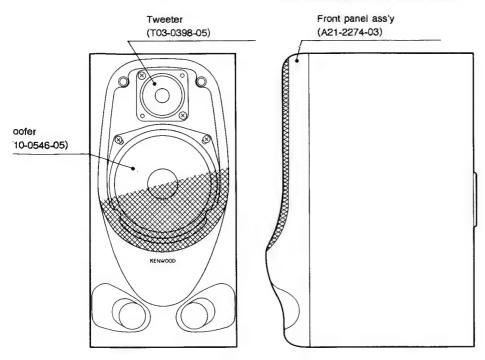
Parts without Parts No. are not supplied.

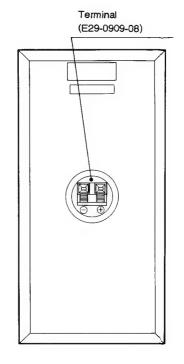
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Ref. No.	Address	S New Parts	Parts No.	Description	Desti-
参照権忠	位置	*	中華四年	郎 由 名/為 格	thation #
2440 2440 242	10 10 10	**	E35-0398-08 E40-4244-05 G01-3428-08 G01-3429-08 G01-3431-08	HEAD WIRE 3P PIN CONNECTOR RETURN GEAR SPRING REL SPRING CLUTCH ARM SPRING	
3443 3445 3446 347	10 18 20 20		G01-3432-08 G01-343-08 G01-3434-08 G01-3435-08 G01-3436-08	PLAY ARM SPRING SHIFT SELECT LEVER SPRING TRIGGER ARM SPRING SHIFT LEVER SPRING HEAD CHASSIS SPRING	
3350 3350 351	20 11C 2C 2C		G01-3437-08 G01-3438-08 G01-3439-08 G01-3440-08 G01-3441-08	PINCH ROLLER ARM SPRING RIGHT PINCH ROLLER ARM SPRING LEFT BRAKE ARM SPRING RIGHT INTER LOCK LEVER SPRING LEFT INTER LOCK LEVER SPRING LEFT	
3355 3355 355 355 355 355 355 355 355 3	10 20,20 10,20		G02-0913-08 G02-0994-08 G11-2117-08 G16-0780-08 G16-0786-08	PACK LOCK FLAT SPRING AZIMUTH SPRING HEAD WIRE CLAMPER REFLECT SEAL INSULATING SHEET	
358 361 362 363	2C 2C 1C,2D 2D		J21-5789-08 J31-0850-08 J31-0851-08 J42-0183-08 J90-0679-08	HEAD PLATE ASSY RETURN GEAR COLLAR EJECT LEVER COLLAR REEL CAP BUSHING TAPE GUIDE	
365 365 365 366 368	20,20 20,20 20,20		J90-0680-08 N09-2870-08 N09-2871-08 N09-2872-08 N09-2873-08	CASSETTE GIDE M2.6X5 SCREW M2.6 SCREW M1.7X8 SCREW M1.7X8	
369 370 372 373	20,20 20,20 10		NO9-2876-08 NO9-2877-08 NO9-2883-08 N19-1224-08 N19-1225-08	HEAD SCREW TAP TITE SCREW M2X4 AZIMUTH SCREW /4 1X6.5X0.25 FLAT WASHER /2.1X4.0X0.25	
374 375 376 377	10 25 20 20		N19-1285-08 N19-1286-08 N19-1287-08 N19-1288-08 N19-1289-08	FLAT WASHER /1 6X3.0X0.13 FLAT WASHER /2.3X4.0X0.25 FLAT WASHER /3.5X6.5X0.5 FLAT WASHER /1.6X5.0X0.5 FLAT WASHER /1.6X6.0X0.5	
379 380 381 385 386	10 28 28 16	* *	N35-2604-46 N09-2900-08 N09-2901-08 S74-0006-08 S74-0007-08	BINDING HEAD MACHINE SCREW SCREW SCREW MXX6 MXX6 MXX6 LEAF SWITCH (REC,METAL, Cr02) LEAF SWITCH (PACK DEFECT)	
388 390	2D 1E		T94-0226-08 W02-1130-08	SOLENGID ASSY ELECTRIC UNIT	
E1. 0.	1E 2D 2C		D16-0326-08 D14-0341-08 D14-0340-08	MAIN BELT PINCH ROLLER ASSY PINCH ROLLER ASSY	
MM PH RPEH PH1	1D 2C 2C 1E		T42-0599-08 T31-0066-08 T39-0020-08 NJL5165K	DC MOTOR ASSY PLAYBACK HEAD REC/PLAYBACK/ERASE HEAD PHOTO REFLECTOR	

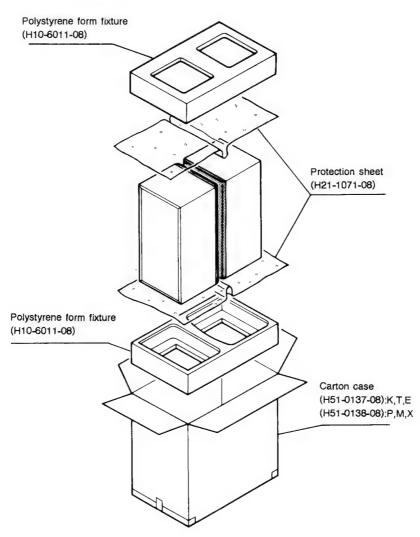
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s #p								m < m <					
Re- marks													
Destination tt 角						B							
Description 御品名/規格	PULLEY ASSY SYNCRO BELT RETAINER FLAT CABE 12P INSULATOR SPRING FRONT	INSULATOR SPRING REAR INSULATOR CLAMPER TRAY SET SCREW (3X8)	TAPTITE SCREW (2.6X8) MACHINE SCREW (2.6X8) BINDING HEAD MACHIN SCREW PAN HEAD MACHIN SCREW BINDING HEAD TAPTITE SCREW	LEVER SWITCH YOKE MAGNET POISK MOTOR ASSY FEED MOTOR	ADING MOTOR TICAL PICKUP HEAD	ASS,	HEAD CHASSIS CALKED ASSY BASE CHASSIS ASSY FLYWHEEL ASSY LEFT FLYWHEEL ASSY RIGHT SHIFT LEVER	PLAY SHIFT LEVER INTER LOCK LEVER INTER LOCK LEVER EJECT LEVER EJECT LEVER	FR ARM PLAY ARM SHIFT SELECT LEVER TRIGGER ARM SELECT ARM	BRAKE ARM CLUTCH GEAR REW GEAR FF GEAR REEL GEAR	PLAY CAM GEAR PLAY GEAR ASSY ROTATION GEAR RETURN GEAR REGL CAP	CLUTCH PULLEY ASSY HOUSING ASSY RIGHT HOUSING ASSY LEFT MOTOR WIRE 15P	S
Parts No. 數 品 書 母	D15-0309-04 D16-0301-05 D23-0265-03 E35-0296-05 G01-3409-04	G01-3410-04 J02-1057-15 J11-0164-03 J99-0507-11 N09-1522-05	NO9-2720-05 N09-2769-05 N35-2005-46 N39-2025-46 N89-2008-46	533-1022-05 T50-1054-04 T99-0503-15 A11-0733-05 T42-0532-05	-0531-05 -0014-05	MECMECHANISM	A10-2922-08 A11-0754-08 D01-0138-08 D01-0139-08 D10-3210-08	D10-3211-08 D10-3212-08 D10-3220-08 D10-3213-08 D10-3213-08	D10-3214-08 D10-3215-08 D10-3216-08 D10-3217-08 D10-3218-08	D10-3219-08 D13-0965-08 D13-0966-08 D13-0967-08 D13-0968-08	D13-0970-08 D13-0974-08 D13-0981-08 D13-0982-08 D19-0270-18	D19-0273-08 D23-0277-08 D23-0270-08 E35-0264-08	E35-0394-08
New Parts	*					ш				,		* *	
Address 位 画	3A 18 18	28 28 1A 2A 1A,2A	1A,3A 2A,2B 3A 2B 2B	28 28 28 28	3A 1B	ASSETT	2C 1D 1E, 2E 2D	250 10 10 10	10 10 20 20 20	16 10 10 10 10	2E 1C 2C 2D	1E 1C 1E	2E
Ref. No. 物器動物	124 126 127 129	131 133 135 136	137 138 139 140	142 147 1148 FM	LM PU	O	301 302 305 306 308	309 310 310 311	312 313 314 315 316	317 319 320 321	323 324 325 326 329	333 333 335 335 336	337

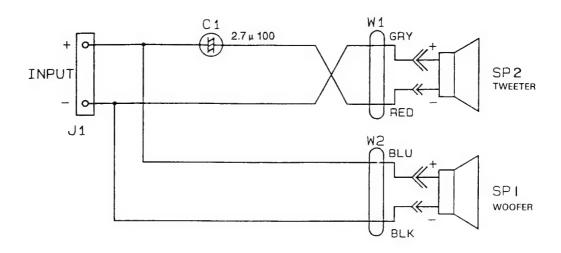
SPEAKER SYSTEM







SPEAKER SYSTEM



* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

Ref. No. 参照番号	Address 位 置	New Parts 新	Parts No. 部品番号	Description 部 品 名 / 規 格		Re- mark 備考
			L	S-A3		
-		*	A21-2274-03	FRONT PANEL ASSY		
-		*	E30-5120-08	OUTSIDE CONNECTING WIRE		
- -		* * * *	H51-0137-08 H51-0138-08 H10-6011-08 H21-1071-08	CARTOM CASE CARTON CASE POLYSTYRENE FOAMED FIXTURE PROTECTION SHEET	KTE	
SP1 SP2		*	T10-0546-05 T03-0398-05	LOUDSPEAKER(WOOFER)		
			NETWORK A	ASSY (X21-5990-10)		
C1 J1		*	C90-1098-05 E29-0909-08	NP-ELEC 2.7UF 100WV INPUT TERMINAL		

L:Scandinavia
Y:PX(Far East, Hawaii)

K:USA T:England P:Canada E:Europe

Y:AAFES(Europe)

X:Australia M:Other Areas

★ indicates safety critical components.

Specifications (For U.K. and Europe)

Amplifier/Graphic equalizer unit (A-A3)

Amplifier section
(IEC/NF) From 63 Hz to 12,500 Hz 0.7% T.H.D.
at 8 Ω 25 W + 25 W
(DIN) 1 kHz, at 8 Ω 25 W + 25 W
Total harmonic distortion at 1/2 rated power
(1 kHz, 8 Ω) 0.03%
Signal to noise ratio
AUX 86 dB (IHF' 66)
Input sensitivity/Impedance
AUX 200 mV/47 kΩ
Graphic equalizer section
Center frequency 60 Hz, 150 Hz, 400 Hz, 1 kHz,
2.4 kHz, 6 kHz, 15 kHz
Control range ±10 dB
GENERAL
Power consumption 85 W
Dimensions W: 270 mm
H: 165 mm
D: 276 mm
Weight (Net) 4.7 kg
Cassette deck/CD player/Tuner unit (X-A3L)
FM tuner section
Tuning frequency range 87.5 MHz ~ 108 MHz
Usable sensitivity (DIN at 75 Ω)
MONO 0.9 μV/10.2 dBf
STEREO 28 μV/40.2 dBf
Total harmonic distortion (DIN at 1 kHz)
MONO 0.3% (65.2 dBf input)
STEREO 0.8% (65.2 dBf input)
Signal to noise ratio (DIN weighted at 1 kHz)
MONO 69 dB (65.2 dBf input)
STEREO 62 dB (65.2 dBf input)
Stereo separation (DIN)
1 kHz
6.3 kHz
Selectivity (DIN ±300 kHz)
Frequency response (30 Hz ~ 15 kHz)
+0.5 dB, -3.5 dB

BANA/ Aumon goodiem
MW tuner section
Tuning frequency range 531 kHz ~ 1,602 kHz
Usable sensitivity 20 μ V (600 μ V/m)
Signal to noise ratio
(at 30% mod. 1 mV input) 47 dB
LW tuner section
Tuning frequency range 153 kHz ~ 279 kHz
Usable sensitivity
Signal to noise ratio
(at 30% mod. 1 mV input) 47 dB
CD player section
Refer to page 56.
Cassette Deck section
Refer to page 56.
GENERAL
Dimensions W: 270 mm
H: 165 mm
D: 250 mm
Weight (Net) 3.4 kg

SPECIFICATIONS

Specifications (For U.S.A. and Canada)

Amplifier	/Granhic	equalizer	unit	(A-A3)
	/ Gi abilic	Euualizei	um	ハーヘン

	section

Continuous rated power output

25 watts per channel minimum RMS, both channels driven, at 8 Ω 1 kHz with no more than 0.7% total harmonic distortion. (FTC)

Total harmonic distortion at 1/2 rate	ed power
(1 kHz, 8 Ω)	0.1%
Signal to noise ratio	
LINE (AUX)	86 dB (IHF' 66)
Input sensitivity/Impedance	
LINE (AUX)	200 mV/47 kΩ

Graphic equalizer section	
Center frequency 60 Hz,	150 Hz, 400 Hz, 1 kHz,
	2.4 kHz, 6 kHz, 15 kHz
Control range	±10 dB
GENERAL	
Power consumption	85 W
Dimensions	W: 270 mm (10-5/8")

Weight (Net) 4.7 kg (10.36 lb)

H: 165 mm (6-1/2") D: 276 mm (10-7/8")

Cassette deck/CD player/Tuner unit (X-A3)

FM tuner section	
Tuning frequency range	87.5 MHz ~ 108 MHz
Usable sensitivity (MONO at	t 75 Ω) 1.2 μ V/12.8 dBf
50 dB quieting sensitivity (at	t 75 Ω)
MONO	1.8 μV/16.2 dBf
STEREO	28 µV/40.2 dBf
Total harmonic distortion (a	t 1 kHz)
MONO	0.4% (65 dBf input)
STEREO	0.5% (65 dBf input)
Signal to noise ratio (at 1 kH	lz)
MONO	78 dB (65 dBf input)
STEREO	71 dB (65 dBf input)
	74 dB (85 dBf input)

Stereo separation	
1 kHz	40 dB
Selectivity (±400 kHz)	50 dB
Frequency response (30 Hz ~ 1	15 kHz)
	+0.5 dB, -3.5 dB
AM tuner section	
Tuning frequency range	
10 kHz step	530 kHz ~ 1.700 kHz
Usable sensitivity	· · · · · · · · · · · · · · · · · · ·
Signal to noise ratio	
(at 30% mod. 1 mV input)	48 dB
CD player section	
Refer to page 56.	
Cassette deck section	
Refer to page 56.	
GENERAL	
Dimensions	W: 270 mm (10-5/8")
	H: 165 mm (6-1/2")
	D: 250 mm (9-13/16")
Weight (Net)	3.4 kg (7.5 lb)

SPECIFICATIONS

Specifications (For other countries)

Signal to noise ratio

Amplifier/Graphic equalizer unit (A-A3)

Amplifie	r section
Continuo	us rated power output
driven, a	s per channel minimum RMS, both channels at 8 Ω 1 kHz with no more than 10% total c distortion. (EIAJ)
(IHF' 66)	From 60 Hz to 20 kHz, 1% T.H.D. at 8 Ω
Total har	monic distortion at 1/2 rated power
(1 kHz, 8	Ω) 0.1%

AUX	86 (IB (IF	11-	66)
Input sensitivity/impedance				
AUX	200	mV/	47	$\mathbf{k}\Omega$

Graphic equalizer section	
Center frequency 60	Hz, 150 Hz, 400 Hz, 1 kHz,
	2.4 kHz, 6 kHz, 15 kHz
Control range	±10 dB
GENERAL	
	OF 141
Power consumption	85 W
Dimensions	

Weight (Net) 4.7 kg

D: 276 mm

Cassette deck/CD player/Tuner unit (X-A3)

FM tuner section		
Tuning frequency range		
Usable sensitivity (MONO at 75	Ω) 1.2 $μ$ V/12.8 dBf	
50 dB quieting sensitivity (at 75	Ω)	
MONO	1.8 µV/16.2 dBf	
STEREO	28 μV/40.2 dBf	
Total harmonic distortion (at 1 l	(Hz)	
MONO	0.4% (65 dBf input)	
STEREO	0.5% (65 dBf input)	
Signal to noise ratio (at 1 kHz)		
MONO	78 dB (65 dBf input)	
STEREO	71 dB (65 dBf input)	
	74 dB (85 dBf input)	
Stereo separation		
1 kHz	40 dB	
Selectivity (±400 kHz)		
Frequency response (30 Hz ~ 15 kHz)		

AM tuner section	
Tuning frequency range	
9 kHz step	531 kHz ~ 1,602 kHz
10 kHz step	530 kHz ~ 1,610 kHz
Usable sensitivity	16 μV (500 μV/m)
Signal noise ratio	
(at 30% mod. 1 mV input)	48 dB
CD player section	
Refer to page 56.	
Cassette deck section	
Refer to page 56.	
GENERAL	
Dimensions	W: 270 mm
	H: 165 mm
	D: 250 mm
Weight (Net)	3.4 kg

SPECIFICATIONS

CD player section	
Laser	Semiconductor laser
Playing rotation	
Playing rotation	200 rpm ~ 500 rpm (CLV)
Audio	
Frequency response	20 Hz 20 kHz +1 5 dB
Signal to noise ratio	wore than 90 dB
Total harmonic distortion	

Wow & flutter	Unmeasurable Limit
Speakers (LS-A3)	
Enclosure	Bass-reflex type
Speaker configuration	
Impedance	
Maximum input level	
Output sound pressure	
Frequency response	
Dimensions	
	H: 330 mm (13 ")
	D: 235 mm (9-1/4 ")
Weight (Net)	

Cassette deck section	
Recording system AC bias	(Frequency: 105 kHz)
Heads	
A DECK Playback heads	
B DECK Playback/recording I	heads 1
Motor	
A DECK	
B DECK	
Fast winding time Approx. 11	0 seconds (C-60 tape)
Frequency response	•
Normal tape 30 I	Hz to 18,000 Hz, ±3 dB
CrO ₂ tape 30 I	
Signal to noise ratio	
Dolby C NR ON	72 dB
Dolby B NR ON	63 dB
Dolby NR OFF	53 dB
Wow & flutter	

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